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# High electron mobility of $\beta$ -HgS colloidal quantum dots with doubly occupied quantum states†

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Electron occupation of the lowest electronic state of the conduction band ( $1S_e$ ) of a semiconducting nanocrystal offers numerous opportunities to efficiently utilize the quantization of the colloidal quantum dot. The steady-state electron occupation of the  $1S_e$  gives rise to unprecedented electrical, optical, and magnetic properties. We report an electron mobility of  $\sim 1.29 \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}$  measured in a mercury sulfide ( $\beta$ -HgS) quantum dot field effect transistor (FET), demonstrating the best carrier mobility for the HgS colloidal nanocrystal solid. The high electron mobility of the HgS nanocrystals with the doubly occupied quantum state originates from the efficient ligand exchange from oleylamine to thiocyanate, better carrier hopping via shortened inter-dot-distance, and the packing of nanocrystals by optimized thermal annealing conditions.

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The size-tunable bandgap transition is a prominent feature of colloidal quantum dots (CQDs).<sup>1</sup> The tunable bandgap is attributed to the quantum confinement effect that is proportional to  $1/R^2$ . Not only the bandgap energy but also other transition energies such as the intraband transition energy rely on the confinement effect.<sup>2</sup> Due to the fast relaxation ( $10^{-15}$  to  $10^{-12}$  s) of the hot electrons and the Auger recombination process, the use of higher quantum state transitions has been regarded as challenging to utilize for practical applications.<sup>3,4</sup> However, recent advances in controlling the carrier density by tuning the stoichiometry (or surface dipole) of the nanocrystal enabled the use of higher quantum state transitions. For HgS CQDs, it was successfully achieved to place the lowest quantum state ( $1S_e$ ) in the conduction band (CB) below the Fermi level through chemical synthesis.<sup>5,6</sup> In this case, the  $1S_e$  state is presumed to be filled with two electrons in steady state, which is called the doubly occupied quantum state (DOQS) since two paired electrons reside in the  $1S_e$  electronic state. The appearance of the DOQS infers that the electron trap states, energetically locating between the CB and valence band (VB), are filled with the electrons, thereby the carrier mobility can be enhanced by removal of the majority carrier trap density. Since the

electrons occupy the  $1S_e$ , the carrier transport takes place mostly through the  $1P_e$  just as the  $1S_e$  serves as the main conduction channel for conventional CQDs such as PbS CQDs.<sup>7–9</sup> Many efforts have been made to increase the carrier mobility of CQD based TFT devices.<sup>10</sup> For example, it was reported that an additional treatment of metal ions into the nanocrystal surface, or filling the vacancy between nanocrystals film significantly enhances the carrier mobility.<sup>11</sup> Furthermore, the removal of the dangling bonds which behave as charge traps by efficient passivation substantially increases the carrier mobility as well. However, the carrier mobility measurement of the air stable n-type HgS CQDs achieved by controlling the stoichiometry of nanocrystal instead of impurity doping has not been carefully performed yet although it would provide one with another option.

Here, we present the air-stable n-type HgS CQD thin film transistor (TFT) with a high electron mobility of  $1.29 \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}$ . The efficient ligand exchange from long organic amine ligands to short inorganic thiocyanate (SCN) ligands enables the high electron mobility with two orders of magnitude higher than the well-known II–VI CQD TFT device such as PbS CQD TFT under the same device and ligand conditions.<sup>12</sup> Furthermore, the thermal annealing effects on the device characteristics were thoroughly examined by imaging and performing optical and electrical measurements to figure out how the annealing process affects the HgS CQD film and the TFT device, which can provide useful information for further optoelectronic applications.

The HgS CQDs were synthesized by following the procedure previously reported.<sup>7</sup> Bis(trimethylsilyl)sulfide in octadecene solution, a chalcogenide precursor, was quickly injected into the mercury precursor,  $\text{Hg}(\text{NHCH}_2(\text{CH}_2)_7\text{CH}=\text{CH}(\text{CH}_2)_7\text{CH}_3)_2$ ,

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that was prepared by reacting  $\text{HgCl}_2$  (1 eq.) and excess oleylamine (40 eq.), under argon during vigorous stirring at  $90^\circ\text{C}$ . The reaction time was controlled by an addition of 50% v/v of the oleylamine chloroform quenching solution. The final product is the HgS CQD passivated with oleylamine in nonpolar solvent. The amine ligand chosen here helps to suppress the hole trapping due to its larger electronegativity (E.N. = 3.0) than that of sulfur (E.N. = 2.5), the chemical composition of the HgS nanocrystal. The conventional thiol ligand molecules, that is optimum to the HgTe CQD synthesis, was not used in this work since the thiols lead to the generation of the hole trap for the HgS CQD synthesis. The details of HgS CQD synthesis are described in the ESI (Fig. S1†).

Fig. 1A shows the steady-state intraband ( $1S_e-1P_e$ ) absorption spectrum of HgS CQD in tetrachloroethylene. The half width at half maximum (HWHM) of the HgS CQD is ca.  $290\text{ cm}^{-1}$ . The narrow vibrational absorption peaks correspond to symmetric ( $2920\text{ cm}^{-1}$ ) and asymmetric ( $2890\text{ cm}^{-1}$ ) C-H stretch modes of oleylamine, the ligand of the nanocrystal. The shoulder shown at ca.  $3200\text{ cm}^{-1}$  is attributed to the N-H stretch mode of the surface-bound oleylamine ligands.

Electron paramagnetic resonance (EPR) spectrum, providing direct information about the magnetic property for the HgS CQD, was obtained (Fig. 1B). In combination with the optical intraband transition in Fig. 1A, the featureless EPR spectrum corresponding to diamagnetism demonstrates the pairing of two electrons in the  $1S_e$  state. The featureless spectrum can be understood by referring to the previously reported result.<sup>8</sup> Jeong *et al.* demonstrated that the  $1S_e$  state of the HgSe CQD showing the intraband transition is filled with two paired electrons. Therefore, the featureless spectrum obtained here also indicates the DOQS in which the paired electrons occupy the  $1S_e$  state. NMR spectroscopy proves the existence of oleylamine, the ligand of the HgS CQD, and corroborates the absence of organic by-product. Furthermore, the mercury-rich HgS CQD is identified by the X-ray photoelectron spectroscopy result tabulated in Table 1.

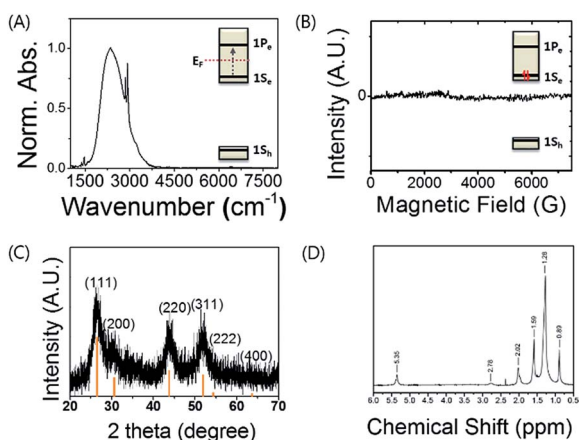


Fig. 1 (A) Mid-IR intraband absorption spectrum, (B) EPR spectrum of HgS CQD solution, (C) XRD pattern of HgS CQD film and (D)  $^1\text{H}$ -NMR spectrum of oleylamine bound to the surface of HgS CQDs.

Table 1 Composition analysis of Hg-rich HgS CQD using the X-ray photoelectron spectroscopy

Element Atomic%	Hg 4f	S 2p	C 1s	N 1s	O 1s	Cl 2p
	6.0	3.6	74.1	8.0	5.6	2.2

Fig. 2A illustrates the schematic structure of HgS CQD TFT with source/drain metallization and its corresponding optical microscopy (OM) image. A heavily-doped Si wafer with 200 nm thermal  $\text{SiO}_2$  was used both as a substrate and as a gate electrode for the TFT fabrication.<sup>14</sup> The Al electrode was thermally evaporated for the top-contacted transistor geometry using a metal shadow mask, forming a channel length and width of 50  $\mu\text{m}$  and 1000  $\mu\text{m}$ , respectively, as shown at the top right of Fig. 2A. These devices were fabricated shortly after the thermal annealing of HgS CQD film.

It was reported that the thermal treatment of CQD films gives rise to the removal of residual solvent, and therefore further reduces the inter-particle distance.<sup>15,16</sup> Thus, the thermal annealing step should be investigated so that the electrical characteristics of the heavily doped HgS CQD film-based devices can be clearly understood and optimized under the configuration of TFT. Fig. 2B shows the series of transfer curves of HgS CQD TFTs annealed at various temperature from 65 to  $150^\circ\text{C}$  and of the pristine. The pristine device (no thermal annealing) exhibited a noticeable current modulation as large as about 10 as a function of gate voltage ( $V_{\text{GS}}$ ) sweep, inferring the effective charge transport after the ligand exchange process. Further increase of the annealing temperature continues to affect the device performance in terms of current on/off ratio ( $I_{\text{on}}/I_{\text{off}}$ ) and the on-state current ( $I_{\text{on}}$ ). For example, HgS CQD TFT annealed at  $65^\circ\text{C}$  showed about  $10^2 I_{\text{on}}/I_{\text{off}}$  ratio while  $I_{\text{on}}$  was kept almost constant. It appears that a gradual increase of the annealing temperature almost monotonically increases  $I_{\text{on}}$  up to  $150^\circ\text{C}$ . However, the  $I_{\text{on}}/I_{\text{off}}$  ratio, as low as about 5, of HgS CQD TFT

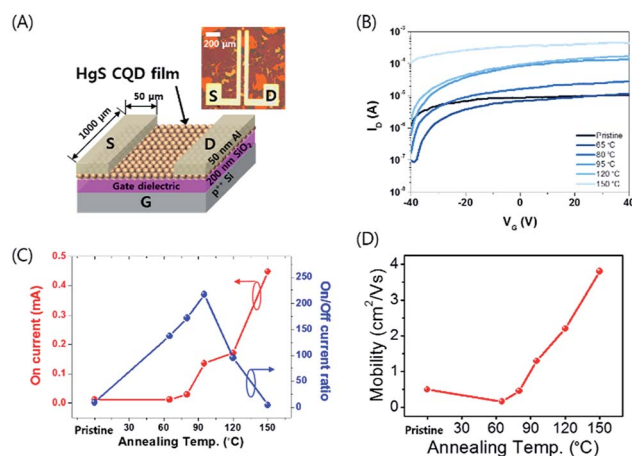


Fig. 2 (A) Schematic illustration of HgS CQD TFT device structure and optical image (B) transfer characteristics of HgS CQD TFT annealed at different temperatures from 65 to  $150^\circ\text{C}$ . (C) comparison of on-state current and current on/off ratios and (D) linear mobility of HgS CQD TFT devices as a function of annealing temperatures.



severely worsened following 150 °C annealing. This negligible current modulation is probably attributed to the quantum confinement loss by the nanocrystal sintering of HgS CQDs.

Fig. 2C summarizes the correlation of  $I_{\text{on}}$  and the  $I_{\text{on}}/I_{\text{off}}$  ratio as a function of annealing temperature, clearly exhibiting the change of electrical property of HgS CQD films. In our study, 95 °C annealing renders the best  $I_{\text{on}}/I_{\text{off}}$  ratio although 150 °C annealing produces the highest  $I_{\text{on}}$  value. It is reasonable that the gradual increase of the  $I_{\text{on}}/I_{\text{off}}$  ratio up to 95 °C results from the efficient packing of individual nanocrystal. In contrast, the attenuation of the  $I_{\text{on}}/I_{\text{off}}$  ratio at above 120 °C is involved with the deterioration of the intraband conduction, based on the XRD spectra change (Fig. 3A). Thus, the annealing at 95 °C seems to be the best condition for thermal treatment for HgS CQD TFTs. This trend also suggests that 95 °C annealing effectively assists the networking of semiconducting CQDs with SCN ligands for efficient charge transport.

The field-effect mobility of HgS CQD TFTs as a function of annealing temperature is shown in Fig. 2D. The carrier mobility ( $\mu_{\text{lin}}$ ) from the linear regime of these devices is used to compare the electrical characteristics of HgS TFTs annealed at the different temperature.

The thermal annealing not only affects the device performance and but also the size of nanocrystal and the intensity of the intraband transition (Fig. 3). X-ray diffraction spectrum exhibits four distinct peaks at (111), (200), (220), and (331), corresponding to the cubic structure of  $\beta$ -HgS CQD in Fig. 3A. Due to the small size of the nanocrystal, the peak width is relatively broad compared to that of bulk HgS. The intensity of the HgS (111) facet indicates that the HgS CQD sample is non-stoichiometrically metal rich, leading to the heavy doping of nanocrystal.<sup>8,13</sup> In Fig. 3B, the intraband peak gradually shifts to lower wavenumber under annealing process due to the coupling between wavefunctions of the nearby electrons. It was found that the intensity of the intraband transition peak substantially drops by an order of magnitude with increasing temperature. The significant attenuation of intraband peak intensity is associated with the nanocrystal sintering as well, as shown in the XRD spectra in Fig. 3A, and the field emission scanning electron microscopy (FESEM) in Fig. 4. The spectroscopic investigation of the steady-state intraband transition using FT-IR seems to be qualitatively consistent with the current modulation of TFTs in Fig. 2C. Therefore, it can be inferred that

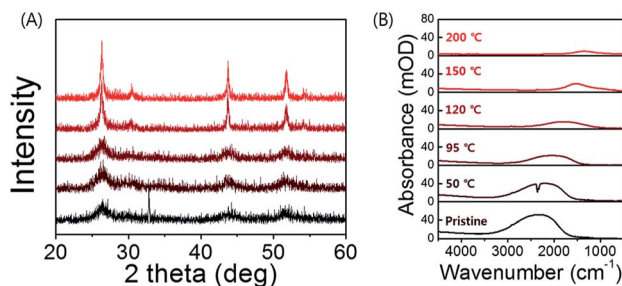


Fig. 3 (A) XRD spectra of HgS CQD at corresponding temperature. (B) IR spectra of HgS CQD films after annealing at different temperature.

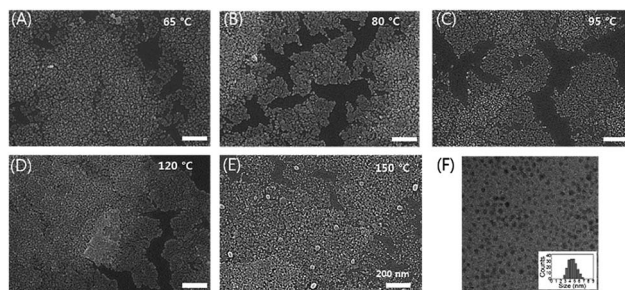


Fig. 4 FESEM images of HgS CQD films after thermal annealing at (A) 65 °C, (B) 80 °C, (C) 95 °C, (D) 120 °C, (E) 150 °C (F) TEM image of HgS CQDs (scale bar = 10 nm) and the size histogram ( $4.4 \pm 0.6$  nm, inset).

excessive thermal annealing at 150 °C tends to eliminate the quantum confinement characteristics of the intraband transition for the HgS CQDs. Thus, the spectroscopic observation of intraband peaks of HgS CQDs films annealed at the different temperature is correlated with their electrical properties measured and could serve as a reference for predicting the optimized annealing condition.

Similarly, the behaviour of field-effect mobility seems to be significantly influenced by the annealing temperature as well, exhibiting a pseudo-linear increase of  $\mu_{\text{lin}}$ .  $\mu_{\text{lin}}$  of HgS TFTs annealed at 0, 65, 80, 95, 120, and 150 °C were measured as high as 0.49, 0.15, 0.45, 1.29, 2.20, and  $3.80 \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}$ , respectively. Relative high field-effect mobility might be associated with the increased electrons per dots in the doubly occupied quantum states as well as the effective inorganic ligand exchange by a thiocyanate. The increased carrier concentration of disordered semiconductor thin films such as CQDs and amorphous oxides alleviates the carrier trapping by preferentially filling the localized trap states at the lower level of energy states, consequently leading to higher field-effect mobility. In addition, the induced charge carrier concentrations at the interface of the CQD film and  $\text{SiO}_2$  dielectric layer of HgS TFTs at  $V_{\text{GS}} = 40 \text{ V}$  can be estimated by the following eqn (1)<sup>17</sup>

$$n_{\text{ind.}} = \frac{C_{\text{ox}}}{e} \left( V_{\text{GS}} - \frac{V_{\text{DS}}}{2} - V_{\text{Th}} \right), \quad (1)$$

where  $C_{\text{ox}}$ ,  $e$ ,  $V_{\text{Th}}$  are the areal capacitance of  $\text{SiO}_2$  dielectric layer, the elementary charge and the threshold voltage, respectively. Assuming most of the induced charges are formed only at the first monolayer of CQD films, the number of charges are 2.9, 1.9, 1.7, 1.5, 1.6, and 1.5 per nanocrystal for 4.4 nm CQD-based films with different annealing temperatures of pristine, 65, 80, 95, 120, and 150 °C, respectively. These induced charges arise by the modulation of uniformly-distributed electrons in the quantum dots by the positive gate bias.

To note, the annealing of HgS CQD film at 150 °C substantially degrades the device performance of the transistor which almost lost the on/off switching characteristics. Therefore, the annealing at 95 °C corresponds to the optimized annealing temperature in terms of TFT operation of HgS CQD films. Since the synthesis was carried out at 95–100 °C, it is reasonable to expect the thermal annealing above the temperature to possibly





trigger the nanocrystal to sinter. Consequently, the transport characteristics of HgS QD TFT annealed at 150 °C, which is the highest annealing temperature performed in this study, produces the minimal current modulation during the  $V_{GS}$  sweeping. The gradual change of the electrical property is probably related to the onset of quantum dot clustering within HgS semiconducting nanocrystal films.

The high mobility of HgS QD solid may arise from the intrinsic property of the nonstoichiometric metal-rich HgS QD with large HgS (111) facet where metal ions are prone to be bonded.<sup>8,18,19</sup> The excess metal element of HgS can offer more conducting channels, leading to high mobility based on the eqn (2)

$$G = \frac{1}{R} = \frac{e^2}{h} \sum_{ij} T_{ij} \quad (2)$$

where  $G$ ,  $R$ ,  $e$ ,  $h$ ,  $T$  are the conductance, resistance, elementary charge, Planck constant and the transmission of the channel between state  $i$  and  $j$  of two different nanocrystals, respectively. The hopping lifetime of 1.3 ps is estimated by the eqn (3):

$$\tau_{\text{hop}} = \frac{ed^2}{6\mu kT} \quad (3)$$

where  $\tau_{\text{hop}}$ ,  $d$ ,  $k$ , and  $T$  are the hopping lifetime, center-to-center distance of the SCN cross-linked HgS QDs, Boltzmann constant, and temperature, respectively.<sup>20,21</sup> In addition, the degeneracy of the  $1P_e$  state could be one reason for the high field-effect mobility. In contrast to the  $1S_e$  state, the main conduction channel of conventional QDs, the  $1P_e$  state comprises of three degenerated electronic states serving as the intraband conduction channels, which is also experimentally identified by Jeong *et al.* via the electrochemical conductivity measurement.<sup>5,6</sup>

To delve into the change of morphology of the HgS QDs after thermal annealing, field emission scanning electron microscopy (FESEM) was employed as shown in Fig. 4A–E. Note that the cracks were also observed due to the thermal annealing process. It could cause the performance variation based on these films. So, we carried out the multiple coating of HgS QDs for alleviated performance deviation. The HgS QD films, fabricated with HgS QDs (Fig. 4F), annealed below 120 °C seemed to have the almost negligible difference in morphology. At 150 °C, however, it was found that noticeable agglomeration of HgS QDs already began. These tens of nm-sized agglomerated HgS nanocrystals should be attributed to the sintering of nanocrystals by the excessive thermal budget. Considering the relation between the morphological variance and the change of the electrical property of HgS QD films, the lowest current modulation of the devices was probably caused by these aggregates induced by the sintering effect. These fused QDs would behave as the semi-metallic islands, and consequently attenuate the  $I_{\text{on}}/I_{\text{off}}$  ratio of QD-TFT. Although this sintering effect might appear beneficial to the increase of the on-current of the HgS QD device, concurrent degradation of current on/off ratio makes it undesirable to be implemented for various application such as sensors. As discussed, the thermal

annealing of QD films after the ligand exchange is proved to enhance the device performance by further reducing the gap between QDs, while excessive thermal budget rather leads to the deterioration.

## Conclusions

In conclusion, the electron mobility of the air-stable n-type HgS QD TFT was first examined at various annealing temperatures. The mobility measured is as high as  $1.29 \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}$  with *ca.*  $10^2$  current on/off ratio after annealing at the optimal temperature of 95 °C. This relatively high field-effect mobility might arise from the increased electrons per dots in doubly occupied quantum state (DOQS) of HgS QD film as well as the effective ligand exchange and optimal thermal annealing. Annealing the QD film enhances the carrier transport due to the efficient packing the nanocrystal film and resultant reduced inter-dot distance. It was also observed that excessive thermal budget gives rise to the phase transition from the n-type semiconducting nanocrystal with the intraband quantum confinement to bulk-nanocrystal alloys. These HgS TFTs with effective current control by a gate bias can represent a unique electronic building block with intraband optical properties. Therefore, the air-stable n-doped colloidal HgS QD TFT with high field-effect mobility under optimized annealing condition will be a promising pathway to the fabrication of low-cost and low-energy-consumed QD based electronics and optoelectronics.

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