



Cite this: *RSC Adv.*, 2019, 9, 17291

Atomic layer deposition and tellurization of Ge–Sb film for phase-change memory applications

Yewon Kim,^a Byeol Han,^a Yu-Jin Kim,^a Jeeyoon Shin,^a Seongyoon Kim,^a Romel Hidayat,^a Jae-Min Park,^a Wonyong Koh^b and Won-Jun Lee^{a*}

We studied the atomic layer deposition (ALD) and the tellurization of Ge–Sb films to prepare conformal crystalline Ge–Sb–Te (GST) films and to achieve void-free gap filling for emerging phase-change memory applications. ALD Ge–Sb film was prepared by alternating exposures to GeCl₂-dioxane and Sb(SiEt₃)₃ precursors at 100 °C. The growth rate was 0.021 nm per cycle, and the composition ratio of Ge to Sb was approximately 2.2. We annealed the ALD Ge–Sb films with a pulsed feeding of di(*tert*-butyl) tellurium. The ALD Ge–Sb films turned into GST films by the tellurization annealing. When the tellurization temperature was raised to 190 °C or higher temperatures, the Raman peaks corresponding to Ge–Sb bond and amorphous Ge–Ge bond disappeared. The Raman peaks corresponding to Ge–Te and Sb–Te bonds were evolved at 200 °C or higher temperatures, resulting in the phase transition temperature of 123 °C. At 230 °C or higher temperatures, the entire film was fully tellurized to form a GST film having a relatively uniform composition of Ge₂Sb₂Te₅, and the carbon impurities in the as-deposited ALD Ge–Sb film were eliminated. As the tellurization temperature increases, the volume of the ALD film is expanded owing to the incorporation of tellurium, resulting in complete filling of a trench pattern by GST film after the tellurization at 230 °C.

Received 21st March 2019
 Accepted 27th May 2019

DOI: 10.1039/c9ra02188d

rsc.li/rsc-advances

Introduction

Since the release of commercial phase change random access memory (PCRAM),¹ the interest in PCRAM as a nonvolatile memory device has been increasing. In particular, PCRAM is getting attention as a server's storage class memory because of lower latency and higher endurance compared with NAND flash memory. It is also possible to integrate with higher memory densities than dynamic random-access memory (DRAM) devices.^{2–4} The confined structure can significantly reduce the thermal interference between the memory cells compared to the conventional mushroom structure,⁵ thereby significantly increasing the memory density. Alternatively, a three-dimensional (3D) vertical PCRAM structure similar to a 3D vertical NAND structure was also proposed.⁶ Among the phase change materials, Ge–Sb–Te (GST) compounds were widely studied because of their high crystalline temperature and low melting point, resulting in fast switching speed, low operation energy, and high data retention.^{7–9} Physical vapor deposition (PVD) has been studied as the method of manufacturing GST.^{10–12} However, it is hard for the conventional PVD techniques to gapfill the confined structure with a phase change material or to prepare a conformal phase change material film

on a 3D vertical PCRAM structure. Besides, the conventional sputtering processes using GST targets result in Te-deficient GST films because of the higher volatility of Te element than those of others.^{13,14} Atomic layer deposition (ALD) of the phase change material is the ideal solution and has been studied for the last decade.^{15,16}

Since the introduction of tellurium silyl compounds, ALD studies using chloride precursors and Te silyl precursors have been accelerated.¹⁷ The ALD process of the compound thin film having the composition of Ge–Sb–Te ternary system is complicated. Many famous compositions, such as Ge₂Sb₂Te₅, Ge₁Sb₂Te₄, and Ge₁Sb₄Te₇, are located on the pseudo-binary line between GeTe and Sb₂Te₃. The supercycles of ALD GeTe and ALD Sb₂Te₃ were reported to prepare Ge₂Sb₂Te₅ film, and ALD GeTe and ALD Sb₂Te₃ were possible by using a Te silyl precursor with GeCl₂-dioxane complex or SbCl₃.^{17,18} However, the composition ratio of Ge to Sb was sensitive to Ge precursor pulse time as well as the ratio of GeTe subcycle.¹⁷ Also, the ALD GST films prepared by supercycle were tellurium-deficient and contained oxygen impurity. Due to the oxygen impurity, the crystallization temperature of the film was higher than that of the PVD Ge₂Sb₂Te₅ film.¹⁹ Chlorine-free ALD chemistry was reported using Ge(OMe)₄, Sb(OEt)₃, Te(SiMe₃)₂, and Sb(SiMe₃)₃.²⁰ Since the oxidation state of Ge in Ge(OMe)₄ is +4, GeTe₂ was obtained by alternating exposures to Ge(OMe)₄ and Te(SiMe₃)₂. The Ge₂Sb₂Te₅ film was prepared by supercycles of ALD GeTe₂, Sb₂Te₃, and Sb. Another chlorine-free ALD chemistry was

^aDepartments of Nanotechnology and Advanced Materials Engineering, Sejong University, Seoul 05006, Republic of Korea. E-mail: wjlee@sejong.ac.kr

^bUP Chemical Co., Ltd., Pyeongtaek-Si, Gyeonggi-do, 17749, Republic of Korea



reported using amide compounds of Ge and Sb.²¹ Tellurium-deficient film was deposited due to the thermal decomposition of the Ge and Sb precursors. Ge₂Sb₂Te₅ film was prepared by supercycles of ALD Ge–Te, Sb–Te, and Te. Tellurium silyl precursor enables low temperature deposition of GeTe, Sb₂Te₃, Sb and Te. However, the film grows as a low-density amorphous film because the deposition temperature is low.¹⁷ This causes volume shrinkage and void formation due to crystallization during PCRAM use.²² When depositing at higher temperatures using a tellurium alkyl precursor, a crystalline thin film is deposited with a rough surface and the seam inside trenches.²³

The way of forming a fully-tellurized crystalline GST film is the tellurization annealing of a Ge–Sb film. Tellurization processes have been reported for CdTe photovoltaic,^{24,25} and Bi₂Te₃ thermoelectric materials²⁶ to make the stoichiometric telluride films. The metallic films were heat-treated at temperatures of 400 °C or higher in tellurium atmosphere that is formed by sublimation of tellurium powders. The tellurization process of a phase change material has also been reported.²⁷ The GeSb thin film was deposited by chemical vapor deposition (CVD) method and then annealed at temperatures between 230 and 300 °C using di(*tert*-butyl)tellurium [(^tBu)₂Te] or di(isopropyl)tellurium [(ⁱPr)₂Te₂] as the tellurium source gas. However, the details of process condition and the changes in film properties during the tellurization process were not adequately described.

In the present work, we propose the tellurization of an ALD Ge–Sb thin film to achieve void-free gap filling of nanoscaled structures with crystalline GST film for emerging PCRAM applications. This process also can produce thin conformal crystalline GST films over high-aspect ratio patterns. We investigated the deposition characteristics of ALD Ge–Sb using GeCl₂-dioxane and Sb(SiEt₃)₃ precursors at 100 °C. It has been reported that the ALD process of Ge–Sb films is possible, but details were not disclosed.²⁸ We annealed the ALD Ge–Sb films with a pulsed feeding of the tellurium precursor and investigated the composition profile and crystallization temperature of the tellurized film with varying the annealing temperature. In the previous work, we studied the tellurization of sputtered Ge–Sb film.²⁹ Crystalline Ge–Sb–Te films were prepared by adjusting annealing temperature or time. When the ALD Ge–Sb film is tellurized at a higher temperature, the volume is expanded owing to the incorporation of tellurium while being crystallized. We selected ^tBu₂Te as the tellurium source for tellurization annealing because it has high vapor pressure than metallic tellurium and is thermally stable at below 300 °C.^{30,31} Gap filling of a trench pattern by the ALD and the tellurization was also investigated and discussed.

Experimental

GeCl₂-dioxane complex was used as the Ge precursor and Sb(SiEt₃)₃ was used as the Sb precursor. We used DFT calculations to check the feasibility of the ALD reaction using Dmol³ package of Material studio 7.0 (Accelrys, USA). The generalized gradient approximation (GGA) with the Perdew–Burke–Ernzerhof (PBE)³² were used as exchange–correlation functional and

the double numerical polarization (DNP)³³ as the basis set. We performed the geometry optimization for all structures of precursors and ALD reaction products and the determined the total energies of the system before and after the ALD reaction.^{34,35}

GeCl₂-dioxane, Sb(SiEt₃)₃, and ^tBu₂Te were synthesized and purified to 99% or higher. A showerhead-type ALD reactor (iOVD100, ISAC Research, Republic of Korea) was used for the deposition and the tellurization of Ge–Sb films. The containers of GeCl₂-dioxane and tris (triethylsilyl)antimony [Sb(SiEt₃)₃] were maintained at 70 °C and 80 °C. The deposition temperature should be higher than 90 °C because of the low vapor pressure of Ge and Sb precursors. The temperature of the precursor delivery line was maintained higher than containers temperature and lower than the deposition temperature to prevent condensation of the precursors. 30 sccm of argon gas bubbled through each of the precursors and an additional 170 sccm of argon was also supplied during precursor pulse to keep the pressure constant at 1 ± 0.05 torr during the whole process. 200 sccm of Argon gas for 15 s was used as purge gas between each pulse of precursors. The growth of the film was *in situ* monitored by quartz crystal microbalance (QCM) technique. QCM equipment was composed of quartz crystal sensor (TS061G, Taewon Scientific Co., Ltd, Republic of Korea), feed-through (CDS-A0F47, Inficon, USA), oscillator and control box (SQM-160, Inficon, USA). Details of the QCM measurements are reported elsewhere.^{34–36} After investigating the ALD kinetics using QCM, the ALD films were deposited on TiN (50 nm)/Ti (10 nm)/Si wafers to analyze the composition and crystal structure of the ALD Ge–Sb film before and after the tellurization annealing. The ALD and the tellurization annealing of Ge–Sb films were performed in the same reactor without air exposure. The tellurization annealing was performed by repeating ^tBu₂Te supply and purging at a constant temperature. The supply time of ^tBu₂Te was 5 s and the purging time was 60 s. The temperatures of the container and the delivery line of ^tBu₂Te were 30 °C and 40 °C, respectively. During the supply of ^tBu₂Te, the vapor of the Te precursor was delivered into the annealing chamber together with an Ar gas of 200 sccm. The partial pressure of Ar was maintained at 1.0 torr, and the exposure of ^tBu₂Te was approximately 1.7 × 10⁶ Langmuir per pulse. Since Ge–Sb and GST films are readily oxidized in air, we passivated the ALD Ge–Sb films and the tellurized films by approximately 4 nm-thick silicon oxide thin film using an e-beam evaporator immediately after unloading from the reactor.

Transmission electron microscope (TEM, JEM-2100F HR, Jeol Ltd., Japan) equipped with energy dispersive spectroscopy (EDS) was used to observe the thickness, the step coverage, and the elemental distribution of the film. The scanning time for EDS ranged from 186 to 300 s. Raman spectroscopy (LabRam HR, Horiba Jobin-Yvon, France) with 514.5 nm argon ion laser source was used for the structural analysis of the amorphous and crystalline phases. X-ray diffraction (XRD, Empyrean, PANalytical, Netherland) with Cu K α was also used range in the 2 θ from 20° to 60° for crystal structure. The composition of the films was analyzed by Auger electron spectroscopy (AES, PHI 700, ULVAC-PHI, Japan) with an incident electron beam energy



of 5 keV. The MNN Auger lines of Sb and Te, the LMM lines of Ge, Si, Ti, and Cl, and the KLL lines of C, O, N were used for chemical analysis. The sputter-etching rate for depth profiling was 1.4 nm min^{-1} for the thermal SiO_2 film. Due to the variation of the AES depth profile, the area corresponding to Ge, Sb, and Te was calculated by integrating the concentration distribution, and the composition of the film was expressed by area ratio. The phase transition temperature of the film was obtained by measuring the resistance during heating and cooling the sample with a ramp rate of $6.6^\circ\text{C min}^{-1}$.

Results and discussion

A. ALD Ge–Sb film

First, we investigated the feasibility of the ALD reaction using GeCl_2 -dioxane or GeCl_4 with $\text{Sb}(\text{SiEt}_3)_3$ precursors by DFT calculations. To simulate the half-reactions of the ALD process, we calculated the ligand-exchange reactions between precursors assuming that dioxane does not contribute to the reaction, as shown in eqn (1) and (2).



Fig. 1 shows the energy diagrams for the reactions. Both reactions are exothermic with the reaction energies of 0.58 eV for GeCl_2 and 1.34 eV for GeCl_4 . However, the two reactions showed a large difference in activation energy. The reaction with GeCl_2 showed a relatively low activation energy of 0.14 eV, whereas the reaction with GeCl_4 showed a rather high activation

energy of 2.85 eV. The transition state for GeCl_2 in Fig. 1(a) is very similar to the physisorption state without any dissociation of chemical bonds in both precursors. However, the transition state for GeCl_4 in Fig. 1(b) shows the dissociation of the SiEt_3 group from the Sb precursor. We calculated the dissociation energy of the Sb–Si bond in the Sb precursor by using the method described in the previous work,³⁷ and found that the calculated value of 2.86 eV is almost the same as the activation energy of 2.85 eV. Therefore, we selected GeCl_2 -dioxane as the Ge precursor of the ALD of Ge–Sb.

The growth kinetic of ALD Ge–Sb film using GeCl_2 -dioxane and $\text{Sb}(\text{SiEt}_3)_3$ was investigated at 100°C by QCM measurement. The substrate temperature was set to 100°C because the precursor pulse time required for saturation of the deposition rate was the shortest in the preliminary work using another reactor (not shown here). The mass changes by 20 pulses of GeCl_2 -dioxane or $\text{Sb}(\text{SiEt}_3)_3$ was less than 0.01 ng cm^{-2} , which shows the thermal decomposition of the precursors is negligible. Fig. 2 shows the mass change per cycle (ng cm^{-2} per cycle) as functions of the feeding time of each precursor. Self-limited growth was observed for the Ge precursor pulse longer than 10 s and for the Sb precursor pulse longer than 10 s, respectively. The saturated mass change per cycle was $\sim 15.6 \text{ ng cm}^{-2}$ per cycle, and the saturated growth rate was $0.021 \text{ nm per cycle}$ by TEM observation.

The composition ratio of Ge to Sb of the ALD Ge–Sb film is approximately 2.2 by AES, as shown in Fig. 3. We investigated the effect of Ge precursor feeding time on the composition, but the composition was almost constant regardless of Ge precursor feeding time. The ALD film contained approximately 10% of carbon impurities, but carbon impurities are not attributed to dioxane or silyl groups of the precursors because no O or Si impurities were not detected in the deposited film. The reaction byproduct of the ALD process of this work is Et_3SiCl as described in eqn (1), and Knapas *et al.* observed the dissociation of Et_3SiCl to form Et_2SiClH and C_2H_4 during the ALD of GeTe at 90°C using the GeCl_2 -dioxane and $\text{Te}(\text{SiEt}_3)_2$.³⁸ Since C_2H_4 is known to adsorb well on the surface of Ge,³⁹ Si,⁴⁰ or metal,^{41,42} it would be redeposited on the growing film surface, resulting in carbon impurities.

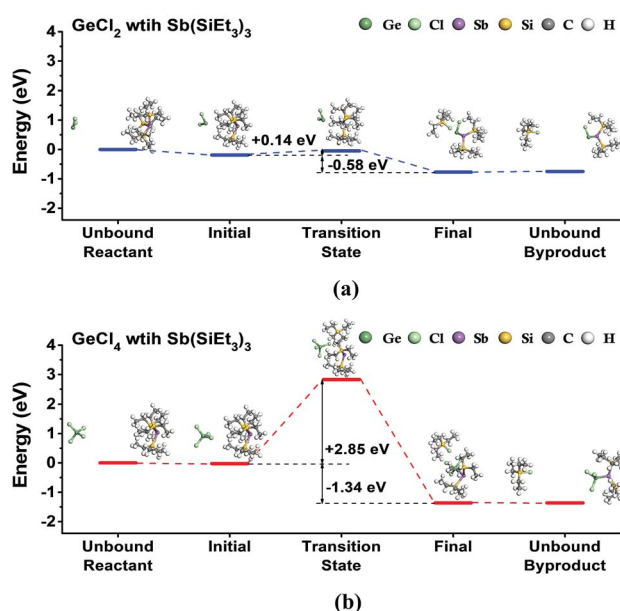


Fig. 1 The energy diagram for the ligand-exchange reaction of (a) GeCl_2 or (b) GeCl_4 with $\text{Sb}(\text{SiEt}_3)_3$, assuming that a chlorine atom of Ge precursor was exchanged by a SiEt_3 ligand of $\text{Sb}(\text{SiEt}_3)_2$. Reaction products are $\text{ClGeSb}(\text{SiEt}_3)_2$ and SiEt_3Cl for GeCl_2 , and $\text{Cl}_3\text{GeSb}(\text{SiEt}_3)_2$ and SiEt_3Cl for GeCl_4 .

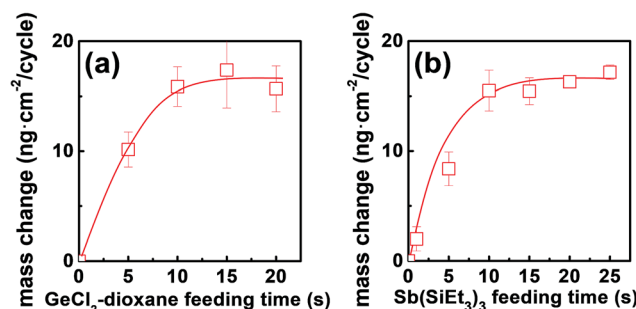


Fig. 2 Mass change per cycle of ALD Ge–Sb at 100°C obtained by QCM measurements as functions of (a) GeCl_2 -dioxane feeding time and (b) $\text{Sb}(\text{SiEt}_3)_3$ feeding time. The feeding time of $\text{Sb}(\text{SiEt}_3)_3$ was fixed at 15 s in (a), and the feeding time of GeCl_2 -dioxane was fixed at 10 s in (b).



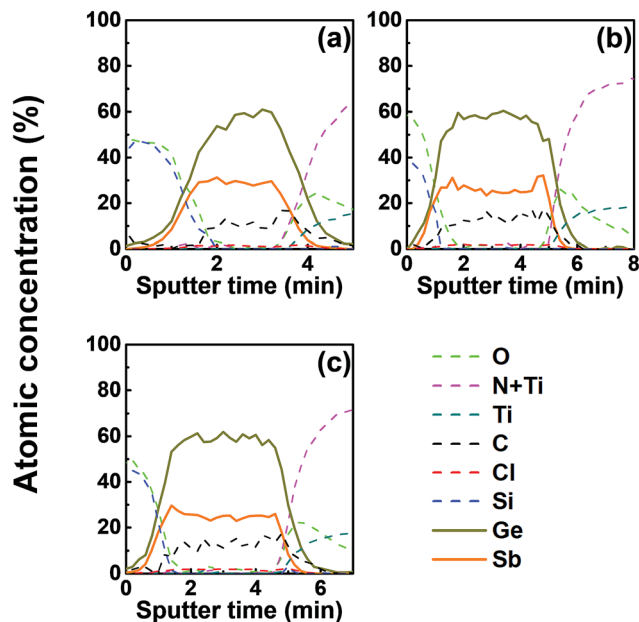


Fig. 3 The AES depth profiles of the ALD Ge–Sb film prepared with the Ge precursor feeding times of (a) 5 s, (b) 10 s, and (c) 15 s. The ALD Ge–Sb films were deposited on TiN substrates and were passivated by e-beam evaporated silicon oxide films.

Fig. 4 shows the Raman spectra of the ALD film. The spectrum was almost the same regardless of Ge precursor feeding time. A peak corresponding to Ge–Sb bonds was found at $\sim 260\text{ cm}^{-1}$ with a shoulder peak at $\sim 220\text{ cm}^{-1}$.⁴³ The peaks at $\sim 75\text{ cm}^{-1}$ and 150 cm^{-1} correspond to amorphous Ge–Ge bonds,⁴⁴ and the peaks at $\sim 110\text{ cm}^{-1}$ and $\sim 160\text{ cm}^{-1}$ correspond to Sb–Sb bonds.⁴⁵

Fig. 5 shows the mass change of the QCM sensor during the ALD process. The QCM analysis gives information about the surface species on the growing film surface because the mass changes by half-reactions are mainly due to the exchange of ligands. The decrement during purge is due to the release of the physisorbed molecules. m_{GeSb} denotes the mass increase after an ALD cycle. m_{Ge} and m_{Sb} indicate the mass change by the first and the second half-reactions, respectively, and were

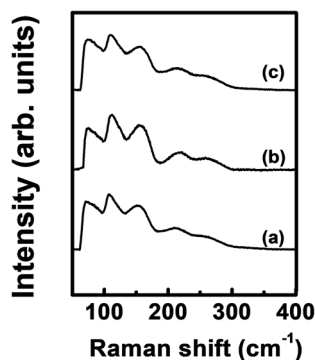


Fig. 4 Raman spectra of the ALD Ge–Sb films prepared with the Ge precursor feeding times of (a) 5 s, (b) 10 s, and (c) 15 s.

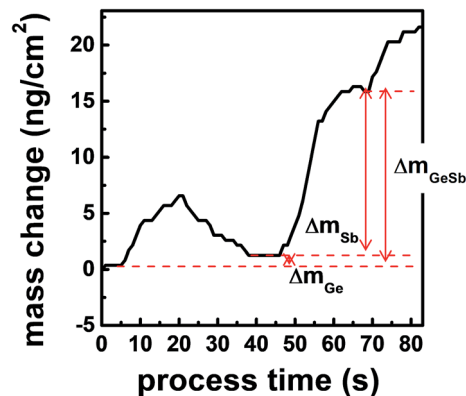


Fig. 5 The mass change during the ALD process using GeCl_2 and $\text{Sb}(\text{SiEt}_3)_3$. m_{GeSb} denotes the mass increase after an ALD cycle. m_{Ge} and m_{Sb} indicate the mass increase by feeding of Ge and Sb precursors, respectively.

determined by saturated frequency changes after the purging. During the first half-reaction by GeCl_2 -dioxane, SiEt_3 is removed by forming Et_3SiCl , and GeCl is chemisorbed on the surface. In Fig. 5, m_{Ge} is very small but is positive. If we assume that the deposited film is Ge_3Sb_2 , the mass change after the first half cycle should be negative because SiEt_3 (115.27 u) leaving the surface is heavier than GeCl (108.09 u) adsorbing on the surface. $\text{Ge}_{2.2}\text{Sb}$ has about 47% more Ge than Ge_3Sb_2 ($\text{Ge}_{1.5}\text{Sb}$), so the mass gain increases by 33.92 u corresponding to 0.47 Ge atoms, resulting in the positive mass change of 26.74 u during the first half-reaction. If dioxane (88.11 u) could be chemisorbed on the surface, m_{Ge} should be significantly increased. Therefore, dioxane did not participate in the ALD reaction after being separated from GeCl_2 -dioxane, confirming the assumption in eqn (1). During the second half reaction by $\text{Sb}(\text{SiEt}_3)_3$, Cl is removed by forming Et_3SiCl , and $\text{Sb}(\text{SiEt}_3)_2$ is chemisorbed on the surface. A relatively large m_{Sb} is due to the difference in masses of Cl (35.45 u) and $\text{Sb}(\text{SiEt}_3)_2$ (352.30 u). In Fig. 5, the observed value of $m_{\text{GeSb}}/m_{\text{Ge}}$ is 17.4, which is comparable to the calculated value of 14.2 considering that a large error occurs in $m_{\text{GeSb}}/m_{\text{Ge}}$ due to the small error of m_{Ge} .

Since the Ge(II) precursor and Sb(III) precursor of this study have 2 and 3 ligands, respectively, the ligand-exchanging reaction assumed in Fig. 1 would produce a Ge_3Sb_2 film. However, the AES composition ratio of Ge to Sb of the ALD Ge–Sb film was approximately 2.2, which is higher than 1.5 of Ge_3Sb_2 . Pore *et al.*²⁸ also reported a Ge concentration of 82% in the ALD Ge–Sb film, which is even higher than the present study. The most stable oxidation state of Ge is +4, and Ge with the oxidation state of +2 is stabilized by dioxane in the GeCl_2 -dioxane complex. When GeCl_2 -dioxane molecules begin to be supplied into the ALD reactor, they initially react with the $-\text{SiEt}_3^*$ surface groups to form the $-\text{GeCl}^*$ surface groups and volatile ClSiEt_3 and dioxane molecules. Since Ge atoms in $-\text{GeCl}^*$ groups are not stabilized by dioxane, additional GeCl_2 -dioxane molecules would have a chance to interact with a portion of Ge atoms in $-\text{GeCl}^*$ groups, resulting in a Ge–Sb film with a higher Ge concentration than Ge_3Sb_2 , as illustrated in Fig. 6. Further



research using *in situ* surface analyses is needed to reveal the reaction mechanism for high Ge concentrations in the ALD Ge-Sb films.

B. Tellurization of the ALD Ge-Sb film

We investigated the effect of tellurization annealing temperature on the composition profile of the ALD Ge-Sb film. 11 nm-thick ALD Ge-Sb films were prepared by alternating exposures to GeCl_2 -dioxane and $\text{Sb}(\text{SiEt}_3)_3$ for 500 cycles and then tellurized for 100 cycles at different temperatures ranging from 170 to 250 °C. The AES depth profiles of the tellurized films are shown in Fig. 7. The ALD Ge-Sb films turned into tellurium-containing films by the tellurization annealing. When the tellurization temperature was 180 °C or below, tellurium was distributed only near the surface. When annealed at 190 °C, tellurium atoms diffused into the lower part of the Ge-Sb film, but tellurium concentration was not uniform. When annealed at 230 °C or higher temperatures, the entire film was fully tellurized to form a GST film having a composition similar to $\text{Ge}_3\text{Sb}_2\text{Te}_6$. Since the GST composition with the highest Ge content is $\text{Ge}_2\text{Sb}_2\text{Te}_5$, the $\text{Ge}_3\text{Sb}_2\text{Te}_6$ phase would be the mixture

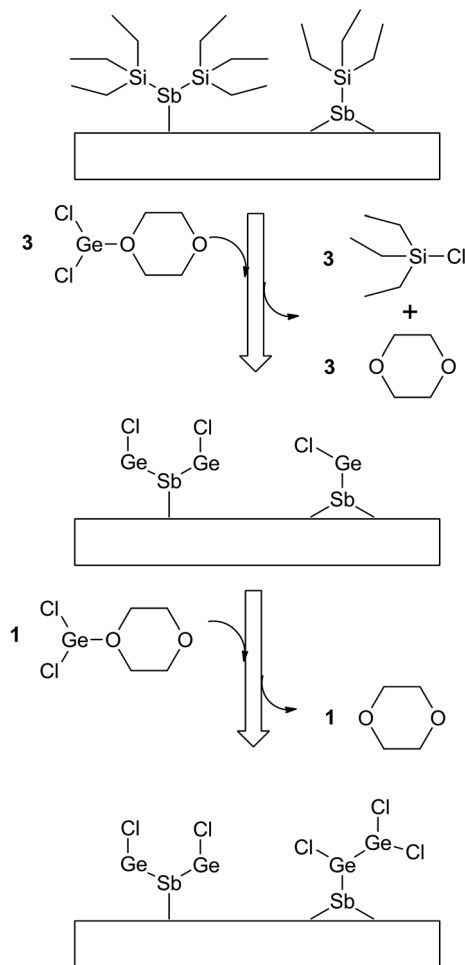


Fig. 6 The proposed surface reaction of ALD GeSb from GeCl_2 -dioxane and $\text{Sb}(\text{SiEt}_3)_3$ to form Ge-rich Ge-Sb films.

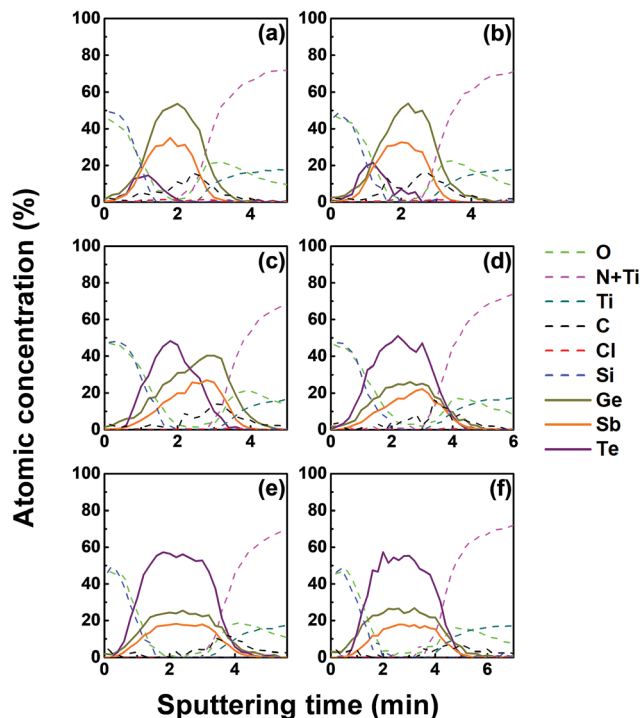


Fig. 7 AES depth profile of the ALD Ge-Sb film on a TiN substrate after tellurization annealing for 100 cycles at different temperatures: (a) 170 °C, (b) 180 °C, (c) 190 °C, (d) 200 °C, (e) 230 °C, and (f) 250 °C. Immediately after unloading the tellurized film from the reactor, a 4 nm thick silicon oxide film was deposited on the tellurized film using an e-beam evaporator.

of $\text{Ge}_2\text{Sb}_2\text{Te}_5$ and GeTe . The tellurized films have lower $[\text{Ge}]/[\text{Sb}]$ ratios than ~ 2.2 of the as-deposited ALD film because Ge diffuses into the TiN underlayer as well as the silicon oxide overlayer. The concentration of carbon impurities in the as-deposited ALD Ge-Sb film were eliminated by tellurization annealing at 230 °C or higher temperatures because the desorption of the chemisorbed C_2H_4 molecules become active at high substrate temperatures.

Fig. 8 shows the concentration ratio of Ge to Sb ($[\text{Ge}]/[\text{Sb}]$) of the ALD Ge-Sb film before and after tellurization. The central region with low oxygen concentration corresponds to ALD Ge-Sb film while the regions with high oxygen concentrations correspond to the silicon oxide overlayer (left) and the TiN underlayer (right). The $[\text{Ge}]/[\text{Sb}]$ ratio is high in the TiN and the silicon oxide layers in all cases, suggesting that Ge diffuses into TiN and silicon oxide. Fig. 8(a) shows that the $[\text{Ge}]/[\text{Sb}]$ is the lowest at the Ge-Sb/TiN and SiOx/Ge-Sb interface. The nonuniform distribution of $[\text{Ge}]/[\text{Sb}]$ is because the diffusion of Ge into TiN and SiOx occurred, but the redistribution of Ge in the Ge-Sb film was very slow. At higher annealing temperature shown in Fig. 8(d), the Ge was redistributed in the GST film, resulting in a relatively uniform $[\text{Ge}]/[\text{Sb}]$ ratio. Therefore, as the annealing temperature increases, the redistribution of Ge in GST film is more active, increasing the minimum $[\text{Ge}]/[\text{Sb}]$ value. The total amount of Ge in GST film can be determined by the average $[\text{Ge}]/[\text{Sb}]$ value, which decreases with increasing



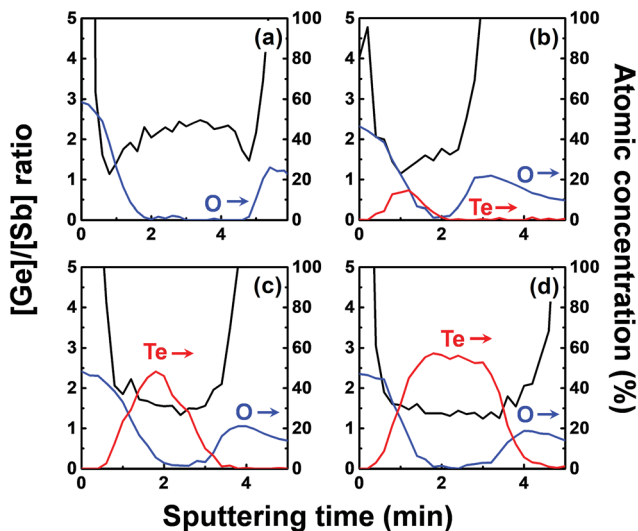


Fig. 8 The ratio of germanium to antimony and the concentration of tellurium and oxygen in the ALD Ge–Sb film: (a) before tellurization; after tellurization at (b) 170 °C, (c) 190 °C, and (d) 230 °C. Immediately after unloading the tellurized film from the reactor, a 4 nm thick silicon oxide film was deposited on the tellurized film using an e-beam evaporator.

annealing temperature. As a consequence, the $[Ge]/[Sb]$ of the as-deposited Ge–Sb film is about 2.2 as shown in Fig. 8(a) but decreased to about 1.5 by tellurization at 230 °C as shown in Fig. 8(d).

Since the diffusion of Ge into TiN is observed before the tellurization annealing, Ge diffuses even during the ALD at 100 °C. Also, Ge was incorporated in the silicon oxide passivation layer during the e-beam evaporation. The report on the diffusion into TiN was rare, and the active diffusion of Ge into TiN might be due to the columnar structure of sputtered TiN.⁴⁶ To improve the reliability of the PCRAM device, ALD GST films, as well as sputtered GST films, require a diffusion barrier superior to TiN as the electrode material. The diffusion of Ge in silicon oxide at high temperatures has been reported,⁴⁷ and the active diffusion of Ge into the e-beam evaporated silicon oxide layer at low temperatures might also be due to the columnar structure of the silicon oxide film.⁴⁸

The structural changes by tellurization at different annealing temperatures were investigated by Raman spectroscopy, as shown in Fig. 9. At the tellurization temperature of 180 °C or below, the Raman spectra show little changes from the spectrum before tellurization. Peaks corresponding to Ge–Sb bonds,⁴³ amorphous Ge–Ge bonds,⁴⁴ and an Sb–Sb bond,⁴⁵ described in Fig. 4, still exist. However, when the annealing temperature was raised to 190 °C or higher temperatures, the Ge–Sb bond peaks disappeared at ~ 220 and ~ 260 cm^{-1} , which agrees with AES depth profiles in Fig. 7. The intensities of amorphous Ge–Ge bond peaks at ~ 75 and ~ 150 cm^{-1} also decreased with increasing tellurization temperature and are overlapped with an Sb–Te peak evolving at ~ 70 cm^{-1} and an Sb–Sb peak remaining at ~ 160 cm^{-1} .^{45,49} The change of an Sb–

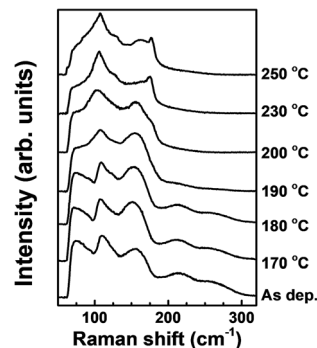


Fig. 9 Raman spectra of the ALD Ge–Sb films before and after the tellurization at different temperatures.

Sb peak at ~ 110 cm^{-1} was hard to distinguish with the vibration of heteropolar bond in the tetrahedral GeTe_4 and pyramidal SbTe_3 evolving at almost the same wave number.⁵⁰ When the tellurization annealing temperature is 230 °C or above, an Sb–Sb peak at ~ 160 cm^{-1} disappeared, and peaks corresponding to Sb–Te and Ge–Te bonds strongly evolved. The intensity of the peak at ~ 110 cm^{-1} significantly increased, Sb–Te peaks evolved at ~ 70 cm^{-1} and ~ 175 cm^{-1} , and a weak peak corresponding to Ge–Te bonds evolved at ~ 125 cm^{-1} .⁵¹ The Sb–Te bond peak located at ~ 175 cm^{-1} suggests that the crystal structure of the tellurized film was hexagonal close-packing (HCP).^{52,53}

The phase transition temperature of the tellurized Ge–Sb film was investigated as shown in Fig. 10. The temperature at which the resistance rapidly drops was determined as the phase transition temperature. Since the film was crystallized by tellurization annealing, the annealed film was amorphized by melt-quenching with a KrF excimer laser. Since the crystallization temperature of GeSb is higher than 300 °C,⁵⁴ we could not observe the phase transition of the ALD Ge–Sb thin film annealed at low temperature or as-deposited state even when the sample was heated up to 300 °C, the maximum temperature

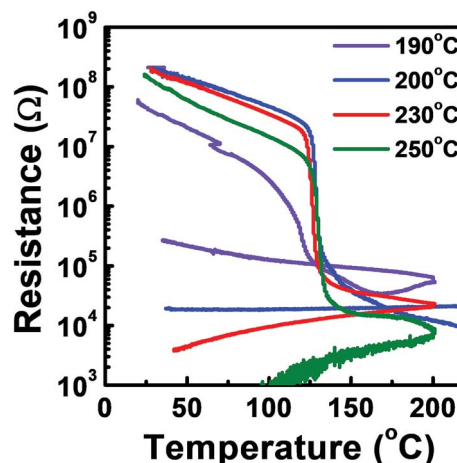


Fig. 10 Resistance versus temperature plot of the ALD Ge–Sb film tellurized at different temperatures.



of the probe station that we used. For the films fully tellurized at 200 °C or higher temperatures, the phase transition temperature was ~123 °C, which is similar to ~130 °C of the Ge₂Sb₂Te₅ prepared by sputtering.⁵⁵

The gap filling characteristics of the tellurized ALD Ge–Sb film were investigated by TEM, as shown in Fig. 11. An 11 nm-thick ALD Ge–Sb film was prepared on the trench pattern with an aspect ratio of 3.75 and a top opening width of 40 nm. As the tellurization temperature increases, the tellurium concentration increased, and the volume of the conformal ALD film tends to expand. In particular, when tellurized at 230 °C, the trench pattern is completely filled with the tellurized film and the 11 nm of GeSb thin film deposited on the planar substrate turned into 24 nm of GeSbTe film. The EDS mapping shows the uniform distribution of Te in the trench. However, when the film was tellurized at 250 °C, a void was formed at the bottom of the pattern, probably due to severe agglomeration of the film.

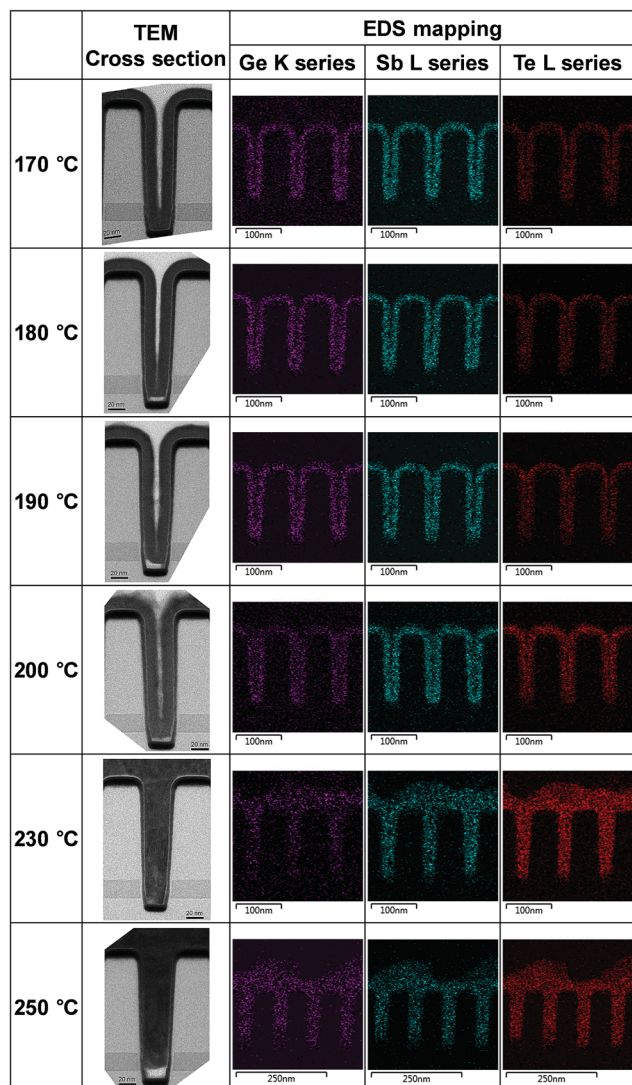


Fig. 11 Cross-sectional TEM photographs and EDS mapping of the tellurized ALD Ge–Sb film prepared on a trench pattern.

Conclusions

We prepared ALD Ge–Sb film by alternating exposures to GeCl₂-dioxane and Sb(SiEt₃)₃ precursors at 100 °C. The growth rate was 0.021 nm per cycle, and the composition ratio of Ge to Sb was approximately 2.2. The ALD Ge–Sb films turned into GST films by the tellurization annealing. When the tellurization temperature was raised to 190 °C or higher temperatures, the Ge–Sb bond peaks and the amorphous Ge–Ge bond peak disappeared. The peaks corresponding to Ge–Te and Sb–Te bonds were evolved at 200 °C or higher temperatures, resulting in the phase transition temperature of 123 °C. At 230 °C or higher temperatures, the entire film was fully tellurized to form a crystalline GST film having a relatively uniform composition of Ge₃Sb₂Te₆, and the carbon impurities in the as-deposited ALD Ge–Sb film were eliminated. As the tellurization temperature increases, the volume of the ALD film is expanded owing to the incorporation of tellurium, resulting in complete filling of a trench pattern by GST film after the tellurization at 230 °C. Also, the thin tellurized film showed excellent step coverage with a smooth surface.

Conflicts of interest

There are no conflicts to declare.

Acknowledgements

This research was supported by the MOTIE (Ministry of Trade, Industry & Energy) (project number 10045360 and 10080625) and KSRC (Korea Semiconductor Research Consortium) support program for the development of the future semiconductor device.

References

- 1 G. Yue-Feng, L. Yun, S. Zhi-Tang and F. Song-Lin, *Chin. Phys. Lett.*, 2008, **25**, 3455–3458.
- 2 G. W. Burr, B. N. Kurdi, J. C. Scott, C. H. Lam, K. Gopalakrishnan and R. S. Shenoy, *IBM J. Res. Dev.*, 2008, **52**, 449–464.
- 3 J. Meena, S. Sze, U. Chand and T.-Y. Tseng, *Nanoscale Res. Lett.*, 2014, **9**, 526.
- 4 S. Hudgens and B. Johnson, *MRS Bull.*, 2004, **29**, 829–832.
- 5 D. H. Im, J. I. Lee, S. L. Cho, H. G. An, D. H. Kim, I. S. Kim, H. Park, D. H. Ahn, H. Horii, S. O. Park, U. I. Chung and J. T. Moon, in *2008 IEEE International Electron Devices Meeting*, IEEE, 2008, pp. 1–4.
- 6 M. Kinoshita, Y. Sasago, H. Minemura, Y. Anzai, M. Tai, Y. Fujisaki, S. Kusaba, T. Morimoto, T. Takahama, T. Mine, A. Shima, Y. Yonamoto and T. Kobayashi, in *2012 Symposium on VLSI Technology (VLSIT)*, IEEE, 2012, vol. 97, pp. 35–36.
- 7 N. Yamada, E. Ohno, K. Nishiuchi, N. Akahira and M. Takao, *J. Appl. Phys.*, 1991, **69**, 2849–2856.



- 8 N. Yamada, in *1997 Optical Data Storage Topical Meeting ODS Conference Digest*, ed. H. Birecki and J. Z. Kwiciczen, IEEE, 1997, vol. 3109, pp. 98–99.
- 9 T. Ohta, *J. Optoelectron. Adv. Mater.*, 2001, **3**, 609–626.
- 10 C.-F. Chen, A. Schrott, M. H. Lee, S. Raoux, Y. H. Shih, M. Breitwisch, F. H. Baumann, E. K. Lai, T. M. Shaw, P. Flaitz, R. Cheek, E. A. Joseph, S. H. Chen, B. Rajendran, H. L. Lung and C. Lam, in *2009 IEEE International Memory Workshop*, IEEE, 2009, pp. 1–2.
- 11 H. K. Lyeo, D. G. Cahill, B. S. Lee, J. R. Abelson, M. H. Kwon, K. B. Kim, S. G. Bishop and B. K. Cheong, *Appl. Phys. Lett.*, 2006, **89**, 151904.
- 12 M. H. Jang, S. J. Park, S. J. Park, M.-H. Cho, E. Z. Kurmaev, L. D. Finkelstein and G. S. Chang, *Appl. Phys. Lett.*, 2010, **97**, 152113.
- 13 S. W. Ryu, J. H. Oh, J. H. Lee, B. J. Choi, W. Kim, S. K. Hong, C. S. Hwang and H. J. Kim, *Appl. Phys. Lett.*, 2008, **92**, 142110.
- 14 M. Bouška, S. Pechev, Q. Simon, R. Boidin, V. Nazabal, J. Gutwirth, E. Baudet and P. Němec, *Sci. Rep.*, 2016, **6**, 26552.
- 15 B. J. Choi, S. Choi, Y. C. Shin, K. M. Kim, C. S. Hwang, Y. J. Kim, Y. J. Son and S. K. Hong, *Chem. Mater.*, 2007, **19**, 4387–4389.
- 16 J. Lee, S. Choi, C. Lee, Y. Kang and D. Kim, *Appl. Surf. Sci.*, 2007, **253**, 3969–3976.
- 17 V. Pore, T. Hatanpää, M. Ritala and M. Leskela, *J. Am. Chem. Soc.*, 2009, **131**, 3478–3480.
- 18 M. Ritala, V. Pore, T. Hatanpää, M. Heikkilä, M. Leskelä, K. Mizohata, A. Schrott, S. Raoux and S. M. Rossnagel, *Microelectron. Eng.*, 2009, **86**, 1946–1949.
- 19 M. Leskelä, V. Pore, T. Hatanpää, M. Heikkilä, M. Ritala, A. Schrott, S. Raoux and S. Rossnagel, *ECS Trans.*, 2009, **25**, 399–407.
- 20 T. Eom, T. Gwon, S. Yoo, B. J. Choi, M.-S. Kim, I. Buchanan, S. Ivanov, M. Xiao and C. S. Hwang, *Chem. Mater.*, 2015, **27**, 3707–3713.
- 21 H. K. Kim, in *SEMICON Korea*, 2016.
- 22 K. Do, D. Lee, D.-H. Ko, H. Sohn and M.-H. Cho, *Electrochem. Solid-State Lett.*, 2010, **13**, H284.
- 23 H. K. Kim, S. J. Chae and B. K. Lee, in *E*Pcos 2015*, 2015.
- 24 S. A. Gamboa, P. J. Sebastian and M. A. Rivera, *Sol. Energy Mater. Sol. Cells*, 1998, **52**, 293–299.
- 25 S. A. Gamboa, P. J. Sebastian, X. Mathew, H. Nguyen-Cong and P. Chartier, *Sol. Energy Mater. Sol. Cells*, 1999, **59**, 115–124.
- 26 Z. Sun, S. Liufu, X. Chen and L. Chen, *J. Phys. Chem. C*, 2011, **115**, 16167–16171.
- 27 K. Suda, T. Uno, T. Miyakawa, N. Sawamoto, H. Machida, M. Ishikawa, H. Sudoh, Y. Ohshita and A. Ogura, *Jpn. J. Appl. Phys.*, 2013, **52**, 128006.
- 28 V. Pore, K. Knapas, T. Hatanpää, T. Sarnet, M. Kemell, M. Ritala, M. Leskelä and K. Mizohata, *Chem. Mater.*, 2011, **23**, 247–254.
- 29 B. Han, Y. Kim, Y.-J. Kim, M.-H. Cho, S.-K. Rha and W.-J. Lee, *ECS J. Solid State Sci. Technol.*, 2019, **8**, P298–P302.
- 30 Elsevier, *Mater. Sci. Semicond. Process.*, 2015, **40**, 50–57.
- 31 J.-F. Zheng, *US Pat.* 8,617,972 B2, Westport, CT, US, 2013.
- 32 J. P. Perdew, K. Burke and M. Ernzerhof, *Phys. Rev. Lett.*, 1996, **77**, 3865–3868.
- 33 B. Delley, *J. Chem. Phys.*, 1990, **92**, 508–517.
- 34 B. Han, Y.-J. Kim, J.-M. Park, L. L. Yusup, J. Shin and W.-J. Lee, *J. Nanosci. Nanotechnol.*, 2017, **17**, 3472–3476.
- 35 B. Han, Y.-J. Kim, J.-M. Park, L. L. Yusup, H. Ishii, C. Lansalot-Matras and W.-J. Lee, *J. Nanosci. Nanotechnol.*, 2016, **16**, 4924–4928.
- 36 B. Han, K. Ha Choi, J. Min Park, J. Woo Park, J. Jung and W.-J. Lee, *J. Vac. Sci. Technol.*, A, 2013, **31**, 01A145.
- 37 J. M. Park, S. J. Jang, L. L. Yusup, W. J. Lee and S. I. Lee, *ACS Appl. Mater. Interfaces*, 2016, **8**, 20865–20871.
- 38 K. Knapas, T. Hatanpää, M. Ritala and M. Leskela, *Chem. Mater.*, 2010, **22**, 1386–1391.
- 39 P. Lal, A. V. Teplyakov, Y. Noah, M. J. Kong, G. T. Wang and S. F. Bent, *J. Chem. Phys.*, 1999, **110**, 10545–10553.
- 40 L. Clemen, R. M. Wallace, P. A. Taylor, M. J. Dresser, W. J. Choyke, W. H. Weinberg and J. T. Yates, *Surf. Sci.*, 1992, **268**, 205–216.
- 41 F. P. Netzer and R. A. Wille, *Surf. Sci.*, 1978, **74**, 547–567.
- 42 E. W. R. Steacie and H. V. Stovel, *J. Chem. Phys.*, 1934, **2**, 581–584.
- 43 C. C. Huang, B. Gholipour, K. Knight, J. Y. Ou and D. W. Hewak, *Adv. Optoelectron.*, 2012, **2012**, 1–7.
- 44 P. Kazimierski, J. Tyczkowski, M. Kozanecki, Y. Hatanaka and T. Aoki, *Chem. Mater.*, 2002, **14**, 4694–4701.
- 45 P. Němec, V. Nazabal, A. Moreac, J. Gutwirth, L. Beneš and M. Frumar, *Mater. Chem. Phys.*, 2012, **136**, 935–941.
- 46 S. K. Rha, W. J. Lee, S. Y. Lee, Y. S. Hwang, Y. J. Lee, D. Il Kim, D. W. Kim, S. S. Chun and C. O. Park, *Thin Solid Films*, 1998, **320**, 134–140.
- 47 M. Ogino, Y. Oana and M. Watanabe, *Phys. Status Solidi*, 1982, **72**, 535–541.
- 48 A. Barna, O. Geszti, L. Gosztola and E. Seyfried, *MRS Proc.*, 1985, **61**, 367.
- 49 K. M. F. Shahil, M. Z. Hossain, V. Goyal and A. A. Balandin, *J. Appl. Phys.*, 2012, **111**, 054305.
- 50 L. Bo, S. Zhi-Tang, Z. Ting, F. Song-Lin and C. Bomy, *Chin. Phys.*, 2004, **13**, 1947–1950.
- 51 K. S. Andrikopoulos, S. N. Yannopoulos, G. A. Voyiatzis, A. V. Kolobov, M. Ribes and J. Tominaga, *J. Phys.: Condens. Matter*, 2006, **18**, 965–979.
- 52 E. M. Vinod, K. Ramesh, R. Ganesan and K. S. Sangunni, *Appl. Phys. Lett.*, 2014, **104**, 063505.
- 53 G. C. Sosso, S. Caravati, C. Gatti, S. Assoni and M. Bernasconi, *J. Phys.: Condens. Matter*, 2009, **21**, 245401.
- 54 S. Raoux, H.-Y. Cheng, B. Muñoz and J. L. Jordan-Sweet, *Eur. Phase Chang. Ovonic Sci. Symp.*, 2009, pp. 91–98.
- 55 T. Nirschl, J. B. Philipp, T. D. Happ, G. W. Burr, B. Rajendran, M.-H. Lee, A. Schrott, M. Yang, M. Breitwisch, C.-F. Chen, E. Joseph, M. Lamorey, R. Cheek, S.-H. Chen, S. Zaidi, S. Raoux, Y. C. Chen, Y. Zhu, R. Bergmann, H.-L. Lung and C. Lam, in *2007 IEEE International Electron Devices Meeting*, IEEE, 2007, pp. 461–464.

