



Electrically tunable spin–orbit interaction in an InAs nanosheet†

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We report an experimental study of the spin–orbit interaction (SOI) in an epitaxially grown free-standing InAs nanosheet in a dual-gate field-effect device. Gate-transfer characteristic measurements show that independent tuning of the carrier density in the nanosheet and the potential difference across the nanosheet can be efficiently achieved with the use of a dual gate. The quantum transport characteristics of the InAs nanosheet are investigated by magnetoconductance measurements at low temperatures. It is shown that the electron transport in the nanosheet can be tuned from the weak antilocalization to the weak localization and then back to the weak antilocalization regime with a voltage applied over the dual gate without a change in the carrier density. The spin–orbit length extracted from the magnetoconductance measurements at a constant carrier density exhibits a peak value at which the SOI of the Rashba type is suppressed and the spin relaxation due to the presence of an SOI of the Dresselhaus type in the nanosheet can be revealed. Energy band diagram simulations have also been carried out for the device under the experimental conditions and the physical insights into the experimental observations have been discussed in light of the results of simulations.

Low-dimensional III–V narrow bandgap semiconductor nanostructures have attracted significant interest due to their potential applications in nanoelectronics,^{1,2} infrared optoelectronics,^{3,4} spintronics,^{5–7} and quantum electronics.^{8–13} Among them, strong spin–orbit interaction (SOI) and large Landé *g*-factor have made InAs and InSb nanostructures widely used in realization of novel devices such as spin transistors,^{14,15} spin–orbit qubits,^{16,17} and topological quantum devices.^{18–21} The SOI originates from the nonrelativistic approach to the relativistic

Dirac equation and describes that a moving (orbit) charged particle in an electric field experiences an effective magnetic field and thus the spin and momentum of the charged particle are coupled with the coupling strength being related to the electric field. In crystalline materials, SOIs appear dominantly due to bulk inversion asymmetry (known as the Dresselhaus type) and structural inversion asymmetry (known as the Rashba type).^{22,23} The Rashba SOIs provide a possibility to manipulate spins in materials by electrical means and thus have potential applications in building spin-based devices, such as Datta-Das spin transistors,^{14,24} and fast controllable spin qubits.^{25–27} Recently, epitaxial growth techniques of single-crystalline, III–V narrow bandgap, semiconductor nanostructures have been successfully extended to grow free-standing InAs nanosheets.²⁸ The preliminary study has experimentally demonstrated the existence of strong SOIs in the nanosheets.⁹ However, due to the employment of a single-gate device configuration, the study has failed to reveal the origin and an efficient control of the SOI in the InAs nanosheets.

In this work, we report a transport measurement study of a dual-gate field-effect device made from an epitaxially grown free-standing InAs nanosheet and demonstration of that the SOI presented in the InAs nanosheet is strongly tunable with the use of a dual gate. We will show that the electron transport in the InAs nanosheet can be efficiently tuned from the weak antilocalization (WAL) to the weak localization (WL) and then back to the WAL regime without tuning the carrier density and that the spin–orbit length extracted at a constant carrier density exhibits a peak value, at which the Rashba SOI is suppressed and thus the Dresselhaus SOI can be revealed. We will also present energy band diagram simulations for the layer structure of the device and discuss the physical insights of the experimental observations.

The free-standing, single-crystalline and pure-phase InAs nanosheet investigated in this work is grown *via* molecular beam epitaxy (MBE) on a Si (111) substrate. The growth is carried out under indium-rich conditions, at a temperature of 545 °C for 40 min, with the indium and arsenic fluxes fixed at

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9.3×10^{-7} and 5.9×10^{-6} mbar, respectively (see ref. 28 for further details about the nanosheet growth). Fig. 1(a) shows a side-view scanning electron microscope (SEM) image of the as-grown free-standing InAs nanosheets on a Si (111) substrate. The thicknesses of these InAs nanosheets are about 15–30 nm. Fig. 1(b) shows a high-resolution transmission electron microscope (HRTEM) image of an MBE-grown InAs nanosheet and the inset of Fig. 1(b) shows an electron diffraction (SAED) pattern taken from a selected area of the nanosheet. The HRTEM measurements reveal that these InAs nanosheets are of high-quality wurtzite crystals.

The device fabrication starts with transferring the as-grown InAs nanosheets from the growth substrate onto a heavily n-doped Si substrate capped with a 200 nm-thick SiO_2 layer on top. The Si substrate (contacted by a gold film at the bottom) and the SiO_2 layer later serve as the bottom gate and the bottom-gate dielectric. We select a few InAs nanosheets on the SiO_2 -capped Si substrate and locate them by SEM. On each selected InAs nanosheet, four contact electrodes are fabricated by pattern definition *via* electron-beam lithography (EBL), deposition of a Ti/Au (5/90 nm in thickness) metal bilayer *via* electron-beam evaporation (EBE) and lift-off. Here, we note that before metal deposition, the sample is briefly merged into

a diluted $(\text{NH}_4)_2\text{S}_x$ solution in order to remove the native oxide layer on the nanosheet in the contact areas and to passivate the obtained fresh surfaces. Then, after another step of EBL, a 20 nm-thick HfO_2 dielectric layer covering the entire InAs nanosheet is fabricated by atomic layer deposition and lift-off. Finally, a Ti/Au (5/120 nm in thickness) metal bilayer top gate is fabricated again by EBL, EBE and lift-off. Fig. 1(c) shows a top-view SEM image (in false color) of a fabricated dual-gate field-effect InAs nanosheet device (named Device 1) studied in this work and Fig. 1(d) displays a schematic of the layer structure of the device. In the device, the average width (W) of the InAs conduction channel is ~ 410 nm and the channel length (L), defined by the separation between the two inner contact electrodes, is ~ 830 nm. The thickness of the InAs nanosheet determined by atomic force microscopy measurements after the device fabrication is in a range of 17 to 21 nm. The fabricated device is characterized by gate transfer characteristic measurements and low-field magnetotransport measurements in a four-probe circuit setup, as shown in Fig. 1(c), using a standard lock-in technique, in which a 17 Hz, 10 nA excitation current (I) is supplied through the two outer contact electrodes and the voltage drop (V) between the two inner contact electrodes is recorded. The channel conductance (G) is obtained through $G =$

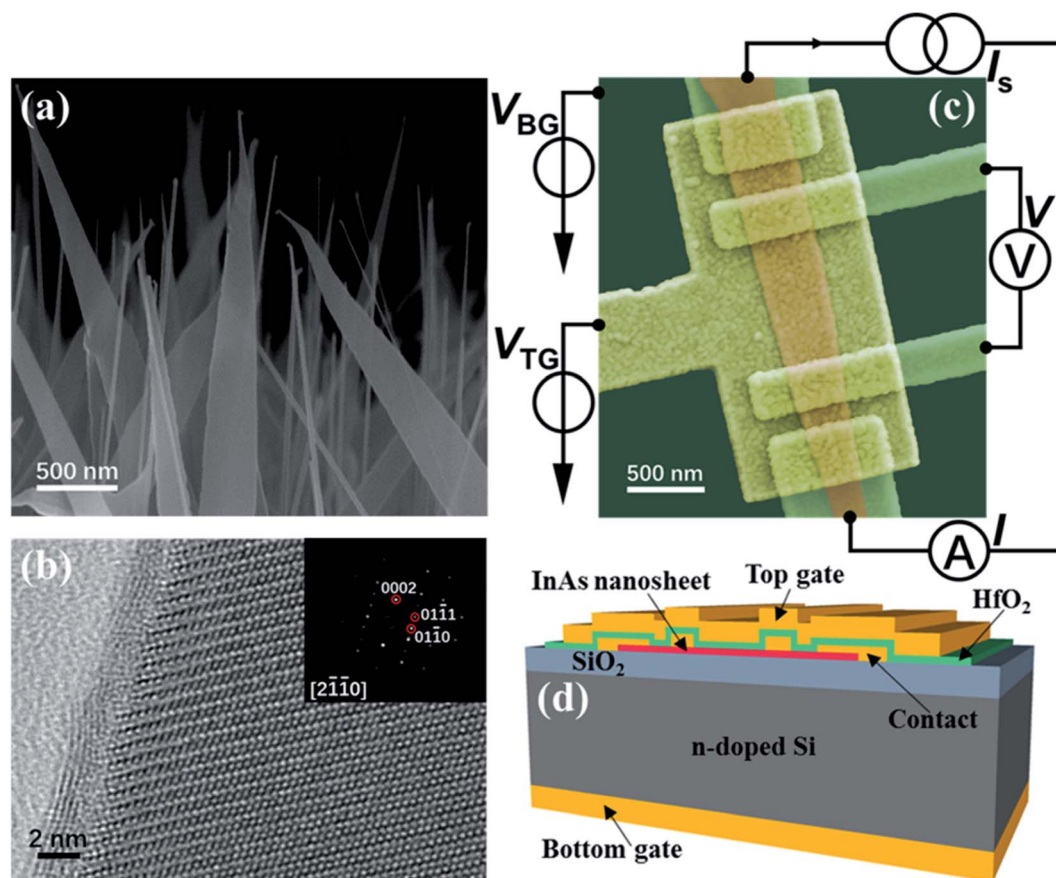


Fig. 1 (a) SEM image (side view) of MBE-grown free-standing InAs nanosheets on a Si (111) substrate. (b) HRTEM image of an InAs nanosheet taken from the substrate shown in (a). The inset represents the corresponding SAED pattern recorded along the $[2\bar{1}10]$ axis. (c) SEM image (top view, in false color) of a fabricated dual-gate field-effect InAs nanosheet device (Device 1) studied in this work and measurement circuit setup. (d) Schematic of the layer structure of the device in (c).



I/V . All the measurements are performed in a physical property measurement system equipped with a uniaxial magnet at low temperatures T and at magnetic fields B applied perpendicular to the InAs nanosheet plane.

Fig. 2 shows the measured InAs nanosheet channel conductance (G) for Device 1 [shown in Fig. 1(c)] as a function of bottom-gate voltage V_{BG} and top-gate voltage V_{TG} at a temperature of $T = 2$ K and zero magnetic field. It is seen that the dual-gate device exhibits a typical n -type transistor characteristic and operates in a depletion mode. It is also seen that the conductance G can be tuned by both gates. However, the top gate exhibits a much strong capacitive coupling to the InAs conduction channel than the bottom gate, which is consistent with the fact that the top gate has a much shorter distance to the InAs nanosheet, and a gate dielectric with a larger dielectric constant than the bottom gate. The pink, purple and orange solid lines in Fig. 2 represent the constant conductance G contours of ~ 12 , ~ 7 and $\sim 4e^2/h$, respectively. Moving along each constant conductance contour, the carrier density in the InAs nanosheet can be assumed to stay approximately at a constant value, but dual-gate voltage V_{D} (defined as $V_{\text{D}} = V_{\text{BG}} - V_{\text{TG}}$) is continuously changed.

The carrier density (n), Fermi wavelength (λ_{F}), and mean free path (L_{e}) in the InAs nanosheet of a dual-gate device can be estimated from the gate transfer characteristic measurements (cf. Section I in the ESI†). The estimated values of n , λ_{F} , and L_{e} for the InAs nanosheet in Device 1 at three different conductance values of ~ 12 , ~ 7 and $\sim 4e^2/h$ are given in Table S1.† The estimated values of $\lambda_{\text{F}} \sim 22$ – 33 nm are comparable to or larger than the thickness of the InAs nanosheet, but much smaller than the lateral sizes of the nanosheet. Thus, the electron transport in the InAs nanosheet at these conductance values could be considered to be of a two-dimensional (2D) nature. The estimated values of L_{e} in the InAs nanosheet are also much smaller than the InAs nanosheet channel length L . The electron

transport in the InAs nanosheet is thus in the diffusive regime. It is seen that a larger value of L_{e} appears at a higher carrier density n in the InAs nanosheet, which is consistent with the fact that a higher carrier density gives rise to a stronger screening of scattering centers for conduction electrons in the nanosheet.

In a 2D diffusive electron conducting system, the transport characteristics, such as phase coherence length L_{ϕ} , spin-orbit length L_{SO} , and mean free path L_{e} , can be extracted from the measurements and analyses of the low-field magnetoconductance. At low temperatures, the electron interference gives rise to the quantum correction of the classical conductance. Hikami, Larkin, and Nagaoka (HLN) showed that by taking the quantum correction into account, the magnetoconductance, $\Delta G = G(B) - G(B = 0)$, at low magnetic fields can then be expressed as:²⁹

$$\Delta G(B) = -\frac{e^2}{\pi h} \left[\frac{1}{2} \Psi \left(\frac{B_{\phi}}{B} + \frac{1}{2} \right) + \Psi \left(\frac{B_{\text{e}}}{B} + \frac{1}{2} \right) - \frac{3}{2} \Psi \left(\frac{(4/3)B_{\text{SO}} + B_{\phi}}{B} + \frac{1}{2} \right) - \frac{1}{2} \ln \left(\frac{B_{\phi}}{B} \right) - \ln \left(\frac{B_{\text{e}}}{B} \right) + \frac{3}{2} \ln \left(\frac{(4/3)B_{\text{SO}} + B_{\phi}}{B} \right) \right] \quad (1)$$

where $\Psi(x)$ is the digamma function, and B_i ($i = \phi, \text{SO}, \text{e}$) are the characteristic fields for different scattering mechanisms. The characteristic transport lengths are related to these characteristic fields by $B_i = \hbar/(4eL_i^2)$.

Fig. 3(a) shows the measured low-field magnetoconductance ΔG of Device 1 at $T = 2$ K and at different dual-gate voltages V_{D} taken along the conductance contour of $G \sim 12e^2/h$ (the pink line in Fig. 2). It is seen that at $V_{\text{D}} = -3.36$ V, ΔG displays a peak near zero magnetic field, *i.e.*, a WAL characteristic,³⁰ implying the presence of a strong SOI in the InAs nanosheet. As V_{D} decreases from -3.36 V to -6.28 V, the measured ΔG shows a transition from a peak-like structure to a dip-like structure near zero magnetic field, *i.e.*, a transition from the WAL to the WL characteristic.³¹ However, as V_{D} is further decreased from -6.28 V to -7.62 V, the WL characteristic is gradually suppressed and ΔG turns to show the WAL characteristic again. This continuous tuning of the electron transport in the InAs nanosheet from the WAL to the WL and back to the WAL characteristic again is for the first time ever demonstrated experimentally in a planar semiconductor nanostructure.

To quantitatively analyze the tuning of the SOI in the InAs nanosheet, we fit the measured ΔG data to the HLN formula [eqn (1)]. The solid lines in Fig. 3(a) represent the results of the fits. Fig. 3(b) displays the extracted values of L_{ϕ} , L_{SO} , and L_{e} from the fits as a function of V_{D} at a fixed conductance value of $G \sim 12e^2/h$. It is seen that the extracted L_{ϕ} displays a weak dependence on V_{D} and stays at $L_{\phi} \sim 214$ nm. This is consistent with the fact that the electron dephasing in the nanosheet is dominantly originated from the electron–electron interaction at low temperatures, which remains approximately unchanged without a change in the carrier density. It is also seen that the extracted L_{e} is also weakly dependent on V_{D} and has a value of L_{e}

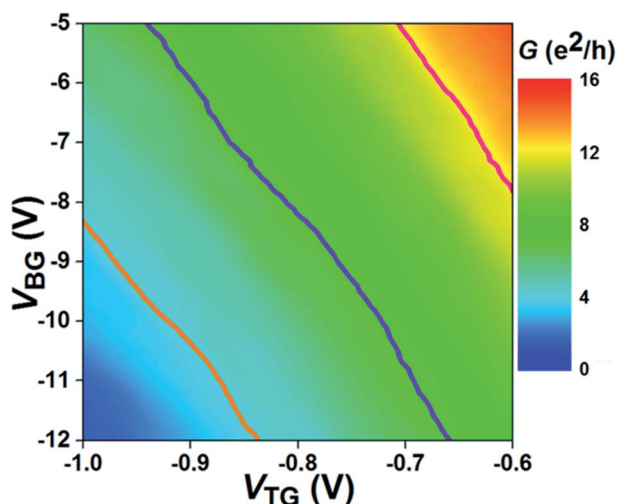


Fig. 2 Conductance G measured for Device 1 as a function of bottom-gate voltage V_{BG} and top-gate voltage V_{TG} at $T = 2$ K. The pink, purple and orange solid lines represent the constant conductance G contours of ~ 12 , ~ 7 and $\sim 4e^2/h$, respectively.



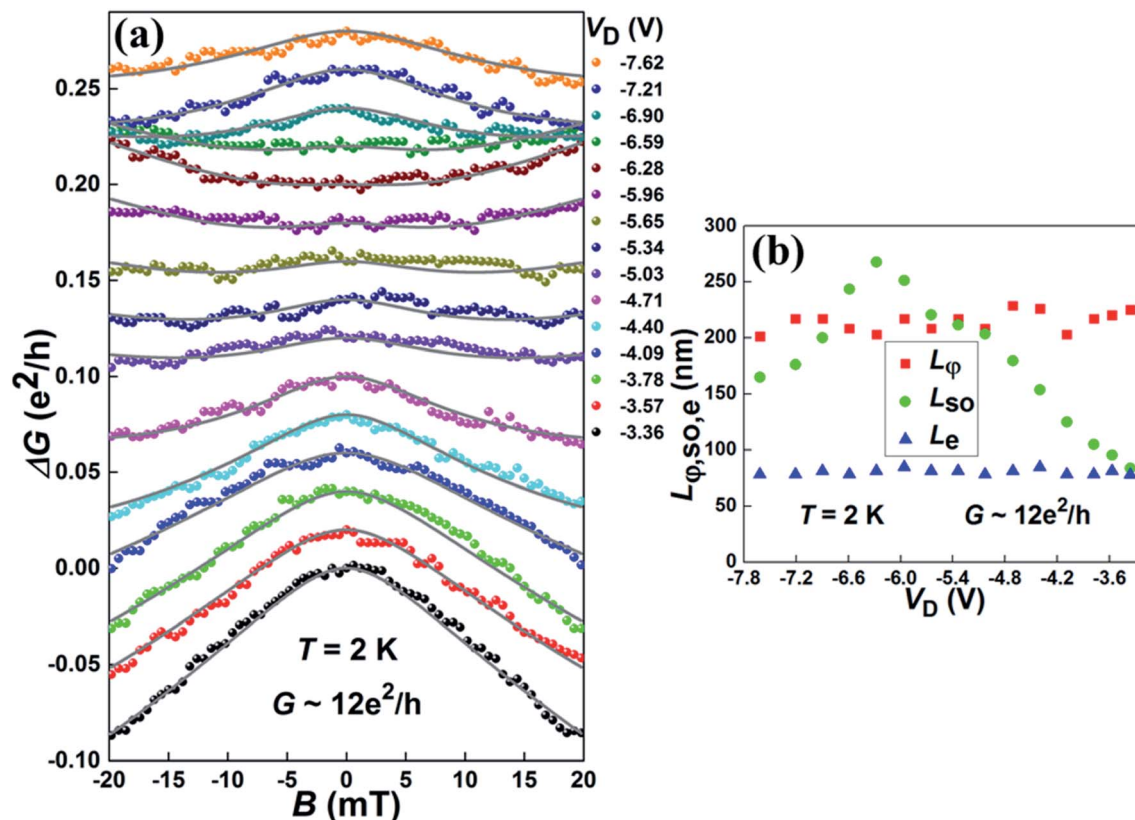


Fig. 3 (a) Low-field magnetoconductance ΔG measured for Device 1 at $T = 2$ K and at dual-gate voltages V_D taken along the contour of $G \sim 12e^2/h$. Here, $\Delta G = G(B) - G(B = 0)$, $V_D = V_{BG} - V_{TG}$, and the applied magnetic field B is perpendicular to the InAs nanosheet plane. The bottom black data points display the measured ΔG at $V_D = -3.36$ V and the ΔG data measured at other values of V_D are successively vertically offset by $0.02e^2/h$ for clarity. The gray solid lines represent the results of the fits of the measured data to the HLN theory [eqn (1)]. (b) Characteristic transport lengths L_ϕ , L_{SO} , and L_e extracted from the fits in (a) as a function of V_D .

~ 82 nm. This value is consistent with the value of ~ 85 nm extracted from the gate transfer characteristic measurements. However, the extracted L_{SO} exhibits a strong dependence on V_D . As V_D decreases from -3.36 V to -6.28 V, L_{SO} increases from ~ 83 nm to ~ 268 nm. As V_D further decreases, L_{SO} turns to decrease and has a value of ~ 165 nm at $V_D = -7.62$ V. A smaller L_{SO} indicates a stronger spin relaxation. Thus, the electrons in the InAs nanosheet of Device 1 exhibit a weak spin relaxation at $V_D = -6.28$ V, but the spin relaxation gets stronger when V_D moves away from the value of $V_D = -6.28$ V. Similar low-field magnetoconductance ΔG measurements have also been carried out for Device 1 along the constant conductance contours of $G \sim 7e^2/h$ and $G \sim 4e^2/h$ at $T = 2$ K (see Section IV and Fig. S4 in the ESI†). These measurements display again the transition from the WAL to the WL and then back to the WAL characteristic with decreasing dual-gate voltage V_D along a constant conductance contour (*i.e.*, a constant carrier density). The extracted spin-orbit length L_{SO} at each constant carrier density again appears to be greatly tunable by dual-gate voltage V_D and exhibits a peak value at which the strength of the SOI is at a minimum value.

Since, in all the above magnetotransport measurements, the tuning of V_D is made along a constant conductance contour and thus approximately at a constant carrier density in the InAs

nanosheet, a change in L_{SO} with decreasing V_D is most probably due to a change in the perpendicular electric field and thus tuning of the Rashba SOI in the InAs nanosheet. To support this, we have simulated the energy band diagrams of the $\text{HfO}_2/\text{InAs}/\text{SiO}_2$ sandwiched layer structure in Device 1 at different dual-gate voltages and carrier densities based on Poisson's equation with the input material parameters of HfO_2 , InAs, and SiO_2 as listed in Table S2 of the ESI† (see Section III in the ESI† for further details). Fig. 4(a) and (b) show, respectively, the simulated bottom conduction-band energies E_C and the extracted out-of-plane electric field distributions in the InAs nanosheet with a carrier density of $n \sim 1.34 \times 10^{12} \text{ cm}^{-2}$ (corresponding to the nanosheet conductance $G \sim 12e^2/h$) for a few experimentally applied dual-gate voltages V_D . It is seen that at $V_D = -3.36$ V, the simulated E_C is strongly downward tilted, leading to the presence of strong, positive, out-of-plane electric fields and thus the presence of strong Rashba SOI in the InAs nanosheet. As V_D decreases, the simulated E_C becomes less downward tilted and thus the out-of-plane field strength is reduced, leading to a decrease in the Rashba SOI strength. At the point when $V_D = -6.28$ V, the simulation gives a flat E_C profile and thus zero electric field inside the InAs nanosheet. As a result, no Rashba SOI should be present in the InAs nanosheet. When continuously decreasing V_D to $V_D = -7.62$ V, the

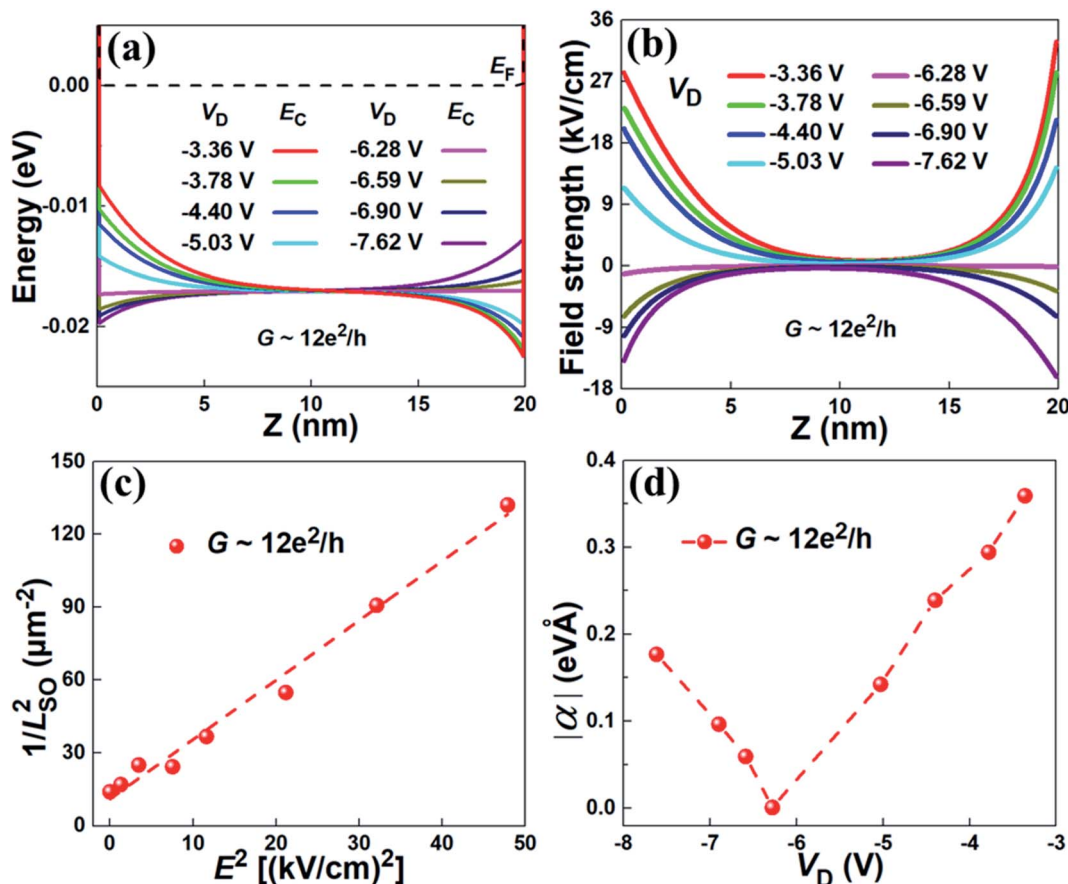


Fig. 4 (a) Simulated conduction band minima E_C in the InAs nanosheet with a fixed value of $n \sim 1.34 \times 10^{12} \text{ cm}^{-2}$ (corresponding to $G \sim 12e^2/h$) for Device 1 at different V_D values. (b) Corresponding effective out-of-plane electric fields in the InAs nanosheet. (c) Extracted $\frac{1}{L_{SO}^2}$ versus evaluated E^2 in the InAs nanosheet. Here, E represents the average out-of-plane electric field strength in the nanosheet and the dashed line represents a linear fit to the data points. (d) Estimated values of the Rashba spin-orbit coupling constant $|\alpha|$ in the InAs nanosheet of Device 1 at a few experimentally applied values of V_D .

simulated E_C becomes upward tilted, leading to the presence of negative, out-of-plane electric fields in the InAs nanosheet and thus a reappearance of the Rashba SOI. Nevertheless, at $V_D = -7.62 \text{ V}$, the field strength in the nanosheet is in general less than that at $V_D = -3.36 \text{ V}$. Thus, the Rashba SOI at $V_D = -7.62 \text{ V}$ is weaker when compared that at $V_D = -3.36 \text{ V}$. The evolution of the Rashba SOI in the InAs nanosheet with decreasing V_D , inferred above from the simulation, is consistent with our observation shown in Fig. 3. Thus, tuning spin-orbit length L_{SO} in the InAs nanosheet from a short value to a long value and then back to a short value originates from tuning the Rashba SOI by the dual gate. Note that in the experiment, at $V_D = -6.28 \text{ V}$, L_{SO} does not go to infinity. This is because a weak, but finite, dual-gate voltage independent SOI term is present in the InAs nanosheet. This term is dominantly of the Dresselhaus type as a result of breaking of bulk inversion symmetry in the InAs compound crystal.

In many cases, it could be convenient to characterize the Rashba SOI strength using the Rashba spin-orbit coupling constant α defined as $\alpha = r_R E$, where E is the average strength of

the out-of-plane electric fields E in the InAs nanosheet and r_R is a material-specific, Fermi-level dependent prefactor found in the Rashba SOI Hamiltonian $H_R = r_R \sigma \cdot k \times E$.²³ Since the Rashba spin-orbit precession length is given by $L_{SO}^R = \frac{\hbar^2}{m^* \alpha} = \frac{\hbar^2}{m^* r_R E}$ (where m^* is the electron effective mass), one can write, to a good approximation, the measured spin-orbit length L_{SO} as $\frac{1}{L_{SO}^2} = \frac{1}{(L_{SO}^R)^2} + C_0 = \left(\frac{m^* r_R}{\hbar^2}\right)^2 E^2 + C_0$, where C_0 takes into account all the electric field-independent contributions to the spin precession in the InAs nanosheet.¹² To estimate the Rashba coupling constant α in the InAs nanosheet, we evaluate the average out-of-plane electric field strengths E at different V_D values from the simulated results shown in Fig. 4(b) and plot the extracted $\frac{1}{L_{SO}^2}$ as a function of E^2 as shown in Fig. 4(c) (data points). The dashed line in Fig. 4(c) represents a linear fit to the data points, whose slope gives a value of $r_R = 51.93 \text{ e nm}^2$. Fig. 4(d) shows the extracted values of $|\alpha|$ at the experimentally applied values of V_D . It is seen that $|\alpha|$ can be

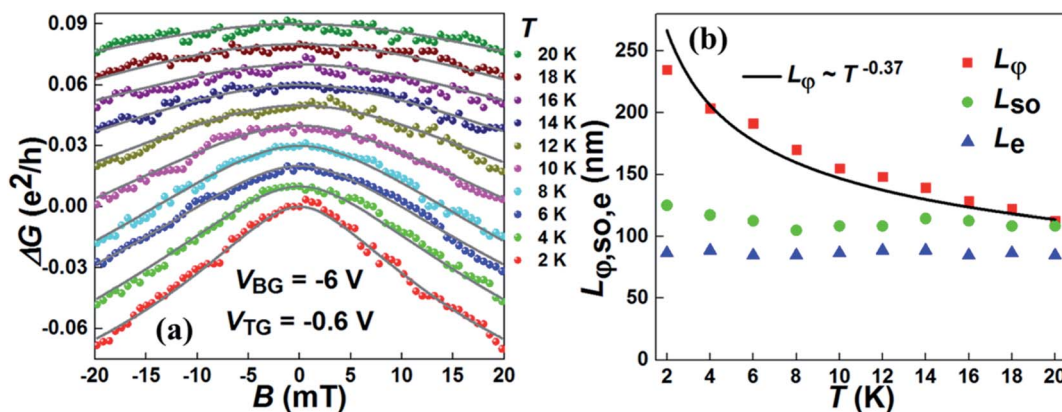


Fig. 5 (a) Low-field magnetoconductance ΔG measured for Device 1 at $V_{BG} = -6$ V and $V_{TG} = -0.6$ V and at different temperatures T . The bottom red data points display the measured ΔG data at $T = 2$ K and the ΔG data measured at other temperatures are successively vertically offset by $0.01e^2/h$ for clarity. The gray solid lines represent the results of the fits of the measured ΔG data to the HLN theory [eqn (1)]. (b) Characteristic transport lengths L_ϕ , L_{SO} , and L_e extracted from the fits in (a) as a function of T . The black solid line is a power-law fit of the extracted values of L_ϕ , showing $L_\phi \sim T^{-0.37}$.

tuned from zero to a value of ~ 0.36 eV Å for the InAs nanosheet in Device 1 at a conductance of $G \sim 12e^2/h$ (with the corresponding carrier density of $n \sim 1.34 \times 10^{12} \text{ cm}^{-2}$ in the nanosheet). Note that $|\alpha|$ is plotted and shown in Fig. 4(d) and in fact the Rashba coupling constant α in the InAs nanosheet of Device 1 can be tuned from a large negative value to a large positive value. A value of $C_0 \sim 11 \text{ } \mu\text{m}^{-2}$ could also be extracted from the fit shown in Fig. 4(c). If we assume that the remaining field-independent contribution to the spin precession is dominantly from the Dresselhaus SOI in the InAs nanosheet, the Dresselhaus spin-orbit precession length can then be extracted as $L_{SO}^D \sim 0.30 \text{ } \mu\text{m}$ at a nanosheet conductance of $G \sim 12e^2/h$. Similar analyses have also been carried out for Device 1 at the conductance values of $G \sim 7e^2/h$ and $G \sim 4e^2/h$, and the same large tunability in the Rashba coupling constant α has been obtained (see Section V and Fig. S5 in the ESI†). In comparison, we note that the tunable Rashba coupling constants of $\alpha \sim 0.01$ – 0.1 eV Å were observed in the InGaAs quantum well and InAs quantum well structures.^{32–35}

Finally, we discuss the influence of temperature on the quantum transport characteristics of the InAs nanosheet device (Device 1). Fig. 5(a) shows the low-field magnetoconductance ΔG measured for Device 1 at $V_{BG} = -6$ V and $V_{TG} = -0.6$ V at temperatures $T = 2$ to 20 K. At $T = 2$ K, the measured ΔG displays clearly a peak-like structure near zero magnetic field, indicating that the electron transport in the nanosheet exhibits dominantly the WAL characteristic. As T increases from 2 to 20 K, the WAL characteristic is gradually suppressed. Again, the measured ΔG data at different temperatures are fitted to the HLN formula [eqn (1)]. The solid lines in Fig. 5(a) represent the fitting results. Fig. 5(b) displays the extracted values of L_ϕ , L_{SO} , and L_e as a function of T from the fits. It is generally seen that both L_{SO} and L_e show a weak temperature dependence. However, L_ϕ is strongly dependent on temperature – it decreases from ~ 235 to ~ 112 nm with increasing T from 2 to 20 K. The extracted L_ϕ data at different temperatures can be fitted by using the power law of $L_\phi \sim T^{-0.37}$ [see the solid line in

Fig. 5(b)]. This $L_\phi \sim T^{-0.37}$ dependence suggests that the dephasing of electrons in the InAs nanosheet is dominantly caused by electron–electron interactions with small energy transfers (the Nyquist scattering mechanism).³⁶ Note that the width of the InAs nanosheet conduction channel in the device is only slightly larger than the extracted phase coherence length ($W \sim 410$ nm vs. $L_\phi \sim 120$ – 230 nm), and the observed $L_\phi \sim T^{-0.37}$ falls in a result lying between the one-dimensional ($T^{-1/3}$) and 2D ($T^{-1/2}$) regime.³⁷

In summary, a dual-gate field-effect device has been made from an epitaxially grown free-standing InAs nanosheet and studied by transport measurements at low temperatures. It is shown that both the carrier density in the InAs nanosheet and the potential difference across the nanosheet can be tuned independently with the use of a dual gate. The magnetoconductance measurements show that the electron transport in the nanosheet can be tuned from the WAL to the WL and then back to the WAL characteristic by sweeping the dual-gate voltage along a constant nanosheet channel conductance contour. Thus, efficient tuning of the Rashba SOI in the InAs nanosheet can be achieved at a constant carrier density. The extracted spin-orbit length at a constant carrier density is found to exhibit a peak value at which the Rashba SOI is suppressed and the spin relaxation by the Dresselhaus SOI can be revealed. Magnetotransport measurements have also been carried out for a dual-gate device made from another InAs nanosheet and similar results are found (see Section VI in the ESI†). We have also performed the energy band diagram simulations for the layer structure of the dual-gate InAs nanosheet device under experimental conditions and the obtained results support our experimental observations.

Author contributions

H. Q. Xu conceived and supervised the project. F. Fan fabricated the devices, performed the transport measurements and carried out the simulations for energy band diagrams with support



from Y. Chen. D. Pan and J. Zhao grew the materials and analyzed the material crystal structures. F. Fan and H. Q. Xu analyzed the transport measurement data and the simulation results. F. Fan and H. Q. Xu wrote the manuscript with contributions from all the authors.

Conflicts of interest

There are no conflicts of interests to declare.

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