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Simple approach for an electron extraction layer in an all-vacuum processed n-i-p perovskite solar cell†

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Vacuum processing is considered to be a promising method allowing the scalable fabrication of perovskite solar cells (PSCs). In vacuum processed PSCs, the n-i-p structure employing organic charge transport layers is less common than the p-i-n structure due to limited options to achieve an efficient electron extraction layer (EEL) on indium tin oxide (ITO) with vacuum thermal evaporation. There are a number of specific applications where an n-i-p structure is required and therefore, it is of interest to have alternative solutions for the n-type contact in vacuum processed PSCs. In this work, we report an efficient vacuum deposited EEL using a mixture of conventional organic small molecules, C₆₀ and bathocuproine (BCP). Incorporation of BCP into C₆₀ does not result in conventional n-doping; however, we observed enhanced charge extraction, which significantly increased the power conversion efficiency (PCE) from 13.1% to 18.1% in all-vacuum processed PSCs. The C₆₀:BCP mixed (co-sublimated) film most likely results in shifted energy levels leading to better alignment with the electrodes.

Introduction

Perovskite solar cells (PSCs) have emerged as a candidate for next generation photovoltaics owing to their rapid increase of power conversion efficiency (PCE) in the last decade, now exceeding 25%.^{1,2} The record efficiency of laboratory-scale PSCs is now higher than that of already commercialized solar cell technologies based on (Cu,In)GaSe₂, CdTe, and amorphous Si and close to that of crystalline Si, and thus considerable interest has been generated for their commercialization.^{3–5} One of the pending issues for the industrial manufacture of

PSCs is to identify a scalable fabrication process for the device stacks. So far, most of the reports on PSCs have shown fabrication with a spin coating-based solution process combined with high temperature annealing, which does not facilitate large area fabrications. A large-area solar module needs all the layers in the photovoltaic device to be deposited with scalable processes. Therefore, PSCs using scalable solution processes, such as blade coating, spray coating, inkjet printing and roll-to-roll printing, are being extensively studied.^{5–11} On the other hand, vacuum processing is one of the promising solutions for upscaling PSCs.^{12–14} Vacuum processing of thin films has several advantages, such as good process stability enabling large area, uniform films, and good control over the film depositions. Therefore, vacuum processed perovskites and all-vacuum processed PSCs have recently attracted considerable attention.^{12–24} In all-vacuum processed PSCs, the p-i-n structure is more commonly used than the n-i-p structure.^{17,18,22–27} This is related to the limited choice of materials for the interfacial layer, or contact layer between the electron extraction layer (EEL) and the electrode. In all-vacuum processed n-i-p devices, organic semiconductors have so far only scarcely been used for the n-interlayer. For example in 2015, Ke *et al.* reported an all-vacuum processed n-i-p PSC demonstrating a PCE of 14.5% using an intrinsic C₆₀ layer contacted with fluorine-doped tin oxide (FTO).²⁸ In 2016 and 2019, our group also reported all-vacuum processed n-i-p PSCs using novel n-type dopants, N1,N4-bis(tri-*p*-tolylphosphoranylidene)benzene-1,4-diamine (PhIm), and pentamethylcyclopentadienyl mesitylene ruthenium dimer, (RuCp*mes)₂, co-evaporated with C₆₀ demonstrating a PCE of 20.3% and 17.7%, respectively.^{29,30} In vacuum processed p-i-n PSCs, undoped C₆₀ is the most widely used material for high efficiency devices. In this structure, the contact between C₆₀ and the top metal electrode is usually optimized with an insertion of interfacial layers such as bathocuproine (BCP), bathophenanthroline (Bphen) or LiF with a top metal layer (e.g., Ag and Al).^{31–33} These interfacial layers, however, do not efficiently work on transparent conducting oxides (TCOs) such as indium tin oxide (ITO), since the mechanism of the improved contact using

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the interfacial layers requires the contact with the metal layers to form a metal complex.^{31–33} Hence, for n-i-p devices different solutions are needed. In particular, it was demonstrated that vacuum deposited CH₃NH₃PbI₃ perovskite films on C₆₀ showed a lower rate of trap assisted recombination than the perovskite film grown on an organic hole transporting material (HTM), (N₄,N₄,N₄'',N₄''-tetra([1,1'-biphenyl]-4-yl)-[1,1':4',1''-terphenyl]-4,4''-diamine, TaTm).³⁴ In addition, vacuum processed PSCs have great potential for tandem application on silicon solar cells with a textured surface.³⁵ A cost efficient and high performing silicon solar cell employs a tunneling oxide passivated contacts (TOPCon) structure, where an n-type silicon substrate with a boron (p⁺) emitter on the textured side has recently attracted much attention in industry.^{36,37} Hence, the vacuum processed n-i-p PSC could be significantly advantageous for the commercialization of high efficiency perovskite/Si tandem solar cells.³⁸ These are the motivations to further develop vacuum processed n-i-p PSCs, as they have potential with high performing devices prepared with scalable processes. However, the previously reported organic n-dopants, PhIm and (RuCp*mes)₂ are not widely used after their first report. Other conventional n-type dopants, such as Rb₂CO₃, Cs₂CO₃ and Ca could be alternative candidates to form an ohmic contact between ITO and C₆₀.³⁸ However, the processing and handling with these dopants is problematic due to their low ionization energy and tendency to decompose in vacuum processes.^{39–41} It is therefore of interest to develop efficient, low-temperature and vacuum-processable EELs for n-i-p structures based on materials that are stable and commercially available.

In this work, we report on all-vacuum processed n-i-p PSCs, using C₆₀ and BCP in the EEL deposited on ITO. We use the co-deposited C₆₀ and BCP, C₆₀:BCP mixed film, as an interfacial layer between ITO and an intrinsic C₆₀ layer. In spite of the energy level distributions of BCP and C₆₀, which do not allow n-type doping, surprisingly, the C₆₀-BCP mixture layer significantly improved the contact between ITO and C₆₀, resulting in an increase of the PCE from 13.1% (without the mixed layer) to 18.1%. We believe that our approach using conventional and vacuum-processable small organic molecules could promote the development of scalable n-i-p PSCs based on stable and low-cost materials.

Results and discussion

Fig. 1 shows the flat band energy diagram for the materials used in our n-i-p PSCs together with the molecular structure of organic semiconductors. Earlier, C₆₀:BCP, or similarly C₆₀:Bphen mixtures have been reported but mainly as an exciton blocking layer in organic photovoltaics (OPVs).^{42–44} However, in low bandgap perovskites, exciton blocking is not needed since free carriers are generated upon photoexcitation.⁴⁵ In addition, it is important to note that the energy levels of C₆₀ and BCP do not allow for conventional n-type doping, where an electron is transferred from the highest occupied molecular orbital (HOMO) of the dopants to the lowest unoccupied molecular orbital (LUMO) of the host. TaTm, 2,2',2''-(1,3,5-benzinetriyl)-tris(1-phenyl-1-*H*-benzimidazole) (TPBi) and molybdenum oxide

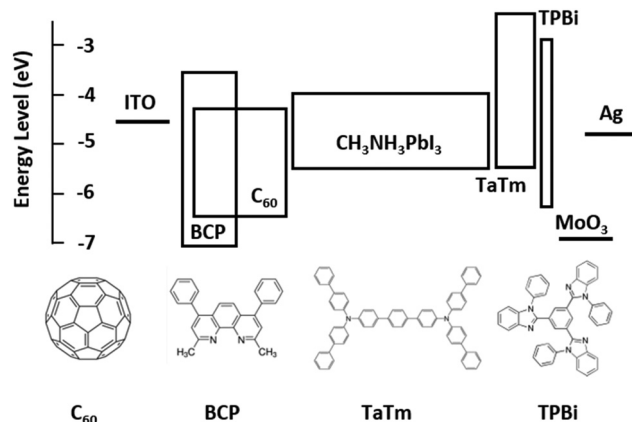


Fig. 1 Energy diagram of the device and molecular structure of the organic materials.

(MoO₃) are used as hole extraction layers (HELs) according to our previous report.⁴⁶

Fig. 2a shows the absorbance spectra of C₆₀ films with different thicknesses, as well as for the co-evaporated C₆₀:BCP mixed layers. C₆₀ shows the expected absorption profile, with a band in between 400 and 550 nm, and higher absorbance at lower wavelengths. As the bandgap of BCP is very large (> 3.5 eV), its optical absorption is negligible in the visible part of the spectrum. C₆₀ is mainly responsible for the visible light absorption, and incorporation of BCP results in a negligible change in the films with 20 nm of C₆₀ (the blue and green lines), and rather decreased in the films with 10 nm of C₆₀ (the red and brown lines). This is originated from low *n* and *k* values of BCP which result in lower absorbance as shown in Fig. S2 (ESI[†]) using the transfer matrix optical model. The overall absorbance with the mixed layers at the given thicknesses is comparable with other typical organic hole extraction layers (HELs) and EELs.^{29,47} Fig. 2b displays the conductivity of the intrinsic C₆₀, the C₆₀:BCP mixed layer, and the mixed film capped with an intrinsic C₆₀ film. The conductivity experiments were done by evaporating the films on interdigitated ITO-coated glass substrates, and by measuring their current density vs. voltage characteristics. Due to the use of ITO as the electrodes, the measurements are affected by the mismatch between the ITO work function and the LUMO of the intrinsic C₆₀ layer. The very low conductivity of the intrinsic C₆₀ layer (~10⁻⁹ to 10⁻⁸ S cm⁻¹) (Fig. 2b) originates from the non-ohmic ITO/C₆₀ interface. Interestingly, however, the conductivity is increased to 4 × 10⁻⁶ S cm⁻¹ for the mixed C₆₀:BCP layer and to 1.5 × 10⁻⁶ S cm⁻¹ when intrinsic C₆₀ is deposited on top of the mixed C₆₀:BCP layer. These values are similar to the previously reported value for amorphous C₆₀ films.^{43,48} As discussed, considering the energy levels of BCP and C₆₀, the observed increase in conductivity is not due to an increase in the conductivity of C₆₀:BCP, but rather is a result of the improved charge injection from the ITO electrode into the organic film.

To evaluate the charge injection/extraction characteristics of the C₆₀:BCP mixed film as the EEL in PSCs, we fabricated *n-i-p* devices in which 20 nm thick films with different ratios of





Fig. 2 (a) Absorbance spectra and (b) conductivity vs. applied voltage of mixed C_{60} :BCP and pure C_{60} films.

Table 1 Photovoltaic parameters extracted from the solar cell characterization shown in Fig. 3b

C_{60} :BCP (ratio)	PCE (%)	V_{OC} (V)	J_{SC} (mA cm^{-2})	FF	R_S ($\Omega \text{ cm}^2$)
0.8 : 1.0	6.7	0.95	13.6	0.52	3.8
1.0 : 1.0	18.1	1.10	21.9	0.75	1.8
2.0 : 1.0	16.4	1.06	21.0	0.74	2.4
2.4 : 1.0	16.2	1.05	19.8	0.78	1.9
C_{60} only (10 nm)	13.1	1.08	15.7	0.66	2.1

R_S : Series resistance.

C_{60} :BCP were deposited. As shown in Fig. 3a, the composition of the C_{60} :BCP mixed films is as follows: C_{60} :BCP = 0.8:1.0, 1.0:1.0, 2.0:1.0 and 2.4:1.0. As demonstrated in Fig. 3b, without the C_{60} :BCP mixed layer (C_{60} only), the current density–voltage (J – V) curve shows a low fill factor and current

density resulting in a PCE of 13.1%. When we introduce BCP in the mixed films (C_{60} :BCP = 0.8:1.0), the solar cell performance is further reduced, leading to a PCE of 6.7%. Interestingly, however, when the C_{60} :BCP ratio in the mixed films is adjusted to 1:1 the PCE is strongly improved (18.1%). Increasing the amount of C_{60} in the mixed films to C_{60} :BCP = 2.0:1.0 and 2.4:1.0, does not further increase the PCE value of the devices. In fact, the PCE decreased to 16.4% and 16.2%, respectively. The mechanism of the enhanced device performance with the mixed layer is not clear. It seems unlikely that an increase in the carrier density due to a redox reaction between the two molecules is the cause as the energy levels do not support such a hypothesis. One potential explanation is that electrostatic interaction between C_{60} and BCP can cause shifts in their respective energy levels (e.g., the LUMO of C_{60}), which enhances



Fig. 3 (a) Device configuration with different composition of the C_{60} :BCP mixture (number in parenthesis represent the layer thickness in nm) and (b) the J – V curve under 100 mW cm^{-2} AM1.5 light illumination.

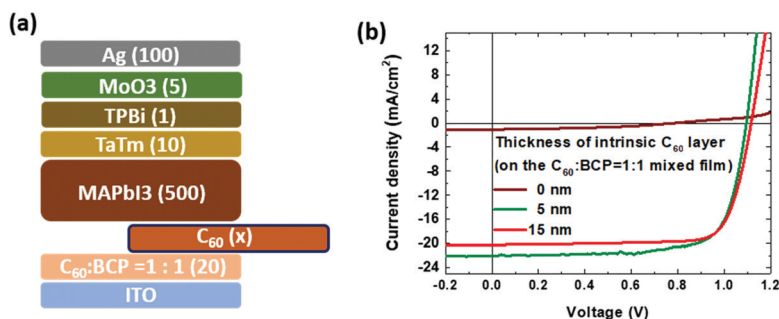


Fig. 4 (a) Device configuration with different thickness of the intrinsic C_{60} layer and (b) the J – V curve under 100 mW cm^{-2} AM1.5 light illumination.





Fig. 5 (a) X-Ray diffraction patterns of $\text{CH}_3\text{NH}_3\text{PbI}_3$ films grown on ITO/C_{60} and $\text{ITO}/\text{C}_{60}:\text{BCP}/\text{C}_{60}$ and (b) photoluminescence quantum yield (PLQY) under different excitation powers.

the injection/extraction property for the ITO contact. This is in line with a recent report that the energy levels of organic semiconductor molecules can be shifted in the mixed phase, including C_{60} , *via* inter molecular electro-static interaction and structural orders.^{49–51}

We also examined the benefits of adding an intrinsic C_{60} layer in between the mixed $\text{C}_{60}:\text{BCP}$ layer for the device performance. The thicknesses of the C_{60} layer are varied from 0 (without the intrinsic layer), to 5 and 10 nm, and deposited on top of the mixed $\text{C}_{60}:\text{BCP}$ layer with the ratio 1.0:1.0. As shown in Fig. 4b, the device without the intrinsic C_{60} layer shows a very low PCE of 0.4%, caused primarily by the extremely low current level. As the thickness of the intrinsic C_{60} layer increases to 15 nm, the V_{OC} is slightly enlarged (1.116 V), as well as the FF (0.78) when compared to the device with a 5 nm intrinsic C_{60} layer, however, the J_{SC} is lowered due to the optical loss by the thick (15 nm) intrinsic C_{60} layer, which dominates and leads to a reduction in the PCE of 17.7%. The key performance data for the solar cells are depicted in Table 1. Therefore, we could confirm that the best condition shown previously, $\text{C}_{60}:\text{BCP} = 1:1$ and 5 nm of intrinsic C_{60} layer is the optimized configuration for the *n-i-p* PSC. As shown in Fig. S3 (ESI[†]), for the device without the C_{60} layer on the $\text{C}_{60}:\text{BCP} = 2:1$ mixed film, the PCE is much higher (15.1%) than the PCE of the 1:1 mixed film. From this result, we thus infer that in the 1:1 mixed film the top surface that is in contact with the perovskite layer is BCP-rich. As the electron affinity of BCP is significantly smaller than that of the perovskite, this leads to an electron extraction barrier leading to charge recombination at the interface between the BCP-rich surface and perovskite layer. This barrier is reduced with increasing C_{60} content in the mixed layer.

To further investigate the origin of the enhanced device performance with the mixed layer, we measured the X-ray diffraction (XRD) and intensity dependent photoluminescence quantum yield (PLQY) of the perovskite films grown on ITO/C_{60} and $\text{ITO}/\text{C}_{60}:\text{BCP}/\text{C}_{60}$, as displayed in Fig. 5 (see ESI[†] for further details). The XRD patterns of $\text{CH}_3\text{NH}_3\text{PbI}_3$ films on $\text{C}_{60}:\text{BCP}$ and $\text{C}_{60}:\text{BCP}/\text{C}_{60}$ are almost identical, as shown in Fig. 5a. Thus, it can be suggested that the improved performance of the device with the mixed layer is not attributed to differed growth of $\text{CH}_3\text{NH}_3\text{PbI}_3$ films. The PLQYs of the perovskite films is also rather similar over a wide range of excitation intensities, from

0.08 to 20 Suns, for both types of interface layers. The similar PLQY values with a similar slope suggest that there are no significant differences in the interface recombination for both films.^{52,53} Hence, based on the datasets discussed above, it can be deduced that the improvement in the device performance using the mixed layer is more likely attributed to the energy level shift *via* electrostatic interaction and structural orders of organic semiconductors as previously reported.^{49–51}

Conclusion

In conclusion, we demonstrated an efficient *n-i-p* PSC prepared using only vacuum deposition methods and employing a novel organic-molecule-based electron extraction layer. The electron extraction layer consists of a mixture of C_{60} and BCP, two well-known and commercially available electron transport molecules. The large band gap of BCP impedes conventional *n*-doping effects of C_{60} ; however, the mixed layer significantly promotes the charge extraction and leads to a PCE increase from 13% to 18%. We believe that the utilization of the organic mixture layer as an interfacial layer, as we demonstrated in this work, will be beneficial for other organic electronic devices.

Conflicts of interest

There are no conflicts to declare.

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