Lab on a Chip

Devices and applications at the micro- and nanoscale

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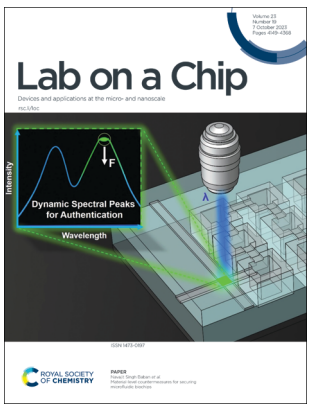

Inside cover
See Navajit Singh Baban *et al.*, pp. 4213–4231.
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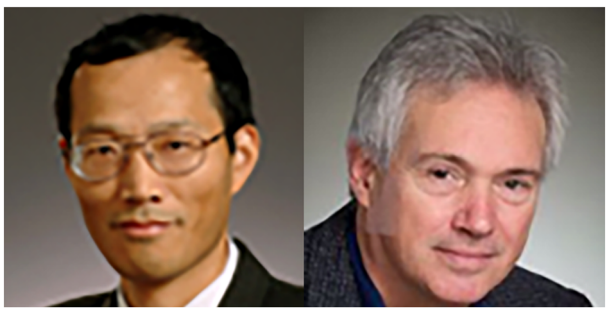

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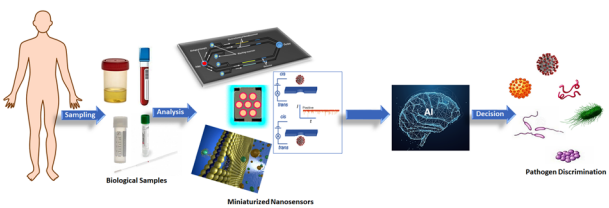

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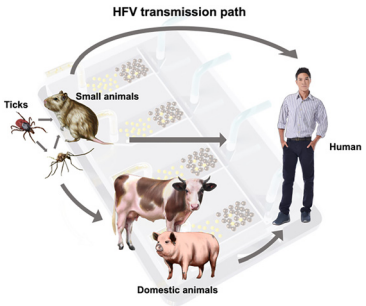

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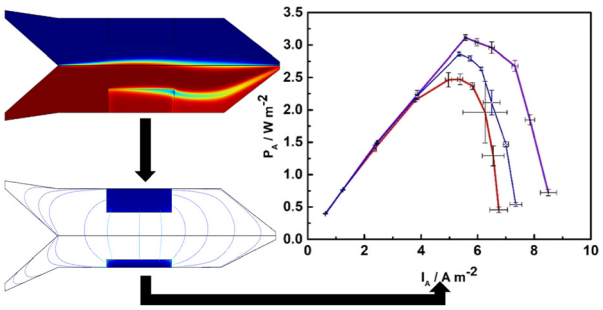

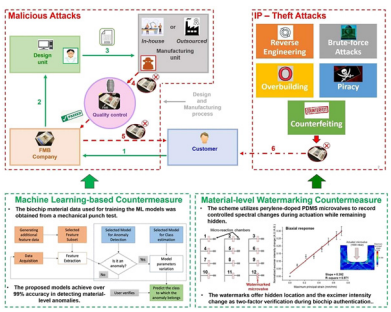

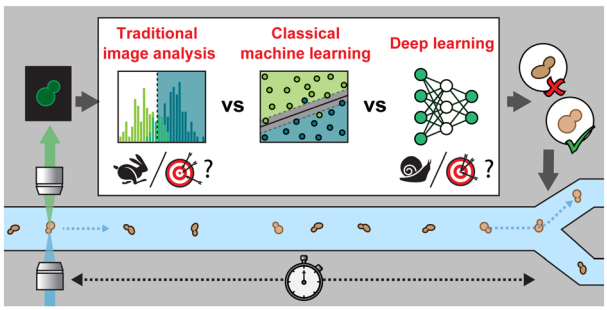

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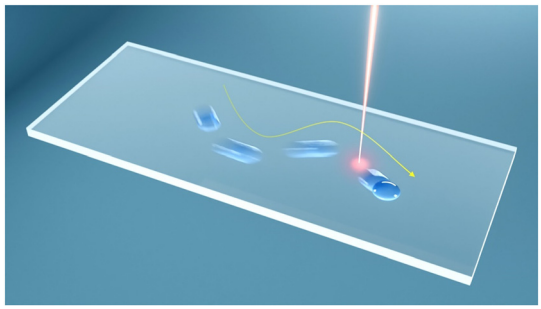

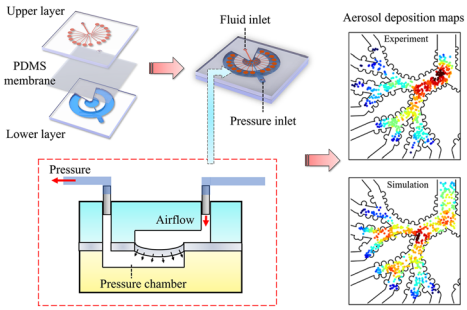

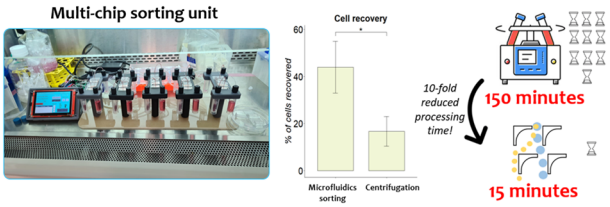

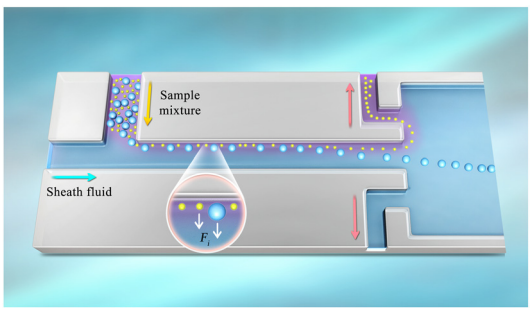

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