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Low-cost and high-performance selenium indoor photovoltaics†

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Selenium (Se), as the world's oldest photovoltaic material, has reemerged as a promising absorber material for indoor photovoltaics (IPVs) due to its suitable wide bandgap of ~ 1.9 eV, nontoxicity, and excellent inherent stability. However, despite the low material cost of Se, conventional high-performance Se photovoltaic devices usually employ high-cost Au as the electrode, significantly increasing the cost of Se-based devices and restricting their future commercialization. Here we replace high-cost Au with low-cost Cu as the electrode, reducing the cost of Se devices by about one order of magnitude. We further introduce an isolation layer of MoO_x between Se and Cu films; this prevents the reaction between Se and Cu to form CuSe during the thermal evaporation of Cu, which significantly lowers the device performance. The resulting Se cells achieve an efficiency of 10.4% under indoor illumination at 500 lux, comparable to that of Au electrode-based Se devices and superior to that of the current IPV industry standard of commercialized amorphous silicon cells with indoor photovoltaic efficiency below 10%. Unencapsulated devices also exhibit negligible efficiency loss after 1000 h of storage under ambient conditions.

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1. Introduction

As the world's first solid-state solar cells fabricated in 1883, selenium (Se) cells initiated modern photovoltaic research.^{1–3} After this, Se was extensively investigated as a photovoltaic material, simultaneously stimulating the broader exploration of photovoltaic materials.^{4–6} These early studies finally resulted in the emergence of silicon (Si) solar cells reported in 1954, laying the foundation for the modern photovoltaic industry.⁷ However, the bandgap of Se (~ 1.9 eV) is too large for single-absorber solar cells, showing a low Shockley–Queisser (S–Q) efficiency limit of $\sim 23\%$ under standard AM1.5G illumination, compared to the suitable bandgap of Si (~ 1.12 eV).^{8–14} The research on Se photovoltaics thereby gradually declined with the rapidly expanding Si photovoltaic industry.

Recently, Se has regained great interest as an ideal candidate for indoor photovoltaics (IPVs) with the recent rapid development of the Internet of Things (IoT), since the absorption spectrum of Se perfectly matches the emission spectra of widely used indoor light sources such as light-emitting diodes (LEDs)

and fluorescent lamps (FLs).^{15–17} This enables that the wide bandgap of Se is just located in the optimum bandgap range from 1.8 to 1.9 eV for IPVs, endowing a high indoor S–Q limit of above 55% for Se IPVs under indoor illumination.¹⁵ Se photovoltaic cells have recently achieved an impressive power conversion efficiency (PCE) of 15.1% under indoor illumination at 1000 lux;¹⁵ the resulting Se modules have successfully powered a radio-frequency identification-based localization tag under indoor light, realizing a self-powered system with no need to charge or replace batteries.¹⁵ Furthermore, the nontoxicity, earth abundance, and intrinsic environmental stability of Se make it ideal for IPV applications.

Besides, Se possesses an advantage of low material cost. The price of elemental Se is as low as 33 \$ per kg according to the SHMET website.¹⁸ This means that the material cost of a Se film is only about 0.32 \$ per m² with a thickness of 2 μm , which is thick enough for the absorption of incident light considering its high absorption coefficient of above 10^5 cm⁻¹.¹⁷ Furthermore, the impressively high vapor pressure of Se arising from its low melting point of 217 °C makes it ideal for thermal evaporation deposition for fabricating high-quality Se films,¹⁷ a high-throughput, low manufacturing cost, and reliable deposition method that has been successfully applied in the industrial manufacturing of Se thin-film photodetectors.¹⁹ However, the current most high-efficiency Se devices usually rely on the use of costly noble metal electrodes such as Au electrodes.^{9,13,15} This suggests that the high cost of metal electrodes would be a critical concern for the future commercialization of Se IPVs. Hence,

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replacing the high-cost Au electrode with low-cost electrode materials is imperative for the future commercialization of Se IPVs.

Here we replace the high-cost Au electrode widely used in conventional high-performance Se photovoltaic devices with low-cost Cu, reducing the cost of Se devices by about one order of magnitude. We find that the thermally evaporated Cu reacts with Se to form CuSe at the surface of the Se/Cu device during the thermal evaporation of Cu. This significantly lowers the device performance. We thereby add an isolation layer of MoO_x between Se and Cu films to avoid the direct contact of Se and Cu and prevent their reaction. The resulting Se cells display a PCE of 10.4% under indoor illumination at 500 lux; this is comparable to that of Au electrode-based Se devices and superior to that of the current IPV industry standard of commercialized amorphous silicon cells with indoor photovoltaic efficiency below 10%. Unencapsulated devices also exhibit negligible efficiency loss after 1000 h of storage under ambient conditions, demonstrating the durable isolation effect of the MoO_x layer.

2. Experimental section

2.1 Photovoltaic device fabrication

FTO-coated glass served as the substrate for depositing devices and was cleaned in ultrasonic baths containing detergent, acetone, and isopropanol for 20 min each in sequence. The substrates were blow-dried using highly pure nitrogen, followed by the UV-ozone treatment for 10 min. A TiO₂ buffer layer was deposited on FTO glass *via* spray pyrolysis, and was placed on a hotplate at 450 °C in an air atmosphere, using a mixture of titanium diisopropoxide bis(acetylacetonate) (75% weight% in isopropanol, Alladin) and absolute ethanol in a volume ratio of 1:9 as a precursor. The film was annealed at 500 °C for 30 minutes and then cooled down naturally. The modification layer (Te), absorption layer (Se), and isolation layer (MoO_x) (99.999%, Alfa Aesar) were then deposited onto an FTO/TiO₂ substrate by thermal evaporation (Beijing Technol Science) under a vacuum pressure of 8×10^{-4} Pa with a thickness of 1 nm, 2 μm and 15 nm (or none as the control device), respectively. The addition of a Te layer not only provides the interfacial adhesion effect between Se and TiO₂ layers but also passivates their interfacial defect states.¹⁵ The devices were then annealed at 200 °C for 2 min on a preheated hotplate under ambient conditions, where the temperature was monitored by using a Hioki memory HiLogger LR8431-30. Finally, the devices were completed with Au or Cu back-contact electrodes (80 nm) deposited by thermal evaporation (Beijing Technol Science) under a vacuum pressure of 8×10^{-4} Pa, and a shadow mask was used to make sure the active area was 0.031 cm².

2.2 Materials characterization

SEM images and EDS mapping were obtained using a Hitachi S-4800 microscope. XRD patterns were collected by using a Rigaku D/Max-2500 diffractometer with Cu Kα₁ radiation ($\lambda = 1.54056$ Å). Raman spectra were obtained using a Horiba JobinYvon, LabRAM HR800 with 532 nm laser excitation. XPS

measurements were recorded using an ESCALab220i-XL electron spectrometer (VG Scientific) equipped with 300 W Al Kα radiation.

2.3 Device performance characterization

J-V measurements were recorded in an air environment using an AM1.5G solar simulator (Newport, USA) with a Keithley 2420 source meter and a 450 W xenon lamp (OSRAM), and a National Renewable Energy Laboratory (NREL)-certified silicon solar cell with a KG-2 filter was used to modulate the light intensity to approach AM1.5G light (100 mW cm⁻²). The scan rate of the devices was 0.1 V s⁻¹ (-1 to 1 V). An aperture shade mask (0.031 cm⁻²) was placed in front of the glass side of the solar cell during the test to eliminate the edge effects and determine the active area. EQE spectra were recorded using a measurement system (QE-R3011, Enli Technology Co. Ltd, Taiwan). The device was stored in air at room temperature with a relative humidity of 50% to 80% for 1000 h during the stability testing. The illumination, emission spectrum, and light intensity of a warm white 2700 K LED (Osram) used for indoor lighting were measured by using a high-precision fibre-optics spectrometer (Maya-2000Pro, Ocean Optics) calibrated within 1 year. IPV performance measurements were carried out in a homemade testing box with all black internal components, and equipped with a baffle to eliminate the effects caused by any stray light as previously reported.²⁰

3. Results and discussion

3.1 Material cost analysis of Se photovoltaic devices

We began by discussing the rise of IPVs. The recent fast-growing IoT is a network of internet-connected devices integrated with wireless communication systems that enable the wireless devices to interact with each other by collecting and communicating data, greatly enhancing both automation efficiency and productivity.²¹ About 20 billion IoT devices have been installed during the past ten years (Fig. S1†), leading to the rapid growth of the wireless sensor market (Fig. 1a).²² Considering that most wireless sensors are usually separated from the electrical grid, batteries are currently the dominant portable power supply for IoT sensors.²³ However, charging and replacing batteries inevitably result in additional maintenance cost and the disturbance of data transfer.²² Considering that the majority of IoT wireless devices are located inside buildings, IPVs that harvest and convert indoor light into mobile electrical energy have been recognized as an ideal alternative for the energy supply that either directly powers IoT wireless sensors or charges the batteries, thereby achieving a self-powered system.²⁴⁻²⁹ Hence, the rapid growth of the wireless sensor market will lead to an explosion in the IPV market, reaching approximately US\$1 billion in 2023.²²

The early solar-powered electronic devices were usually concentrated on relatively expensive consumer devices such as calculators and watches (Fig. 1b).^{22,26} The cost of IPVs could be thereby absorbed in those high-price devices. Currently, the IPV-powered devices have been switched to the low-cost IoT

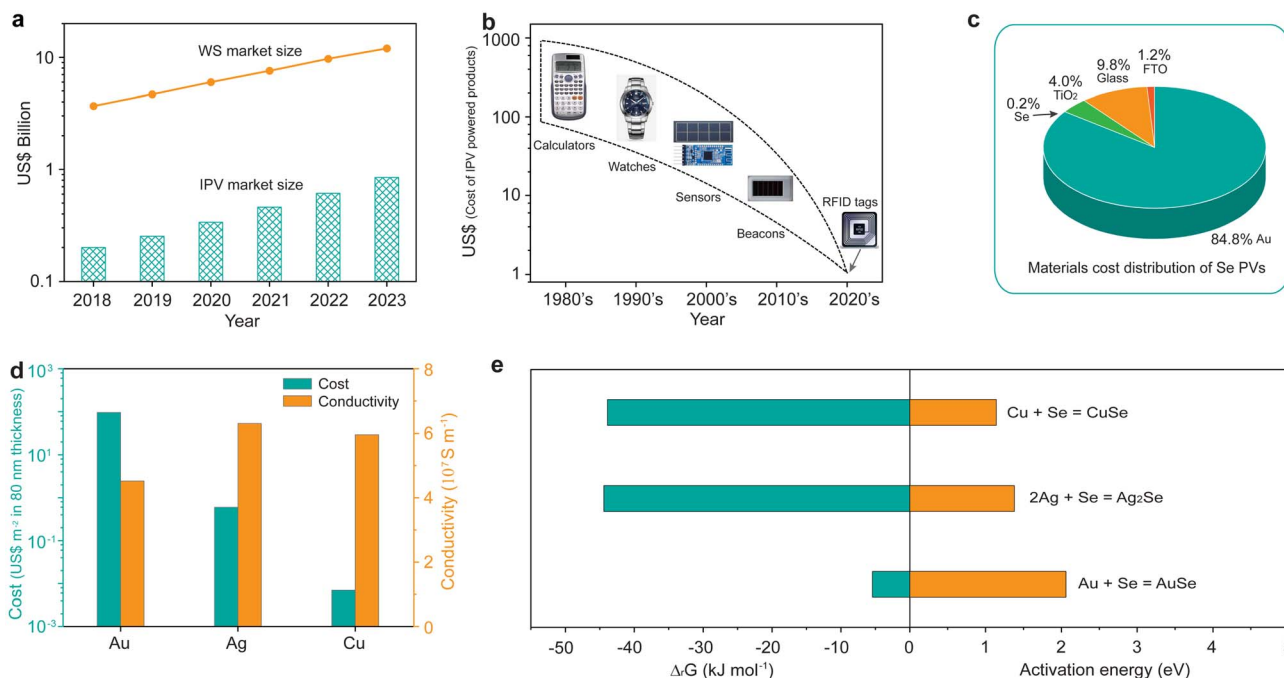


Fig. 1 (a) The projected market size of an indoor photovoltaic and wireless sensor, respectively. (b) A summary of the cost of indoor photo-voltaic-powered electronic devices. (c) Material cost distribution of widely reported Se PVs. (d) Comparison of the material cost per m² for 80 nm thickness and the conductivity of Au, Ag, and Cu. (e) Comparison of the Gibbs free energies and activation energies of the reaction between Se and Au, Ag, and Cu.

devices such as sensors and radio-frequency identification (RFID) tags. This enables the cost of IPVs to be also significantly reduced, matching the low cost of IoT devices. We thereby performed a material cost analysis on the emerging Se IPVs. Fig. 1c shows that the dominant material cost of the widely reported Se PVs is the cost of a Au electrode, accounting for 84.8% of the total material cost. The cost of the absorber material Se only contributes 0.2% to the total cost, which is attributed to the low material cost of Se (Table S1†). The remaining 15% of material costs belong to the cost of TiO₂, glass, and fluorine doped tin oxide (FTO). Replacing the high-cost electrode Au with low-cost electrode materials should thereby be quite necessary to reduce the total cost of Se PVs.

In this regard, we chose copper (Cu), another widely used electrode material, to replace the costly Au electrode due to the following two advantages: (i) the cost of a Cu layer with a thickness of 80 nm is as low as 0.007 US\$ per m², which is significantly lower than that of Ag (0.6 US\$ per m²) and Au (95.7 US\$ per m²);³⁰ (ii) the conductivity of Cu (5.96×10^7 S m⁻¹) is larger than that of Au (4.52×10^7 S m⁻¹), and comparable to that of Ag (6.3×10^7 S m⁻¹), as shown in Fig. 1d. We then calculated the Gibbs free energy of the reaction between Se and Au, Ag, and Cu,³¹ respectively, and found that all the three electrode materials can react with Se based on the results that all the corresponding ΔG values are negative (Fig. 1e). However, there exist activation energies for the three reactions according to previous reports.^{32–34} The activation energy for Cu is only 1.14 eV, whereas it is 2.1 eV for Au, about twice that of Cu. The above results suggest that Cu would be easier to react with Se,

compared to that of Au, thus explaining the widely-used Au as electrode rather than Cu in Se PVs arising from the easy reaction between Se and Cu.

3.2 Addition of an MoO_x isolation layer between Se and Cu layers

We then sought to avoid the reaction between Cu and Se by adding an isolation layer to prevent the direct contact of Se and Cu. We posited that MoO_x as a hole-transport layer (HTL) arising from its suitable energy level with t-Se (Fig. S2†) would also serve as an efficient isolation layer due to its relatively high chemical inertness.^{13,35} Two types of Se thin-film photovoltaic devices were fabricated in a superstrate structure with and without a MoO_x isolation layer under the same fabrication conditions. The devices of glass/FTO/TiO₂/Se/Cu and glass/FTO/TiO₂/Se/MoO_x/Cu are labelled as the Se/Cu device and Se/MoO_x/Cu device (Fig. 2a and b), respectively. The optimal thickness of the MoO_x layer is about 15 nm (Table S2†), which provides a good balance between the high coverage of MoO_x on the Se layer and the low conductivity of MoO_x. From the optical photographs of two such types of devices (Fig. 2c and d), we see that in contrast to the expected yellow colour of the Se/MoO_x/Cu device, the Se/Cu device exhibits a dark blue colour. The different colours of devices with and without a MoO_x layer may be attributed to the isolation effect of the MoO_x layer.

To investigate the Se/Cu interface in these two types of devices, we applied a combination of cross-sectional scanning electron microscopy (SEM) and energy-dispersive X-ray spectroscopy (EDS) elemental mapping to identify the Se and Cu

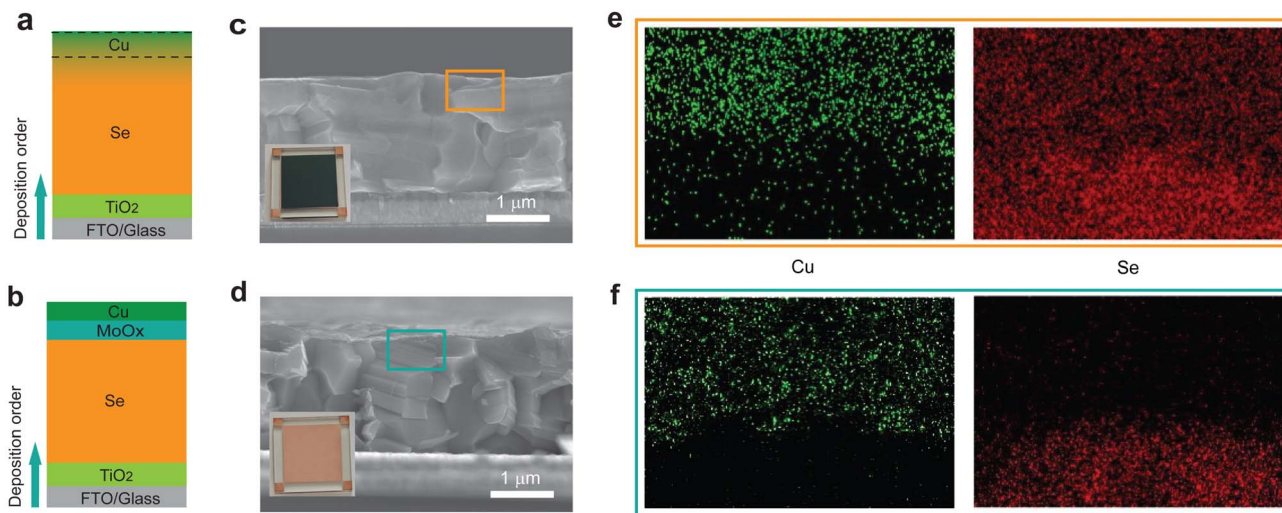


Fig. 2 Schematic configurations of (a) glass/FTO/TiO₂/Se/Cu and (b) glass/FTO/TiO₂/Se/MoO_x/Cu devices. Cross-sectional SEM images and top-view photographs of (c) glass/FTO/TiO₂/Se/Cu and (d) glass/FTO/TiO₂/Se/MoO_x/Cu devices. Energy-dispersive spectroscopy elemental mapping of Cu and Se on the surface of (e) glass/FTO/TiO₂/Se/Cu and (f) glass/FTO/TiO₂/Se/MoO_x/Cu devices.

distribution at the Se/Cu interface. For the Se/Cu device, the absence of distinct demarcation and the obvious overlapped edges of Se/Cu in their spatial distribution maps between Se and Cu layers indicate the diffusion of Cu and Se (Fig. 2e). In contrast, the Se/MoO_x/Cu device shows sharp edges of Cu and Se (Fig. 2f), illustrating the negligible interfacial diffusion at the Cu/Se interface. The above characterization results thereby suggest that the MoO_x layer acts as a diffusion barrier to efficiently prevent the Cu and Se diffusion at the Cu/Se interface.

We next employed X-ray diffraction (XRD) to study the reaction between Se and Cu layers. For the Se/MoO_x/Cu device,

all diffraction peaks expectedly match well with those of trigonal Se (t-Se) (JCPDS 73-0465) and cubic Cu (JCPDS 04-0836) with no other phase impurities, except the peaks of the FTO substrate (Fig. 3a). This indicates that a 15 nm MoO_x isolation layer deposited between Se and Cu layers efficiently prevents the easy reaction between Cu and Se. Furthermore, the XRD pattern of the Se/MoO_x/Cu device stored in an ambient atmosphere at room temperature for 1000 h still shows the diffraction peaks of cubic Cu and t-Se with no other emerging diffraction peaks (Fig. S3[†]), illustrating the durable isolation effect of the MoO_x layer. In contrast, for the XRD pattern of the Se/Cu device, the

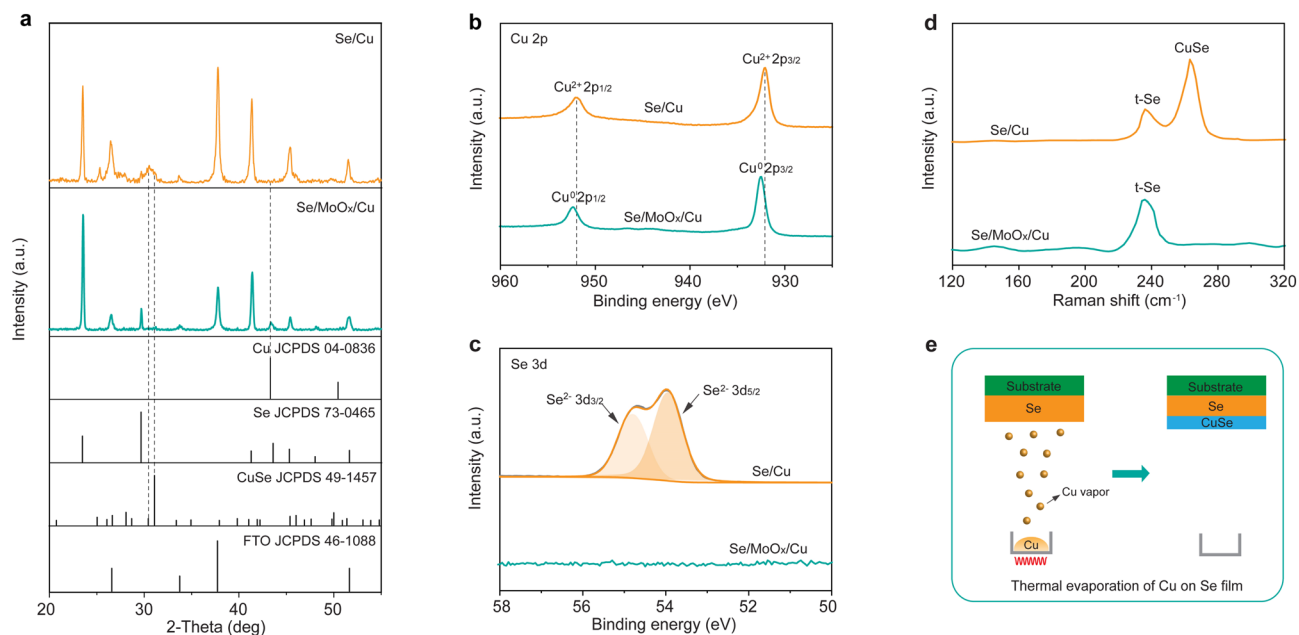


Fig. 3 (a) XRD patterns of Se/Cu and Se/MoO_x/Cu devices. XPS spectra of (b) Cu 2p and (c) Se 3d in the Se/Cu and Se/MoO_x/Cu devices. (d) Raman spectra of Se/Cu and Se/MoO_x/Cu devices. (e) Schematic of the reaction between Cu and Se during the thermal evaporation of Cu atop the Se film.

diffraction peak at 43.3° corresponding to the (111) crystal plane of cubic Cu disappears, while two diffraction peaks at 30.4° and 31.0° appear, which are assigned to the (133) and (006) crystal planes of hexagonal CuSe (JCPDS 49-1457). This suggests the reaction between Se and Cu to form CuSe during the thermal evaporation process of Cu atop the Se film. The above results are consistent with the EDS mapping characterization, confirming that the addition of a MoO_x layer can efficiently prevent the reaction between Se and Cu.

We further applied X-ray photoelectron spectroscopy (XPS) to investigate the element composition and chemical state at the surface of both Se/Cu and Se/ MoO_x /Cu devices. In the magnified Cu 2p XPS spectrum of the Se/ MoO_x /Cu device (Fig. 3b), two peaks located at 932.5 and 952.4 eV correspond to the Cu $2p_{3/2}$ and $2p_{1/2}$ binding energies of elemental Cu^0 , indicating that the existing state of thermally evaporated Cu atop the Se film is elemental Cu. From the magnified XPS spectrum from 50 to 58 eV (Fig. 3c), we found that Se is not detected in the Se/ MoO_x /Cu device. Considering the shallow probing depth of XPS (about 10 nm), this thereby indicates that Se doesn't exist at the surface of the Se/ MoO_x /Cu device. In contrast, we observed that the Cu 2p peaks shift toward a lower binding energy to 932.1 and 952.0 eV when there is no addition of a MoO_x layer. This is consistent with the binding energy in CuSe,³⁶ indicating that the thermally evaporated Cu reacts with Se to form CuSe at the surface of the Se/Cu device. No other possible Cu chemical states such as Cu^0 and Cu^+ are detected in the XPS spectrum of the Se/Cu device considering the perfect Gaussian-Lorentzian peak fitting of Cu 2p. Accordingly, Se exhibits the expected valence of Se^{2-} in CuSe.³⁶ The above XPS results thereby demonstrate the formation of CuSe in the Se/Cu device, and

confirm the isolation effect of MoO_x , consistent with the XRD results.

Raman spectroscopy was further performed on the Se/Cu and Se/ MoO_x /Cu devices to evaluate the effect of the MoO_x layer. Two Raman peaks located at 236 cm^{-1} and 263 cm^{-1} are observed in the spectrum of the Se/Cu device, which are assigned to the vibrational modes of t-Se and hexagonal CuSe,^{37,38} respectively. This agrees well with the XRD and XPS results. In contrast, there is only one Raman peak at 236 cm^{-1} assigned to the vibrational modes of t-Se with no signal of CuSe in the spectrum of the Se/ MoO_x /Cu device. Besides, the Raman spectra of the Se/ MoO_x /Cu device after long-term storage exhibit the same results, suggesting the durable isolation effect of the MoO_x layer to prevent Se and Cu from forming CuSe (Fig. S4†). The above combined material characterization thereby allows us to depict the schematic of the reaction between Se and Cu during the thermal evaporation of Cu atop the Se film; when the Cu vapor reach the Se film, it reacts with Se to form CuSe at the surface of the Se film (Fig. 3e).

3.3 Photovoltaic device performance

We finally measured the current density–voltage (J - V) characteristics of the Se/Cu and Se/ MoO_x /Cu devices under standard AM1.5G illumination at an intensity of 100 mW cm^{-2} . Fig. 4a shows the J - V curves of the best-performing devices of each type. Compared to the Se/Cu device with a PCE of 3.2% ($V_{oc} = 0.65\text{ V}$, $J_{sc} = 10.1\text{ mA cm}^{-2}$, FF = 48.4%), the champion Se/ MoO_x /Cu device exhibits a much higher PCE of 5.4%, with a corresponding V_{oc} of 0.89 V, J_{sc} of 11.4 mA cm^{-2} , and FF of 53.6%. This low PCE of the Se/Cu device is attributed to the

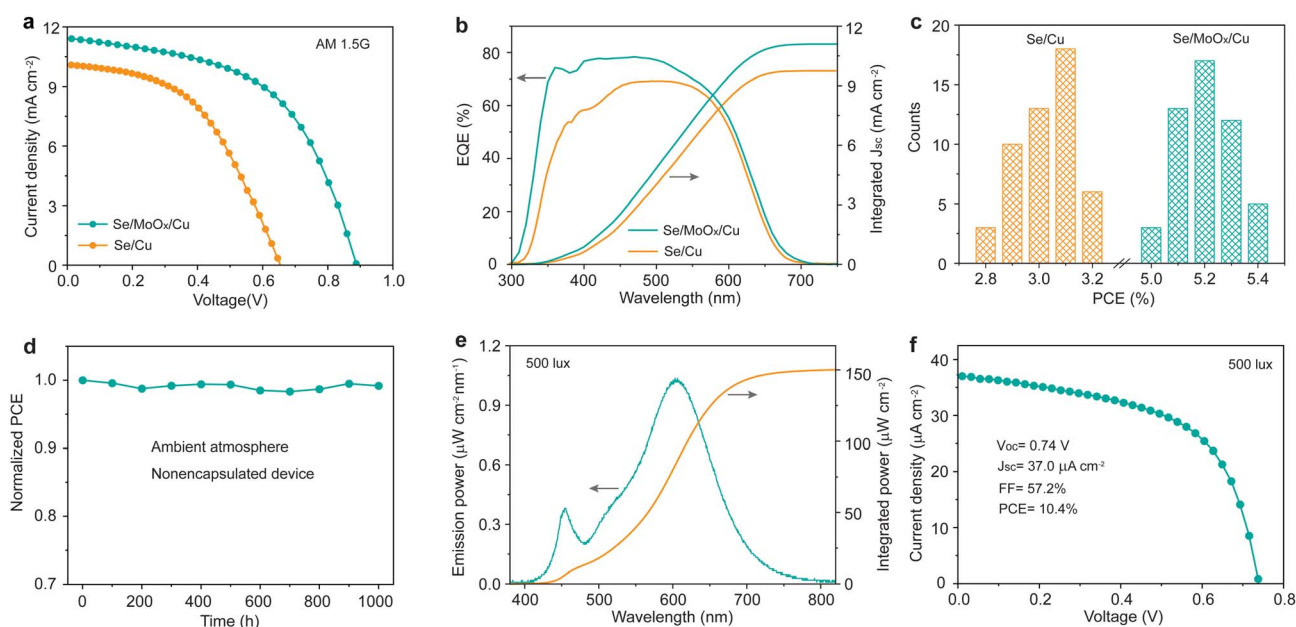


Fig. 4 (a) J - V curves of Se/Cu and Se/ MoO_x /Cu devices under 100 mW cm^{-2} simulated AM1.5G irradiation. (b) EQE spectra of Se/Cu and Se/ MoO_x /Cu devices. (c) Histograms of PCE values for 50 Se/Cu and Se/ MoO_x /Cu devices, respectively. (d) Evolution of normalized PCEs of Se/Cu and Se/ MoO_x /Cu devices stored in the ambient atmosphere at room temperature and a relative humidity of 50% to 80%. (e) Emission power and integrated power spectra of a 2700 K LED at 500 lux. (f) J - V curve of the Se/ MoO_x /Cu device under indoor illumination at 500 lux.

absence of a metal electrode, where the formed CuSe is a typical semiconductor with a bandgap of 1.66 eV.³⁸ This leads to reduced carrier collection and increased carrier recombination, finally resulting in a severe reduction in V_{oc} , J_{sc} , and FF. We then measured the light intensity-dependent V_{oc} of Se/MoO_x/Cu and Se/Cu devices to investigate the recombination. In the equation $V_{oc} = (nk_B T/q) \ln P$, n , k_B , T , and q are the ideality factor, Boltzmann constant, temperature and elementary charge. Fig. S5† shows that the values of n decrease from 1.8 for Se/Cu devices to 1.5 for Se/MoO_x/Cu devices, indicating that trap-assisted recombination is effectively suppressed due to the addition of a MoO_x isolation layer. Notably, the performance of Se/MoO_x/Cu devices is comparable to that of the conventional well-performing Se/Au devices with a PCE of 5.6% ($V_{oc} = 0.89$ V, $J_{sc} = 11.5$ mA cm⁻², FF = 55.0%) (Fig. S6†). Compared to the slightly improved performance from Se/Au devices to Se/MoO_x/Au devices, the significantly enhanced performance from Se/Cu devices to Se/MoO_x/Cu devices is mainly attributed to the isolation effect of the MoO_x layer (Table S3†). The cost of the Se/MoO_x/Cu device is only about one-tenth that of the Se/Au device arising from the high-cost Au electrode, dramatically reducing the material cost of Se photovoltaic cells.

External quantum efficiency (EQE) measurement was subsequently performed on such devices (Fig. 4b). The integrated current densities calculated from EQE spectra match well with the J_{sc} values measured from J - V characterization (within 5% deviation). Particularly, the Se/MoO_x/Cu device exhibits an obviously higher EQE in the 300–500 nm wavelength region compared to that of the Se/Cu device, indicating that the Se/MoO_x/Cu device possesses lower recombination loss of photogenerated carriers. Fig. 4c shows the statistical photovoltaic performance of 50 devices of both types. The efficiency histograms demonstrate the good repeatability and reliability of the performance improvement by the addition of a MoO_x layer between Se and Cu films. The long-term stability test of the unencapsulated Se/MoO_x/Cu device was further performed in an air environment at room temperature with a relative humidity of 50% to 80%. It is shown that there is negligible PCE loss of the device after 1000 h of storage (Fig. 4d). The high stability of Se devices thereby demonstrates the long-term stability and durable isolation effect of the MoO_x layer, in good agreement with the XRD results.

Considering the suitable wide bandgap of Se for indoor light harvesting, we then measured the IPV performance of the Se/MoO_x/Cu device under indoor illumination at 500 lux using a common warm white 2700 K LED as the light source, where the 500 lux illumination is the most frequent value in most indoor environments such as offices, classrooms, libraries, and shopping malls, based on the CIE/ISO standard for lighting design of buildings (CIE S 008/E-2001).²⁰ The corresponding input light intensity of the LED at 500 lux was measured to be 150.1 μW cm⁻² (Fig. 4e). All IPV performance measurements were carried out in a homemade testing box with black internal components to prevent the interference of stray light, according to a reliable measurement method for IPV reported by the Hou group.^{20,23} The Se/MoO_x/Cu device displays a PCE of 10.4% with a corresponding V_{oc} of 0.74 V, J_{sc} of 37.0 μA cm⁻², and FF of

57.2% under indoor illumination at 500 lux (Fig. 4f), comparable to that of the Se/Au devices (Fig. S7†). The integrated current density of the device closely matches the J_{sc} value measured by J - V characterization under 500 lux indoor illumination (Fig. S8†). The IPV performance of such Se devices is higher than that of commercialized amorphous silicon cells with an indoor PCE below 10% (Table S4†), which is the current IPV industry standard.²² The advantage of low cost combined with a high IPV performance thereby guarantees the future commercialization of Se IPV for powering IoT wireless sensors.

4. Conclusion

In summary, we substitute low-cost Cu material for the widely used Au electrode in conventional high-performance Se photovoltaic devices, significantly reducing the cost of Se devices by about one order of magnitude. We further introduce an isolation layer of MoO_x between Se and Cu films, preventing the direct contact of Se and Cu and avoiding the reaction between Cu and Se; the CuSe formed during the thermal evaporation of Cu dramatically lowers the device performance. The MoO_x isolated Cu electrode-based Se devices exhibit a PCE of 10.4% under indoor illumination at 500 lux. This performance is comparable to that of Au electrode-based Se devices, outperforming the present IPV industry standard of amorphous silicon cells with an indoor photovoltaic efficiency below 10%. The unencapsulated devices also show no efficiency loss when stored in the ambient atmosphere for 1000 h, demonstrating the excellent stability of such Se photovoltaic devices. This work thereby provides a cost-effective fabrication of high-performance and stable Se devices, accelerating the commercialization of Se IPV.

Author contributions

Zhouqing Wei: conceptualization, methodology, data curation, and writing – original draft; Wenbo Lu: data curation and investigation; Zongbao Li: data curation and investigation; Mingjie Feng: investigation; Bin Yan: investigation; Jin-Song Hu: writing – review & editing; Ding-Jiang Xue: conceptualization, writing – review & editing, supervision, and funding acquisition.

Conflicts of interest

There are no conflicts to declare.

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