

Lab on a Chip

Devices and applications at the micro- and nanoscale
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ISSN 1473-0197 CODEN LCAHAM 24(5) 987-1530 (2024)



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See Hyunjung Lim, Aram J. Chung *et al.*, pp. 1229–1261.
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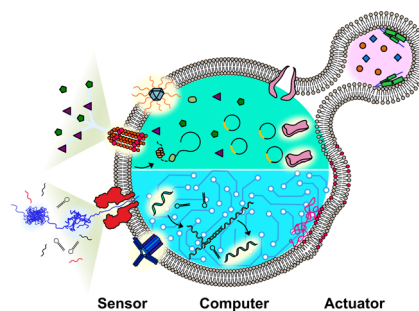
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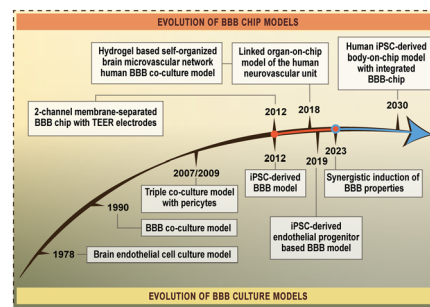
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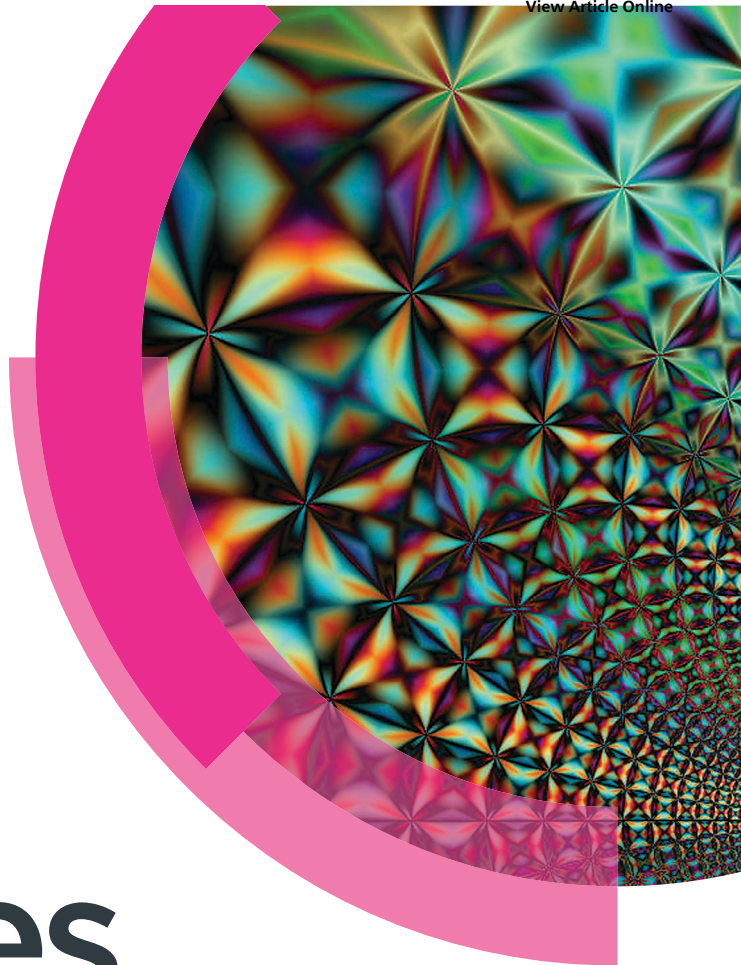


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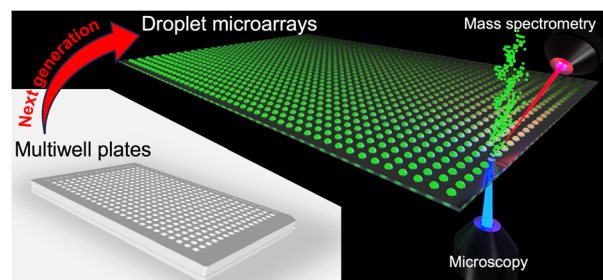


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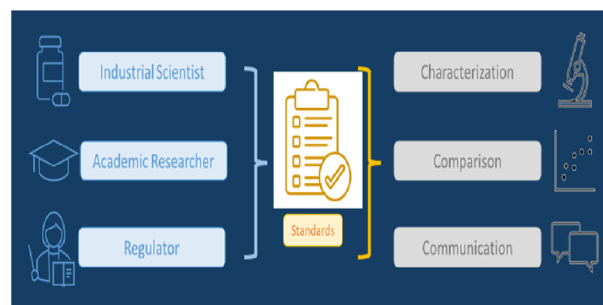
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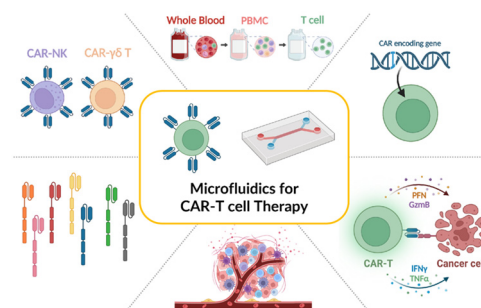


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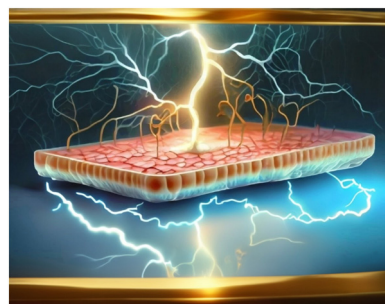
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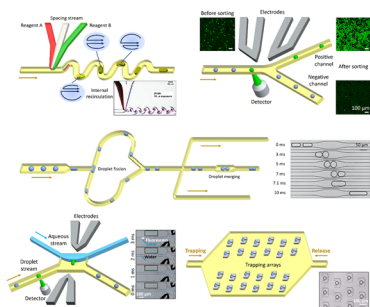
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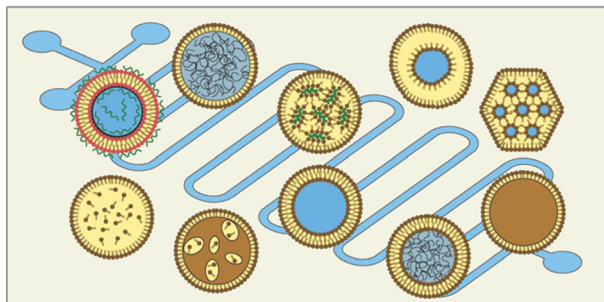
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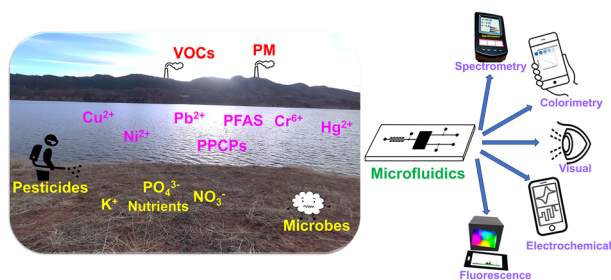
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Sima Mehraji and Don L. DeVoe*

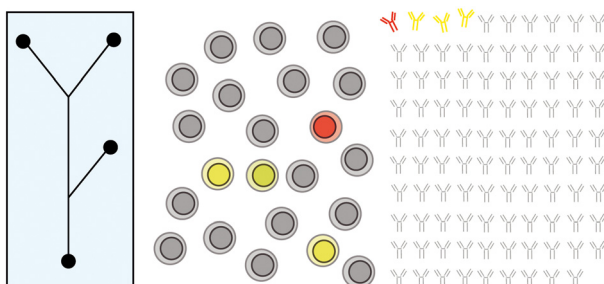
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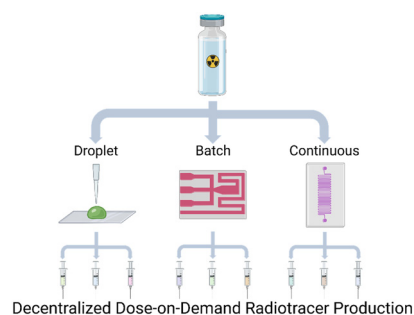


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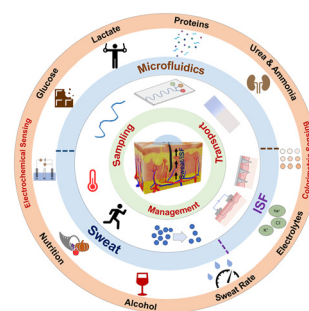
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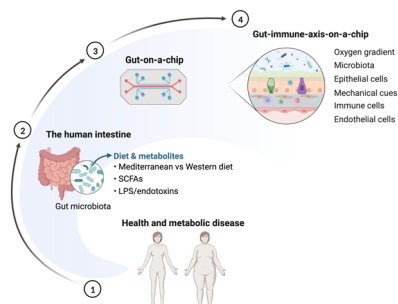
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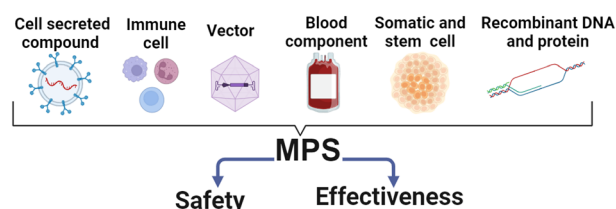
Alexandra E. Wheeler, Verena Stoeger and Róisín M. Owens*



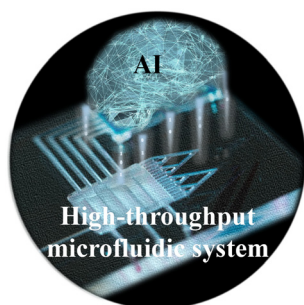
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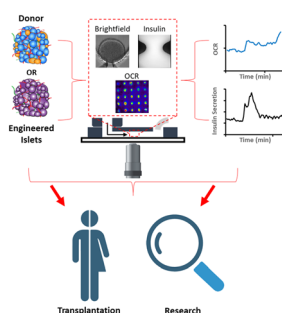
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Jianhua Zhou, Jianpei Dong, Hongwei Hou, Lu Huang* and Jinghong Li*

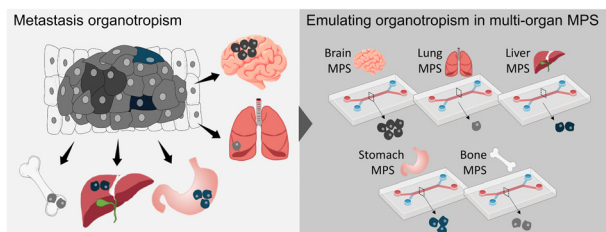
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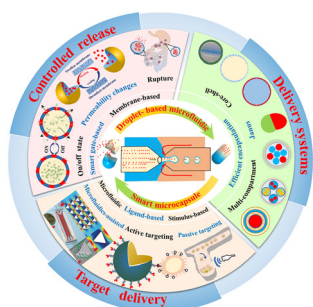
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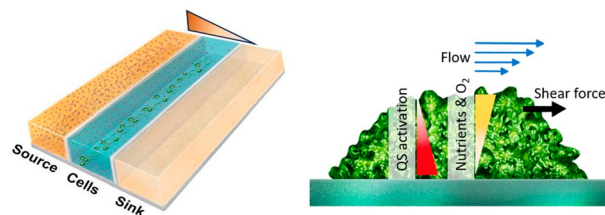


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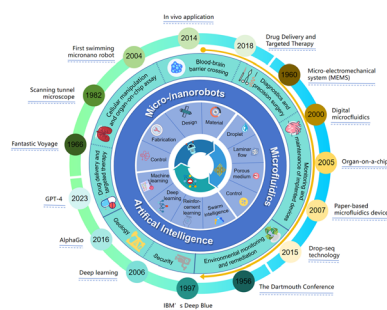


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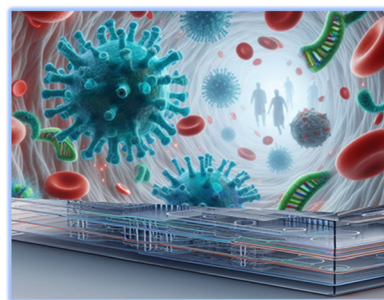
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