

Lab on a Chip

Devices and applications at the micro- and nanoscale
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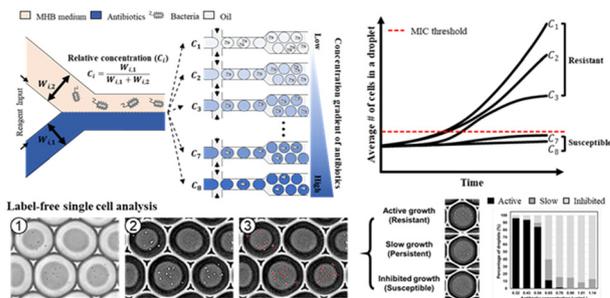
Cover
See Chang-Soo Lee *et al.*,
pp. 5274–5289.
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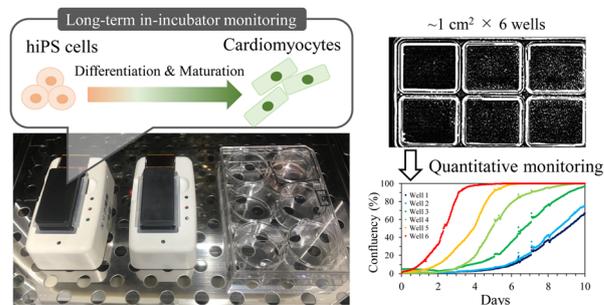
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Taishi Kakizuka, Tohru Natsume and Takeharu Nagai*





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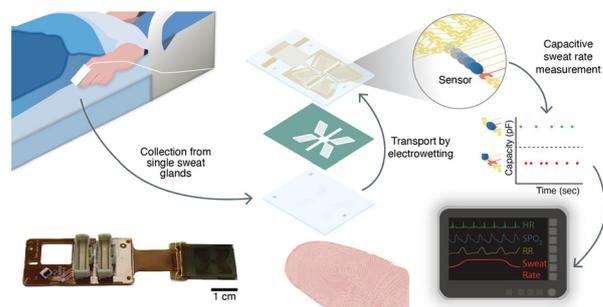
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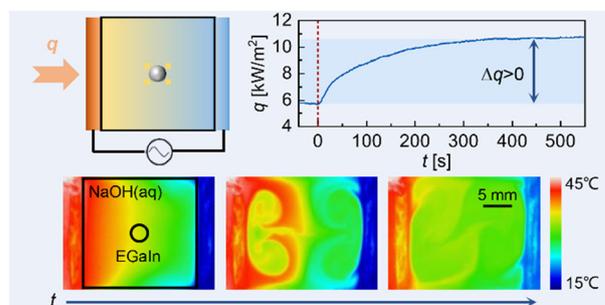
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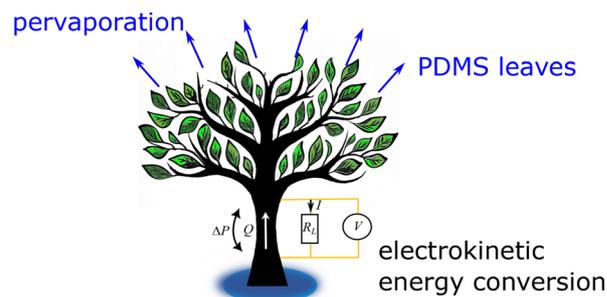
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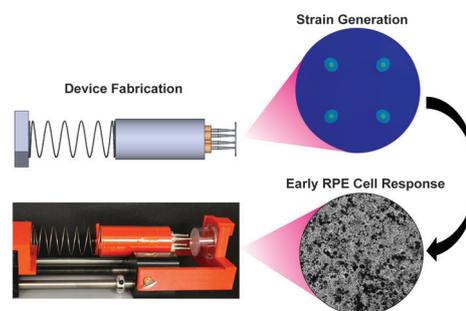
Hrishikesh Pingulkar, Cédric Ayela and Jean-Baptiste Salmon*



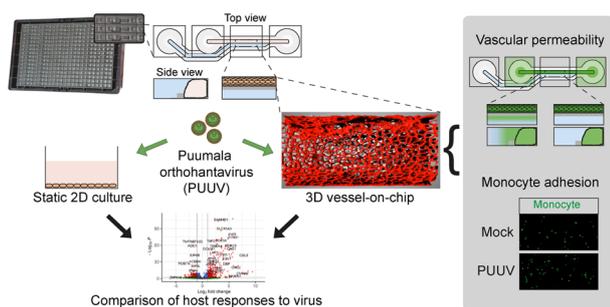
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Chase Paterson and Elizabeth Vargis*



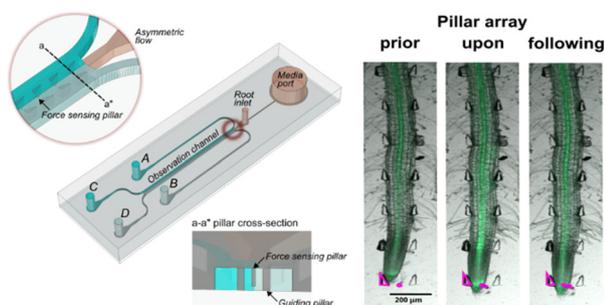
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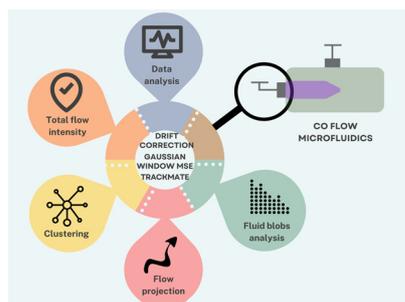
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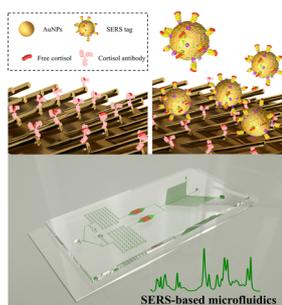
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