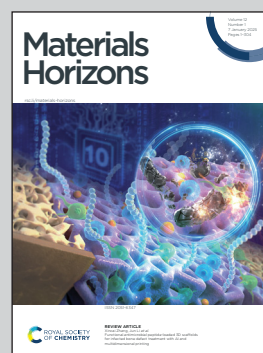


**Showcasing research from Professor Kyung Min Kim's laboratory at the Department of Materials Science and Engineering, KAIST, Daejeon, Republic of Korea.**

Demonstration of a novel majority logic in a memristive crossbar array for in-memory parallel computing

A memristive majority gate characterized by its parallel operation is proposed for the first time, serving as a universal gate and achieving both robust reliability and high efficiency for in-memory computing. This technology demonstrates 8.5x higher efficiency than the previous state-of-the-art memristive logic technology in 64-bit adder operations, representing a significant step toward the commercialization of memristive in-memory computing.

**As featured in:**



See Kyung Min Kim *et al.*,  
*Mater. Horiz.*, 2025, 12, 131.