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Electrical Characterization of Nanocrystal Solids

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Here we provide a primer for correctly selecting and implementing optoelectronic characterization techniques on semiconductor nanocrystal solids and choosing the appropriate models with which to interpret the data. We discuss the two most fundamental device architectures incorporating a solid composed of solution-processed semiconductor nanocrystals: the metal-semiconductor-metal diode and the heterojunction diode, and the broad classes of techniques that can be used to analyse them including quasi-static (e.g. current-voltage), harmonic (e.g. admittance spectroscopy), and transient measurements. We then separately address the models that are commonly applied to understand the semiconductor behaviour including the Shockley diode equation and capacitance-based analyses such as Mott-Schottky and Shockley-Read-Hall models and potential pitfalls in their application. We additionally highlight methods to extract the density and position of trap states, located in the band gap of these semiconductors and which play an important role in device performance. Due to its interest as an absorptive material for third generation solar cells, we discuss the techniques and analysis in the context of PbS nanocrystal solids and provide reference values for this material throughout the text.

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I. Introduction

The impressive advances in colloidal chemistry in the past twenty years have enabled the advent of solution-processed semiconductors assembled from preformed nanocrystals (NCs).¹ These semiconductors not only offer the possibility for low cost manufacturing and processing on flexible substrates, but also enable the integration of quantum-confined materials with their unique electronic and optical properties into large-area devices. These features make nanocrystal-based solids particularly attractive for optoelectronic applications, such as LEDs², photodetectors,³ and solar cells.^{4–7}

However, NC-based semiconductors present challenges for electric characterization. The disordered nature of the nanocrystal solid, the large surface area of the nanocrystals, and their hybrid nature with inorganic cores and organic ligands, introduce large numbers of electronic trap states, a broad window of characteristic times scales for charge carrier dynamics, and often a mix of localized and band-like transport.⁸ Nonetheless, many of the characterization techniques and models developed over the past fifty years to understand transport in crystalline and polycrystalline materials can be applied to understanding the electronic properties of nanocrystal solids. This must be done carefully, with a clear understanding of the limitations of the measurement techniques and the assumptions behind the models. Here we aim to provide a compact description of optoelectronic measurement techniques and common pitfalls facing their application to nanocrystal solids. In particular, we emphasize the importance of distinguishing between the physical measurement and the application of models to interpret the measurement results.

II. Devices and Measurement Techniques

Device Architectures

While a variety of different architectures have been conceived for nanocrystal-based solar cells,⁶ here we focus on the two most fundamental: the metal-semiconductor-metal (MSM) diode and the heterojunction (HJ) diode.

The simplest device to characterize the photovoltaic and electronic properties of a semiconductor is the metalsemiconductor-metal (MSM) diode. As depicted in Figure 1a, in this device architecture, a metal with high workfunction (ϕ_p) is used to extract holes while a metal with a low workfunction (ϕ_n) is used to extract electrons. Like all solar cells, the MSM diode derives its photovoltaic effect from the carrier-type selectivity of these contacts.⁹ This selectivity is primarily provided by the built-in field, which stems from the difference in workfunction between the two contacts: $\phi_p - \phi_n$.

Due to the presence of the metal-semiconductor interfaces, NCbased MSM diodes are often referred to as "Schottky-diodes" in reference to the applicability of the Schottky-model to crystalline semiconductor-based MSM-diodes.^{10,11} However, it is unclear if the charge transport physics in the NC-based MSM-diode is related to the majority-carrier Schottky-model.

NC-based MSM-diodes are advantageous over recordperforming NC-based devices for characterizing the physical properties of a NC-solid due to their ease of fabrication and the fact that they only contain one semiconducting material.¹¹⁻¹⁶ However, a disadvantage is that the low-workfunction metals that are used as electrodes, such as Al, Mg, Ca, are easily oxidized and hydrolized,¹⁷ and the metal-semiconductor interface is sensitive to this degradation.

It has been shown that the properties of the metal electrodes can be improved by introducing thin metal-oxide or salt layers. For the hole-extracting electrode, transition metal oxide layers, such as MoO_3 and VaO_x , ~10 nm in thickness, have improved



Figure 1: a) Schematics of the two fundamental device architectures for NC solar cells: the MSM-diode and the HJ-diode. Common materials for each layer are indicated. b) Band alignment under open circuit conditions for MSM-diode (left) and the HJ-diode (right) in the dark (top) and under illumination (bottom). In the dark, parts of the diode are depleted and an electric field is present, as indicated by the slope of the vacuum energy $E_{\rm vac}$. Under illumination, the quasi-Fermi energies for electrons and holes split and establish a chemical potential, which is the source of the open-circuit voltage. No electric field is present at open-circuit condition, as the free charge carriers screen the electric field. c) Schematic of a generic optoelectronic characterization system. The source-measure-unit (SMU) applies a voltage to the diode ($V_{\rm D}$) and measures the current ($J_{\rm D}$). The diode is in a cryostat that controls the temperature (T) and the atmosphere. Depending on the type of measurement, illumination ($I_{\rm ph}(\lambda)$) is provided by a broad-band or monochromatic light source.

performance.^{18–20} On the electron-extracting electrode, LiF (~1.0nm) is used to improve the open-circuit voltage and the stability under ambient conditions.^{15,17} In the bulk, MoO₃ and LiF are wide band gap insulators and would be expected to impede charge transport. As thin layers, it is suspected that they pin the Fermi-energy at the semiconductor-metal interface at an advantageous position that increases the carrier selectivity of the electrode, while allowing charge extraction through trap state mediated transport.^{17–19} Additionally, the metal-oxide interlayers can act as charge carrier blocking layers and physical buffer layers, further enhancing solar cell performance.¹⁸ An excellent review on this topic was recently published by Greiner and Lu.²¹

The current record power conversion efficiency for a MSM solar cell with a PbS NC-solid is 5.2% and 4.6% for a PbSe NC-solid.^{22,16} The efficiency of this architecture is limited by short charge carrier diffusion lengths, which restrict the maximum film thickness to 100 - 200nm.⁵ Although some reports claim that the MSM-diode has intrinsically low opencircuit voltages and fill factors, there exist several reports with open-circuit voltages and fill factors on par with record performing devices.²³⁻²⁵

As shown in Figure 1a, the heterojunction (HJ) diode consists of an n-type semiconductor and a p-type semiconductor, which are ohmically contacted to a low-workfunction and a highworkfunction metal respectively. The advantage of the HJ over the MSM is the improved spatial separation of charge carriers, which reduces recombination losses. This comes at the cost of increased complexity in device fabrication and characterization.

In a typical HJ diode, an n-type metal oxide (TiO₂ or ZnO) is used to extract electrons, and the NC-solid is used as a p-type layer to extract hole carriers. Light absorption takes place predominantly in the PbS NC-solid. Variations to the HJ structure have been reported, including planar HJs^{18,19,26-30} and nano-structured bulk-heterojunctions (BHJs).^{31–36} The currently certified maximum power conversion efficiency of such devices is at 7.0 %.³⁰ In a second variety of HJs, the n-type layer is provided by an n-type NC-solid. Again, planar^{37,38} and BHJ^{39,40} geometries have been reported. Currently, 4.9 % is the highest reported power conversion efficiency in this category.⁴⁰

Measurement Setup

As shown in Figure 1c, for complete physical characterization of a device, a measurement setup must offer: reliable electrical contacts, optical access, accurate temperature control and measurement, and environmental control.

Environmental control is desirable as NC-based semiconductors are highly sensitive to atmospheric oxygen and water

vapour.^{11,17,41,25} Temperature dependent measurements on diodes are used to determine various activation energies in the system. This requires that the electrical contacting mechanism be robust to temperature change.

Regardless of the means for electrical contact, a source-measure-unit (SMU) is needed. This device (or a collection of devices) has four terminals. The *Generator* terminal is connected to a voltage source, which supplies a voltage ($V_{\rm D}$) to the device. The second terminal (*Lo*) is connected to a device that measures the current coming back from the diode. In this review, we consistently refer to the current density ($J_{\rm D}$), where the current is normalized by the characteristic device area to facilitate comparison between different device geometries. The remaining two terminals (*Vhi*, *Vlo*) are connected to a high impedance ($\gg 1$ MOhm) voltage measurement device to measure $V_{\rm D}$.

III. Optoelectronic Characterization Techniques

In this section, we describe optoelectronic measurement techniques that are most relevant for NC-based solar cells and the specifics (e.g. instrumentation) of how these measurements are implemented. For a more complete overview, we recommend *Electrical Characterization of Organic Electronic Materials and Devices*⁴² by Peter Stallinga or *Advanced Characterization Techniques for Thin Film Solar Cells* by Abou-Ras, Kirchhartz and Rau.⁴³

To facilitate discussion, we classify the techniques in three different groups that depend on their temporal requirements for the SMU: (1) quasi-static, (2) harmonic, and (3) transient.

To extract physical parameters from these measurements, the data has to be interpreted in the context of specific models of the device physics, which we describe separately in Section IV. This is done to emphasize the importance of reporting the raw data from these characterization techniques prior to analysis of the data through application of a specific model, which is often borrowed from classical or organic electronics and has not to date been validated for nanocrystal-based devices.

Quasi-static Techniques

The most common and straightforward optoelectronic characterization techniques are quasi-static measurements, in which a constant $V_{\rm D}$ is applied and $J_{\rm D}$ is measured, yielding a current-voltage (IV) curve. These measurements can be performed with a SMU such as the Keithley 2400. Representative data for a MSM diode with a benzenedithiol (BDT)-treated PbS NC-based semiconductor layer are provided in the inset in Figure (2a).^{23,44}

Measuring the IV curve under illumination with a standardized solar spectrum is used to determine the performance parameters of the solar cell: the open-circuit voltage ($V_{\rm oc}$), the short-circuit current-density, ($J_{\rm sc}$), and the power conversion efficiency (η). Due to the interest in assessing the uniformity of the solution-processed semiconductor layer in the different devices on the same substrate or across several different substrates, it is helpful to report uncertainties obtained from a statistically relevant number of devices. Measurement of $V_{\rm oc}$ and $J_{\rm sc}$ for different illumination intensities (Figure 2b), and temperatures (Figure 2c) are performed to obtain information on the charge transport in the diode.^{23,44} In Section IV, we discuss the physical parameters that can be obtained from such measurements by application of the Shockley diode equation.

Harmonic Techniques

The second class of optoelectronic characterization involves application of a frequency dependent voltage or illumination and measurement of the system response.

Admittance Spectroscopy

When the diode is biased at a certain level $V_{\mathrm{D},0}$ with an additional harmonic signal of frequency $f=\omega \ / \ 2\pi$, such that $V_{\mathrm{D}}(\omega)=V_{\mathrm{D},0} + \delta \hat{V}_{_{\mathrm{D}}} \sin \omega$, the response to this excitation is measured as

$$J_{\rm D}(\omega) = J_{{\rm D},0} + \delta \hat{J}_{_{\rm X}} \sin \omega + \delta \hat{J}_{_{\rm Y}} \cos \omega \qquad (1.1)$$

where $\delta \hat{J}_{_{\rm X}}$ and $\delta \hat{J}_{_{\rm Y}}$ are the in-phase and out-of-phase components of the current.

In most cases, an impedance analyzer is used to perform such measurements. However, an impedance analyser simply consists of a harmonic voltage source and a lock-in amplifier, and harmonic measurements can also be carried out using an arbitrary waveform generator (AWG) in combination with a lock-in amplifier to measure the in-phase and out-of-phase components.

From these values the complex admittance can be calculated:

$$Y(\omega) = Y'(\omega) + iY''(\omega) = \delta\hat{J}_{_{\rm X}} / \delta\hat{V}_{_{\rm D}} + i\delta\hat{J}_{_{\rm Y}} / \delta\hat{V}_{_{\rm D}}$$

where Y' and Y'' are the real and imaginary parts of the admittance and *i* is the imaginary number. The admittance can be transformed into other equivalent representations,⁴⁵ such as the complex capacitance, which is often the most useful in the context of semiconductor diodes:

$$C(\omega) = C'(\omega) + iC''(\omega) = \frac{iY(\omega)}{\omega}, \qquad (1.2)$$

where C' is the real part and C'' the imaginary part of the capacitance. In well behaved systems, C' and C'' are coupled by the Kramers-Kronig relations, which means that C' and



Figure 2: a) Current-voltage characteristic of a MSM-diode with a PbS:BDT NC-solid in the dark (black) and under AM1.5 illumination (blue) for different temperatures. b) Short-circuit current ($J_{\rm sc}$) and open-circuit voltage ($V_{\rm oc}$) under different illumination intensities. c) $J_{\rm sc}$ and $V_{\rm oc}$ for different temperatures. Figures are reproduced from Refs. 23,44.

C'' contain essentially the same information.⁴⁵ It is therefore sufficient for most practical purposes of diode characterization to only investigate C'.

Some authors discuss capacitance measurements in terms of the absolute value of the capacitance, given by $|C| = \sqrt{C'^2 + C''^2}$. While this is not incorrect per se, one must keep in mind that physical models, such as the Mott-Schottky model, which are developed for C', cannot be applied to |C| (See Section IV).

Figures 3a and 3b provide examples of common types of capacitive measurements that have been performed on PbS NC-solids. Capacitance versus frequency measurements at different temperatures (Figure 3a), also known as thermal admittance spectroscopy (TAS), can be used to obtain information on trap

states using the Shockley-Read-Hall trapping model.^{25,30} Capacitance versus voltage measurements, as shown in Figure 3b, and are used to determine the built-in voltage of the diode and the doping density of the semiconductor using the Mott-Schottky model of the depletion region.¹⁰

Photocurrent Spectroscopy

A second group of harmonic techniques is known as photocurrent spectroscopy. Here, the illumination is modulated $I_{\rm D}(\omega) = I_{{\rm D},0} + \delta \hat{I}_{_{\rm D}} \sin \omega$ using a chopper and the current response is recorded using a lock-in amplifier. The diode is usually reverse biased ($V_{\rm D} \leq 0V$) to facilitate extraction of photo-excited charge carriers. In contrast to admittance measurements, the photocurrent is most practically discussed in terms of its magnitude ($|J_{\rm D,ph}(\omega)|$) and phase ($\varphi_{\rm D,ph}(\omega)$):

$$|J_{\rm D,ph}(\omega)| = \sqrt{\delta \hat{J}_{\rm x}^{2} + \delta \hat{J}_{\rm y}^{2}},$$

$$\varphi_{\rm D,ph}(\omega) = \arctan \frac{\delta \hat{J}_{\rm y}}{\delta \hat{J}_{\rm y}}$$
(1.3)

The most common variation of photocurrent spectroscopy is the measurement to determine the external quantum efficiency (EQE) of a device, as shown in Figure 3c.⁴¹ Here, a fixed chopping frequency in the range of 10 - 1000Hz is set and the wavelength λ of a monochromatic light source is tuned from the UV to the NIR while the photocurrent is recorded. To obtain the EQE, the same measurement is performed on a calibrated reference device with a known EQE.

This technique can be extended to the mid and far-infrared region using a Fourier transform infrared spectrometer (FTIR), which is known as Fourier transform infrared photocurrent spectroscopy (FTPS)⁴⁶. This technique was recently applied to PbS NC-diodes and revealed a discrete optical transition within the bandgap indicative of a trap state (See Figure 3d).²⁵

Alternatively, changing frequency of the light modulation is known as intensity-modulated photocurrent spectroscopy (IMPS),^{47,48} which was recently applied by Erslev et al. to investigate the Urbach tail in PbS NC solids (See Figure 3e).⁴⁹

Transient Techniques

In the third class of optoelectronic characterization techniques, an electrical or optical pulse form is applied to the device, and the voltage or current response is recorded as a function of time.

Most commonly, a voltage signal, $V_{\rm D}(t)$, with the form of a ramp or pulse, is applied to the diode using an AWG. The time dependent current $J_{\rm D}(t)$ is measured, typically by a combination of a broad-band transimpedance amplifier and a



Figure 3: Different harmonic measurement techniques applied to PbS NC-solid diodes. a) Capacitance vs. frequency at 0 V bias for different temperatures (thermal admittance spectroscopy (TAS)).²⁵ b) Capacitance vs. voltage at 20 Hz (Mott-Schottky analysis).¹⁰ c) External quantum efficiency (EQE) for illumination wavelengths.⁴¹ d) Photocurrent vs. illumination for near to far-infrared illumination (FTPS).²⁵ e) Photocurrent for different modulation frequencies (IMPS).⁴⁹ Figures are reproduced from the indicated references.

digitizer, or with the low-impedance input of an oscilloscope, if the sensitivity of the latter is sufficient.

Example $V_{\rm D}(t)$ profiles and $J_{\rm D}(t)$ measurement traces are shown in Figure 4.^{50,51} The linear voltage ramp in Figure 4a is a Charge Extraction by Linearly Increasing Voltage (CELIV) measurement. CELIV was designed to determine charge carrier mobilities in organic semiconductors,^{52,53} and has been applied to PbS NC-solids to determine the majority carrier mobility⁵⁴ and the dielectric constant.^{50,10} The rectangular pulse in Figure 4b is for charge (Q)-based Deep Level Transient Spectroscopy (Q-DLTS) measurements, which can be used to determine trap activation energies and trap densities based on the Shockley-Read-Hall (SRH) model.

In photovoltage transient techniques, the sample is illuminated by a constant light source superimposed with a short laser pulse, such that the measured $V_{\rm oc}(t)$ shows a small transient (Figure 3c, left). Such transient open-circuit voltage measurements rely on the use of a high impedance voltage measurement device. A regular oscilloscope input with $R_{\rm in} = 1 \,\mathrm{MOhm}$ can be too low and will limit the longest

recombination time that can be measured to $R_{\rm in}C\approx 1\,{\rm ms}$, where C is the high frequency capacitance of the diode. Instead, broadband voltage amplifiers with up to $1\,{\rm TOhm}$ input impedance are preferable.

Photovoltage transient measurements are often accompanied by measurement of the photocurrent transient, $J_{\rm sc}(t)$. This is done with a broadband transimpedance amplifier and is used to determine the amount of charge introduced by the light pulse and the charge extraction time. 55 A typical photocurrent transient is shown in Figure 3c (right). 30

IV. Interpretation of Data

To interpret the quasistatic, harmonic, and transient measurements described above, various models are applied. Here we discuss the key models for extracting parameters of relevance to solar cell performance, the underlying physical assumptions of these models, and the considerations that must be taken in applying them to NC-based solids.

The Shockley Diode Equation

The most fundamental characteristic of a diode is its IV curve, which is generally analysed using the Shockley Diode equation. Here we use the notation in the review by Lunt *et al.*:⁵

$$J_{\rm D} = \frac{R_{\rm p}}{(1.4 R_{\rm s} + R_{\rm p}} \left\{ J_0 \left[\exp(\frac{e(V_{\rm D} - J_{\rm D} R_{\rm s})}{n_{\rm V} k T}) - 1 \right] \right\} + \frac{V_{\rm D}}{R_{\rm p}} - J_{\rm sc} \,,$$

where $R_{\rm p}$ is the shunt resistance, $R_{\rm s}$ the series resistance, $V_{\rm D}$ the voltage across the device, $J_{\rm sc}$ the short circuit current, $n_{\rm V}$ the ideality factor related to the voltage, k the Boltzmann constant, and J_0 the reverse saturation current. The Shockley diode equation is phenomenological so its parameters are not unambiguously linked to specific physical processes.

The low-forward bias region between 0.1 to 1V, where the current increases exponentially, is the most important for solar cell performance. The exponential current increase in the dark is described by the reverse saturation current (J_0) and the ideality factor related to voltage (n_V) . Typical values of J_0 for PbS nanocrystal diodes at room temperature range from $10^{-4} - 10^{-7} \,\mathrm{mA/cm^2} \,{}^{10,44,24} \,J_0$ derives its name from the fact that ideal diodes saturate to this current in the reverse region. In the context of solution processed semiconductor based devices, the name *reverse saturation current* is rather misleading, as this current is hardly ever observed due to the presence of larger leakage currents that dominate in reverse bias.

In all common physical diode models, J_0 is associated with a thermal activation energy $E_{\rm JA}$ and is given by:

$$J_0 = J_{00} \exp\left(\frac{-E_{\mathrm{JA}}}{n_{\mathrm{E}} kT}\right),\tag{1.5}$$



Figure 4: Transient measurement techniques. a) Voltage ramp is applied and transient current is measured (CELIV).⁵⁰ b) Voltage pulse is applied and transient current is measured (Q-DLTS).⁵¹ c) Small laser pulse is applied and small transient distortion in the open-circuit voltage is measured (left). In a second measurement the short-circuit current of the same laser pulse is measured (right) to obtain the differential charge Δq .³⁰ Figures are reproduced from the indicated references.

where $n_{\rm E}$ is the ideality factor related to the activation energy. We distinguish between the ideality factors $n_{\rm V}$ and $n_{\rm E}$ to facilitate the discussion of existing literature below.

For a semiconductor with a band gap $E_{\rm g}, E_{JA}$, and $n_{\rm E}$ assume different values depending on the dominant mode of charge conduction. In the case of:

- Band-to-band recombination in an ideal diode, $E_{JA} = E_g$ and $n_E = n_V = 1$ • Trap-assisted band-to-band recombination,
- Trap-assisted band-to-band recombination, $E_{\rm JA}=E_{\rm g}$ and $n_{\rm E}=n_{\rm V}=2$
- Ideal thermoionic-emission or diffusion of majority carriers over a Schottky-barrier with height $\phi_{\rm B}$, $E_{\rm JA} = \phi_{\rm B}$ and $n_{\rm E} = n_{\rm V} = 1$

A mixture of direct band-to-band recombination $(n_{\rm E} = n_{\rm V} = 1)$ and trap-assisted band-to-band recombination $(n_{\rm E} = n_{\rm V} = 2)$ can lead to ideality factors in the range of 1 to $2.^{56,57}$ While it was proposed that space charge effects can increase the ideality factor in organic semiconductors, ⁵⁸ in light of the high dielectric constant of PbS NC-solids, we expect that space charge limited conduction is improbable and that trap assisted recombination would be the source of increased ideality factors, in agreement with the findings in Ref. 23.

Equation (1.5) shows that any assumption made about $n_{\rm E}$, directly affects the determination of the activation energy, $E_{\rm JA}$. This has resulted in a range of different values of $E_{\rm JA}$ reported in literature. Using three distinct data sets - temperature dependent IV characteristics, temperature-dependent open circuit voltage measurements, and optical absorption - reported by Szendrei et al.^{44} - we show how the relevant parameters $n_{\rm V}$, J_{00} , and $E_{\rm JA}$ can be determined self-consistently.

First, we take the model used for thin-film photovoltaics, where $n_{\rm E}=n_{\rm V}^{-.59}$ Now, the open-circuit voltage can be calculated from Eq. (1.4) and (1.5) to be:

$$V_{\rm oc} \, = \, E_{\rm JA} \, + \frac{n_{\rm V} kT}{e} \log \! \left(\frac{J_{\rm sc}}{J_{00}} \right) . \tag{1.6}$$

We fit Eq. (1.6) to the IV characteristics in the dark at room temperature and determine $n_{\rm V}=1.6$ (Figure 5a). Setting $n_{\rm E}=n_{\rm V}=1.6$, we then fit Eq. (1.5) to J_0 determined at different temperatures and find $E_{\rm JA}=0.91\,{\rm eV}$ and $J_{00}=1.7\times10^5\,{\rm mA/cm^2}$. Alternatively, in Figure 5c, we plot the temperature-dependent open-circuit voltage data with a fit of Eq. (1.6) and find $E_{\rm JA}=0.92\,{\rm eV}$. Finally, we fit the optical absorption tail of the NCs according to a direct semiconductor model and obtain a band gap of $E_g=0.93\,{\rm eV}$. The agreement of the energies determined using three separate data sets convinces us that $n_{\rm E}=n_{\rm V}$ is a valid assumption for NC-solids.

Furthermore, the strong agreement between the extracted parameters demonstrates the importance of distinguishing between the optical absorption exciton energy ($E_{\rm exc}$) and the charge transport bandgap. As explained above, we find from the data in Ref. 44 that $E_{\rm JA} = E_{\rm g} = 0.92\,{\rm eV}$ is the effective bandgap for charge transport while $E_{\rm exc} = 1.1\,{\rm eV}$. A recent report shows that J_0 scales with the exciton energy according to $E_{\rm JA} = E_{exc}$ with $n_{\rm E} = 1.92$ and $n_{\rm V} = 1.7-2.4$.²⁴ However, if the 15% discrepancy between $E_{\rm g}$ and $E_{\rm exc}$ is accounted for, an ideality factor of $n_{\rm E} = 1.6$ is obtained for the data in Ref. 24. Thus the data in Ref. 24 and Ref. 44 are actually in very good agreement.

For the case of ITO/PbS/LiF/Al MSM diodes, we thus conclude that the:



Figure 5: Data by *Szendrei et al.*⁴⁴ reanalyzed according to Eq. (1.4) - (1.6). a) Current-voltage (IV) curve at room temperature allows to extract the ideality factor $n_{\rm V} = 1.6$. b) Reverse-saturation current vs. temperature determined by IV measurements in dark at different temperatures. Thermal activation energy is found $E_{\rm JA} = 0.91\,{\rm eV}$ according to Eq. (1.5). c) Open-circuit voltage vs. temperature, fitted to Eq. (1.6) indicates $E_{\rm JA} = 0.92\,{\rm eV}$. d) Optical absorption of the PbS NCs. The absorption exciton peak is found at $E_{\rm exc} = 1.1\,{\rm eV}$, while the absorption band edge extrapolates to $E_{q} = 0.93\,{\rm eV}$.

- forward current has an activation energy, which is the effective bandgap for charge transport $E_{JA} = E_g$, which can be ~15% smaller then the absorption-exciton energy (E_{exc}).
- ideality factors $(n_{\rm E} = n_{\rm V})$ are in the range of 1.6 2.0, indicating a mixture of direct recombination and trap-assisted recombination.

Capacitance Models

The performance of a semiconductor can be highly influenced by the presence of states within the band gap such as dopants or trap states. These states are fundamentally related to the capacitance of the semiconductor, and most models that are used to determine the number, density, and properties of midgap states are therefore based on different measurements of the capacitance.

A semiconductor diode has two major capacitance contributions: the electrostatic capacitance of the depletion region ($C_{\rm D}$) and the chemical capacitance ($C_{\rm T}$) of the electronic states of the semiconductor material, as shown in Figure 6a.

The electrostatic capacitance can be understood as a plate capacitor over the depletion region, where no or few free charge carriers are present, and is given by

$$C_{\rm D} = \frac{\epsilon_{\rm s} \epsilon_0}{W_{\rm D}},\tag{1.7}$$

where $\epsilon_{\rm s}$ is the dielectric constant of the semiconductor and W_D is the width of the depletion region. The dielectric constant of a NC-solid can be calculated by the Maxwell-Garnett effective medium theory if the volume filling fraction (δ) of NCs is known:

$$\epsilon_{\rm s} = \epsilon_{\rm h} \frac{\epsilon_{\rm nc}(1+2\delta) - \epsilon_{\rm h}(2\delta-2)}{\epsilon_{\rm h}(2+\delta) + \epsilon_{\rm nc}(1-\delta)}, \qquad (1.7)$$

where $\epsilon_{\rm h}$ and $\epsilon_{\rm nc}$ are the bulk dielectric constants of the host matrix material and the NC material. For a matrix of organic ligands ($\epsilon_{\rm h}=2.2$) and PbS ($\epsilon_{\rm nc,PbS}=169\,)^{60}$ in a close packed configuration ($\delta=0.74$), we obtain an upper limit of the dielectric constant of PbS NC-solids of $\epsilon_{\rm s}=18.5$, in good agreement with reported measurements.^{10,50,18,25}

If the NC-film is thin enough we can assume that the depletion region extends through the whole film, i.e. $W_{\rm D} = d$. This approximation is helpful since W_D is often times unknown. This approximation can also be used to calculate the smallest possible value for the electrostatic capacitance:

 $C_{\rm D}>C_{\rm D,0}=\epsilon_{\rm s}\epsilon_0/d$. For a typical $200\,{\rm nm}$ -thick PbS NC-solid, this lower bound on the electrostatic capacitance is between $62\,{\rm nF/cm^2}$ and $82\,{\rm nF/cm^2}$ assuming $\epsilon_{\rm s}=14-18.5\,.^{18.25}$

To understand what defines the width of the depletion region (W_D) we introduce the dielectric relaxation time of free charge carriers ($\tau_{\rm rel}$), which is given by

$$\tau_{\rm rel} = \frac{\epsilon_{\rm s} \epsilon_0}{\sigma} = \frac{\epsilon_{\rm s} \epsilon_0}{e(\mu_n \ n + \mu_p \ p)}, \tag{1.8}$$

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where σ is the conductivity and n and p are the density of free electrons and holes with respective mobilities μ_n and μ_p . When $\tau_{\rm rel}$ exceeds the characteristic timescale of the measurement, which, for a harmonic capacitance measurement, is ω^{-1} , the charge carriers cannot follow and the material behaves like a electrostatic capacitor. As shown in Figure 6a, $\tau_{\rm rel}$ varies over the semiconductor thickness depending on the local density of electrons and holes. The edge of the depletion region can now be defined as the point where $\tau_{\rm rel}(x) = \omega^{-1}$.

In traditional semiconductor theory, it is often assumed that the mobility is high ($\mu_n \gg 10 \, {\rm cm}^2/{\rm Vs}$) such that $\tau_{\rm rel}$ in the bulk is in the range of ns to ps. These time scales are significantly shorter than the time scales of most capacitance measurement instrumentation (typically below $100 \, {\rm MHz}$). However, in low



Figure 6: a) Schematic of the electrostatic ($C_{\rm D}$) and chemical capacitance ($C_{\rm T}$) in a diode. $C_{\rm D}$ is dominated by the width of the depletion region (W_D), which is defined by the divergence of the dielectric relaxation time ($\tau_{\rm rel}$). $C_{\rm T}$ is dominated by the density of states ($N_{\rm T}$) and the position of the quasi-Fermi energies ($E_{\rm F,n}$ $E_{\rm F,p}$). The kinetics of the charge carriers contributing to $C_{\rm T}$ is given by the Shockley-Read-Hall capture (β_n , β_p) and emission (e_n , e_p) coefficients. b) $N_{\rm T}$ determined by thermal admittance spectroscopy (TAS) measurements.²⁵ c) $N_{\rm T}$ determined by IMPS measurements.⁴⁹ d) DLTS signal for different temperatures, indicating a discrete trap state with an activation-energy of 0.4 eV.⁵¹ e) $N_{\rm T}$ determined by transient open-circuit voltage (or differential capacitance) measurements.⁷⁶ Figures are reproduced from the indicated references.

mobility semiconductors such as PbS NC-solids, a typical relaxation time of $\tau_{\rm rel} = 0.7\,\mu{\rm s}$ can be estimated assuming $\mu_p = 10^{-3}\,{\rm cm}^2/{\rm Vs}$ and $p = 10^{16}\,{\rm cm}^{-3}.^{54}$ Thus $\tau_{\rm rel}$ is in the range of most capacitance meters. Therefore to measure the depletion region, we cannot safely neglect the relaxation of free charge carriers and must carefully choose the measurement frequency. Further complicating the measurement of the depletion width is the second major contribution to the diode capacitance, the chemical capacitance.

The chemical capacitance,⁶¹ or equivalently the differential capacitance,^{62,30} is related to the occupation of electronic states in the semiconductor. The occupation of a discrete electron state "A" at an energy $E_{\rm A}$ in the Boltzmann approximation is:

$$n_{\rm A} = N_{\rm A} \exp \frac{-(E_{\rm A} - E_{\rm F,n})}{kT},$$
 (1.9)

where $E_{\rm F,n}$ is the quasi-Fermi energy of the electrons. If $E_{\rm F,n}$ is changed, the occupation changes, requiring an addition or removal of electrons. This effect is conveniently described by the chemical capacitance⁶¹

$$C_{\rm T,A} = e^2 \frac{\partial n_{\rm A}}{\partial E_{\rm F,n}} = e^2 \frac{n_{\rm A}}{kT}, \qquad (1.10)$$

which is a function of the $E_{\rm F,n}$. Considering all electrons (n) in conduction band (CB) and trap states in a structure with a density of states $N_{\rm T}(E)$, the chemical capacitance is given by

$$\begin{split} C_{\rm T} &= e^2 \frac{\partial n}{\partial E_{\rm F,n}} \\ &= e^2 \int N_{\rm T}(E) f'(E - E_{\rm F,n}) dE \approx e^2 N_{\rm T}(E_{\rm F,n}) \end{split}$$
(1.11)

where f' is the first derivative of the Fermi-Dirac distribution. The fact that the chemical capacitance is proportional to the density of states clarifies why capacitance can be used to characterize trap state density.

The absence of the dielectric constant in Eq. (1.12) highlights that the chemical capacitance is not of electrostatic origin and can therefore not be understood as a plate capacitor. Furthermore, the chemical capacitance has a unit of capacitance per volume (F/cm^3), so, to obtain the total chemical capacitance, Eq. (1.12) must be integrated along the film thickness. Assuming that the dominant contribution of the chemical capacitance comes from a region of width W_T , the total capacitance is given by:

$$C_{\rm T} = e^2 N_{\rm T} (E_{\rm F,n}) W_{\rm T}$$
, (1.12)

which has the unit of capacitance per area (F/cm^2). If we assume W_T is on the order of the film thickness, we find a typical value of $32 nF/cm^2$ for a PbS NC-solid of thickness of 200 nm with a midgap state density of $10^{16} cm^{-3} eV^{-1}$.³⁰

The total capacitance of a diode is thus given by a combination of $C_{\rm D}$ and $C_{\rm T}$ where both capacitance contributions are comparable and on the order of $10 - 100 \,\mathrm{nF/cm^2}$. This

complicates the interpretation of measurement data, as most models are based on the assumption that only $C_{\rm D}$ or $C_{\rm T}$ is measured.

Qualitatively, we expect that the capacitance of a PbS NC-diode in the dark and open circuit condition, where only a low density of mobile charge carriers is available, will be dominated by the $C_{\rm D}$. $C_{\rm T}$ will be comparatively low, reflecting the density of states in the middle of the band gap. Under increasing illumination or forward bias, more free charge carriers will be available. $C_{\rm D}$ will first increase and then disappear as $\tau_{\rm rel}$ becomes faster than the timescale of the measurement. $C_{\rm T}$ will be significantly larger, as the quasi-Fermi energies for electrons and holes move towards the band edges, where the density of states increases.

Mott-Schottky Model

To interpret capacitance-voltage (CV) measurements, many authors apply the Mott-Schottky model.⁵⁶ In this model, it is assumed that the capacitance, C', measured at a given frequency, ω , is dominated by the electrostatic capacitance of the depletion region and is given by:

$$C' \doteq C_{\rm D} = \frac{\epsilon_{\rm s}\epsilon_0}{W_{\rm D}} = \sqrt{\frac{e\epsilon_{\rm s}\epsilon_0 N_{\rm A}}{2(V_{\rm bi} - V)}}, \qquad (1.13)$$

where $N_{\rm A}$ is the doping density and $V_{\rm bi}$ the built-in voltage. Eq. (1.14) can be used to determine $N_{\rm A}$ and $V_{\rm bi}$ by plotting C'^{-2} vs. $V_{\rm D}$ (Figure 3b). If a straight line can be fitted to this so-called Mott-Schottky plot, the x-axis intercept is taken as $V_{\rm bi}$ and the slope is related to $N_{\rm A}$. Using this model, typical values for $N_{\rm A}$ are $1-5\times10^{16}\,{\rm cm}^{-3}$ and $V_{\rm bi}$ is around 0.55 V, corresponding to a depletion width at 0 V of $W_D=200-250\,{\rm nm}$. 10,63,64,18

Perhaps due to its seeming simplicity, the Mott-Schottky model is at times incorrectly applied to NC-based solids. For example, one common mistake found in literature is the use of the absolute value of the capacitance $|C| = \sqrt{C'^2 + C''^2}$ instead of the real part of the capacitance C'. In the forward region of a diode, large dc-currents pass through the diode and |C| will approach C'', since $C'' \propto \partial J_{\rm D} / \partial V_{\rm D}$. Plotting $|C|^{-2} = 1 / (C'^2 + C''^2)$ versus $V_{\rm D}$ leads to a plot with the typical Mott-Schottky shape, but the linear slope is simply a measure of the slope of the IV curve and cannot be used to extract $N_{\rm A}$ and $V_{\rm bi}$.

Furthermore, it has not even been established that the Mott-Schottky analysis is applicable to NC-solids in general. To show this, it would be necessary to demonstrate that the measured capacitance is only weakly dependent on frequency and temperature in the region where the Mott-Schottky model is applied. If this is not the case, the capacitance is most probably dominated by the chemical capacitance of trap states instead of the electrostatic capacitance of the depletion region.

analysis as applied in existing literature is difficult as very few articles report the raw capacitance spectrum prior to applying the inverse-square operation. In fact, the few studies, which report raw capacitance data, show anomalies, such as negative capacitances^{50,31} and increased bias dependence for thinner films³⁹, which are not consistent with the Mott-Schottky model. We note that raw capacitance spectra could provide additional insights into the physics of NC-solids. For example, the appearance of negative capacitances – which are reported but not commented upon in Refs. 50,31 – are in fact found in a large variety of electronic systems,⁶⁵ including metalsemiconductor interfaces,⁶⁶ Quantum-Well devices,⁶⁷ and general low-mobility semiconductors.^{68,69}

Unfortunately, confirming the validity of the Mott-Schottky

Shockley-Read-Hall Model

The trapping and detrapping of charge carriers plays a central role in the device operation of diodes and solar cells, in particular in materials with a high density of trap states, such as NC-solids. The kinetics of charge carrier trapping is usually described in the context of the Shockley-Read-Hall (SRH) model.^{56,70} In this model, the capture of electrons in a trap state located at an energy, $E_{\rm T}$, below the conduction band is described by a capture coefficient ($\beta_{\rm n}$) (See Figure 5a). The emission rate of an electron from the trap state to the conduction band at energy $E_{\rm C}$ is thus found to be:

$$e_{\rm n} = \beta_{\rm n} N_{\rm C} \exp \frac{-(E_{\rm C} - E_{\rm T})}{kT},$$
 (1.14)

where $N_{\rm C}$ is the density of states of the conduction band. Coupling to the valence band follows the same formalism.

The pre-exponential factor in Eq. (1.15) is known as the attempt-to-escape frequency (or attempt-frequency) $\nu_0=\beta_{\rm n}N_{\rm C}$. 70,25 The temperature dependence of the ν_0 can be obtained within the effective mass approximation as

$$\nu_0 = \beta_{\rm n} N_{\rm C} = \nu_{00} T^2 = \sigma_{\rm T} \Gamma_n T^2 \qquad (1.15)$$

where ν_{00} is the reduced attempt frequency in units of s⁻¹K⁻², $\sigma_{\rm T}$ the trap capture cross section, and Γ_n a material dependent parameter for electrons given by:^{71,56}

$$\Gamma_{\rm n} = 2 \times 3^{1/2} (2\pi / h^2)^{3/2} k^2 m_n^*, \qquad (1.16)$$

where m_n^* is the effective mass of the electron. For bulk PbS, $\Gamma_n = 3.256 \times 10^{20} \text{K}^{-2} \text{cm}^{-2} \text{s}^{-1}$.⁵¹

Because the quadratic dependence of the attempt frequency on temperature is weak compared to its exponential dependence (Eq. (1.15)), this quadratic term is sometimes neglected.⁷⁰ We have therefore evaluated how both cases are related to each other: First we generate datasets for different values of $E_{\rm T}$ based on Eq. (1.15) where the pre-exponential factor of the emission rate is given by $\nu_{00}T^2$. We then fit these datasets with the same equation with a temperature-independent pre-exponential factor ν_0 . We find that by neglecting T^2

dependence, the activation energy is consistently overestimated by 39 meV and that $\nu_0=4.0\times 10^5 K^2\,\nu_{00}$, for any given activation energy.

Trap State Characterization

Of particular interest in the optimization of NC-solids for solar cell performance is the reduction of trap states. Here, we summarize several approaches to quantify the energy and density of trap states in NC-solids from a variety of measurement data. These approaches invoke the models described above.

Thermal Admittance Spectroscopy

With the SRH model, it is possible to extract information about traps from the capacitance spectrum.⁷⁰ As shown in Figure 3a, a discrete trap state introduces a step in the capacitance spectrum at a frequency, which is given by the SRH model as

$$\omega_0 = 2\nu_0 \exp\left(-\frac{\Delta E_{\rm T}}{kT}\right). \tag{1.17}$$

To determine the activation energy ($\Delta E_{\rm T}$) and the attemptfrequency (ν_0) of a trap state, the capacitance spectrum is recorded versus temperature. This is known as Thermal Admittance Spectroscopy (TAS). The trap state density in a diode can be reconstructed from the real part of the capacitance C' by:⁷⁰

$$\begin{split} N_{\mathrm{T}}(E) &= \frac{{V_{\mathrm{bi}}}^2}{W_{\mathrm{D}} \left(e V_{\mathrm{bi}} - (E_{\mathrm{F,n,\infty}} - E) \right)} \frac{1}{kT} \frac{\omega \, dC'(\omega)}{d\omega}, \\ & (1.18) \end{split}$$

where $E_{\rm F,n,\infty}$ is the bulk Fermi-energy with respect to the conduction band as defined in Ref. 70. Using TAS, we have recently shown the presence of a discrete trap state in PbS:EDT NC-solids with a density of $1.8\times10^{17}\,{\rm cm^{-3}}$ and an activation energy of $E_{\rm T1}=0.36{\rm eV}$. The reduced attempt frequency for the T1 trap state was found to be $\nu_{00}=1.2\times10^5\,{\rm s^{-1}K^{-2}}$, which corresponds to $\nu_0=4.8\times10^{10}\,{\rm s^{-1}}$ according to the relation given above.^25

While TAS is a powerful and simple tool to detect trap states, trap states can be invisible to this technique.²⁵ A given trap state only contributes to the capacitance if the Fermi-energy crosses the trap level at some point in the device.⁷⁰ If this is not the case, the trap state will be invisible to TAS and can only be detected using complementary techniques.²⁵

Intensity-Modulated Photocurrent Spectroscopy

Also based on the SRH model, intensity-modulated photocurrent spectroscopy (IMPS) measurements can be used to determine the number density of trap states.^{72,49} The frequency dependent photocurrent (Eq. (1.3)) is used to reconstruct the trap state density:

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$$N_{\rm T}(E) \propto \frac{\partial}{\partial \log \omega} \frac{\cos \varphi_{\rm D,ph}(\omega)}{|J_{\rm D,ph}(\omega)|}.$$
 (1.19)

IMPS measurements on PbS:EDT NC-solids by Erslev *et al.* indicate that the band edge of the material has an exponential trap distribution of only $15 \mathrm{meV}$, comparable with polycrystalline semiconductors and significantly lower than for amorphous silicon (See Figure 6c). Additionally, they identify a discrete trap state with an activation energy of $E_{\mathrm{T}}=0.25 \mathrm{eV}$ and an attempt frequency of $\nu_0=5.0\times 10^5 \mathrm{s^{-1}}$.

Deep Level Transient Spectroscopy

A more direct approach to detect the emission of trapped charge carriers (Eq. (1.15)) is deep level transient spectroscopy (DLTS), which is based on a large signal transient measurement.⁷³ In the original DLTS method, the diode is first excited by a voltage pulse, after which the transient of C' is recorded. C' is typically measured at 1 MHz using a capacitance bridge (e.g. Boonton 7200). This method is based on the assumptions that:

- 1) At the chosen measurement frequency, the chemical capacitance of the trap states is negligible in comparison to the electrostatic capacitance.
- 2) The dielectric relaxation time of free charge carriers $\tau_{\rm rel} = \epsilon_{\rm s} / \sigma$ is much shorter than the inverse measurement frequency (i.e. free charge carriers can move instantaneously at the chosen measurement frequency).

We recently showed that trap states contribute significantly to the capacitance of PbS NC-diodes up to a frequency of 20kHz at room temperature.²⁵ The dielectric relaxation time of free carriers in PbS NC-solids can be estimated to be on the order of 1 μ s, restricting the measurement frequency to $\ll 160$ kHz. Since the trap response frequency and the dielectric relaxation frequency differ by less than an order of magnitude, we conclude that transient capacitance measurements cannot be interpreted within the standard theory of DLTS. Previous capacitance based DLTS measurements on NC-diodes were therefore not conclusive.⁷⁴

An alternative DLTS method for high-trap density low-mobility semiconductors is given by Q-DLTS, which is based on the measurement of current transients. The current transient resulting from the carrier emission of a single discrete trap is given by:

$$J(t) = \frac{W_{\rm D}N_{\rm T}}{\tau_{\rm E}} \exp\left(-\frac{t}{\tau_{\rm E}}\right),\tag{1.20}$$

where the emission time constant is given by $\tau_{\rm E} = e_{\rm n}^{-1}$.

In the presence of multiple trap states or a distribution of trap states, the measured current transient $(J_{\rm D}(t))$ will be the sum of the individual contributions and become multi-exponential. The measured current transients can be processed by the box-

car correlation method, parameterized by two times t_1 and t_2 , which are typically chosen with a fixed ratio $t_2 / t_1 = 2$. The DLTS signal (ΔQ) is then given by

$$\begin{split} \Delta Q(\tau_{\rm Q}) &= \int_{t_1}^{t_2} J_{\rm D}(t) \, dt, \\ \tau_{\rm Q} &= \frac{t_2 - t_1}{\log t_2 \ / \ t_1}, \end{split} \tag{1.21}$$

where $\tau_{\rm Q}$ is the detector time constant. Intuitively, one can think of $\Delta Q(\tau_{\rm Q})$ as an approximation of the distribution of time-constants present in the decay of $J_{\rm D}(t)$.

A typical trace for $\Delta Q(\tau_{\rm Q})$ is shown in Figure 6d, where the large peak indicates a single emission time constant. The number of traps can be determined from the peak value ($\Delta Q_{\rm max}$):

$$N_{\rm T} = \frac{4\Delta Q_{\rm max}}{eW_{\rm D}}.$$
 (1.22)

We have applied Q-DLTS to PbS:EDT NC-diodes and have reported the measurement of a trap state with an activation energy of $\Delta E_{\rm T}=0.36~{\rm eV}$ and density of $\sim 2\times 10^{17}\,{\rm cm}^{-3}$ in agreement with TAS measurements. 51,25

Charge Extraction by Linearly Increasing Voltage (CELIV)

A second technique based on transient current measurements is known as charge extraction by linearly increasing voltage (CELIV).^{52,53} As shown in Figure 4a, a linear voltage ramp with speed $dV_D / dt = s$ is applied and the current is measured.⁵⁰ A flat plateau of the current is interpreted as the displacement current of the electrostatic capacitance and is used to estimate the dielectric constant

$$\epsilon_{\rm s}\epsilon_0 = \frac{J_{\rm D}W_{\rm D}}{s} \,. \tag{1.23}$$

CELIV was applied to PbS NC-diodes to find $\epsilon_s = 17 \pm 2$ and $\epsilon_s = 18$, which is in line with effective medium theory.^{50,10} In the report by *Johnston et al.* CELIV is also used to determine the majority carrier (hole) mobility to $1.5 \times 10^{-3} \, \mathrm{cm}^2 \mathrm{V}^{-1} \mathrm{s}^{-1}$.⁵⁴

Transient open-circuit voltage

Transient open-circuit voltage measurements, also known as differential capacitance measurements, are another approach to determine the chemical capacitance of trap states. These techniques have been used extensively in the field of dye-sensitized solar cells,⁵⁵ organic solar cells,^{62,75} and have recently been applied to PbS NC-solids.^{30,76}

To carry out these measurements, the diode is biased by constant illumination intensity ($I_{\rm ph}$) and a short laser pulse introduces a small perturbation. The open-circuit voltage is measured versus time $V_{\rm oc}(t)$ as shown in Figure 4c. For

different $I_{\rm ph}$ this trace is measured and three parameters are extracted: the open circuit voltage baseline $(V_{\rm oc,0})$, the differential open circuit voltage increase ($\Delta V_{\rm oc}$), and the small signal recombination time ($\tau_{\rm rec}$).

In a second experiment, the differential charge Δq is measured, which is the charge that is transported by the laser pulse from the valence to the conduction band. The same laser pulse as in the transient open-circuit voltage measurement is used to excite the diode without background illumination (i.e. $I_{\rm ph}=0$). The short circuit current (Figure 4c) is measured vs. time $J_{\rm sc}(t)$, and Δq is found by the integration of the peak in $J_{\rm sc}(t)$.

The differential capacitance ($C_{\rm DC}$) is then calculated using Δq and the obtained pairs of values $(\Delta V_{\rm oc},V_{\rm oc,0})$:

$$C_{\rm DC}(V_{\rm oc,0}) = \frac{\Delta q}{\Delta V_{\rm oc}}$$

It is typically assumed that the differential capacitance represents the chemical capacitance of the trap states and that one of the quasi-Fermi energies (e.g. $E_{\rm F,p}$) is fixed and independent of the illumination. In this case, the quasi-Fermi energy of the other charge carrier can be expressed in terms of the open-cicuit voltage: $E_{\rm F,n} = V_{\rm oc,0} - E_{\rm F,p}$ and the density of states can now be obtained by using Eq. (1.13):

$$\frac{C_{\rm DC}(V_{\rm oc,0})}{e^2 W_{\rm T}} \doteq \frac{C_{\rm T}(E_{\rm F,n})}{e^2 W_{\rm T}} = N_{\rm T}(E_{\rm F,n}) \eqno(1.24)$$

This technique has been applied to PbS NC-diodes to measure the midgap trap density (Figure 6e).^{30,76} Organically passivated PbS NC films were found to have a broad exponential distribution of trap states on the order of $10^{16}-10^{17}\,{\rm cm}^{-3}{\rm eV}^{-1}$, some of which could be passivated with a hybrid approach using thiol-based and halide ligands.^{30,76}

For materials with a high dielectric constant, such as PbS NC-solids, the differential capacitance can be strongly influenced by the electrostatic capacitance of the depletion region, which introduces a baseline given by $\epsilon_{\rm s} / (eW_{\rm D})^2$. If the electrostatic capacitance is not subtracted from the differential capacitance, it will add a significant baseline to the obtained trap state density (e.g. $7.7 \times 10^{16} \, {\rm cm}^{-3} {\rm eV}^{-1}$ for a $100 \, {\rm nm}$ film PbS NC film) and present a lower detection limit for trap states.

In addition to measuring the midgap trap density, transient open-circuit measurements enable investigation of the recombination dynamics that limit the open circuit voltage and have been used to assess different passivation schemes for PbS-based heterojunction devices.^{77,78} However, a detailed analysis of the recombination dynamics and its relation to trap states has not yet been carried out for PbS NC-materials.⁶²

Conclusions

In summary, a number of standard electrical characterization techniques can be applied successfully to NC-solids. However, the applicability of different models to interpret the data must be verified. This is particularly challenging in NC-solids because distinct processes occur on comparable time scales with similar magnitudes.

For example, we show that NC-solids have both electrostatic and chemical (differential) capacitance of comparable magnitude. However, the Mott-Schottky model considers only the electrostatic capacitance while the SRH-based models take only the chemical capacitance into account. Thus, the regimes of the applicability of these models to NC-solids must be carefully considered.

To emphasise the importance of carefully selecting models of the device physics, we show for example that seemingly different current-voltage data in literature reports are in agreement, if the diode model used for thin-film photovoltaics is consistently applied.

Presentation of quasi-static, harmonic, or transient data prior to analysis can greatly assist the community in achieving a deeper understanding the strengths and limitations of existing models and permit the eventual development of more comprehensive models, specifically aimed at NC solids.

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