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ARTICLE

Hafnium Silicate: A New Microwave Dielectric Ceramic with Low Thermal Expansivity

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The HfSiO₄ ceramic are prepared by conventional solid state synthesis method by sintering at 1600 °C. The morphology of sintered surface is characterized using scanning electron microscopy and atomic force microscopy and the average surface roughness is about 118 nm. The sintered HfSiO₄ ceramics has $\epsilon_r = 7.0$, $Q_u \times f = 25,000$ and $\tau_f = -44$ ppm/°C at 10 GHz. It exhibits promising thermal properties such as low linear thermal expansivity (CTE) of -1.8 ppm/°C (dilatometer) in the temperature range of 30-800 °C and room temperature thermal conductivity of 11 W/mK.

Introduction

The unprecedented growth of wireless communication has led low loss dielectric materials as one of the most vibrant research areas in recent years. In general, wireless communication covers various applications ranging from wireless fax, cellular phones, global position satellite (GPS), military radar systems, intelligent transport system (ITS), Internet of Things (IoTs) and direct broadcast satellites [1]. Electronic data transmission with low signal attenuation is quite common using low relative permittivity ($\epsilon_r > 15$) ceramics in digital or analog modes throughout the radio and microwave frequency ranges [2]. The important characteristics required for such applications are low relative permittivity, low dielectric loss, high thermal conductivity and matching coefficient of thermal expansion to that of the materials attached to it, in addition to low temperature coefficient of resonant frequency [3].

The silicates with low relative permittivity and low dielectric loss tangent are attracting attention as a suitable dielectric material for ultrahigh frequencies [4]. Orthosilicates are generally used for refractory purposes because of its high thermal stability, dimensional accuracy, metal penetration resistance, low thermal expansion and high thermal conductivity. Moreover ASiO₄ [A = Zr, Hf, Th] ceramics are suitable for fission reactor and storage of nuclear wastes because of their effective radiation resistance [5, 6]. This resemblance is due to the similarity in their electronic structures and ionic radii which permits complete miscibility in most Zr-Hf compounds [7]. One of the major advantage of these orthosilicates based materials is low coefficient of thermal expansion and high thermal conductivity among the silicates. The dielectric properties of some of the alkali earth orthosilicates such as A₂SiO₄ (A = Ba, Ca, Sr) have been well characterized and are found to be suitable for packaging as well as millimeter wave communication applications due to their low ϵ_r and high $Q_u \times f$ values [8]. The values of ϵ_r were decreased (14.1, 10.4, 10.1) in the order Ba₂SiO₄ > Sr₂SiO₄ > Ca₂SiO₄.

The $Q_u \times f$ values obtained were 17900 GHz, 19100 GHz and 26100 GHz for Ba₂SiO₄, Sr₂SiO₄ and Ca₂SiO₄ respectively [8]. The τ_f of Ba₂SiO₄ was -17 ppm/°C, whereas Sr₂SiO₄ and Ca₂SiO₄ exhibited high values of τ_f , -205 ppm/°C and -89 ppm/°C respectively [8]. The coefficient of thermal expansion (CTE) of A₂SiO₄ (A = Ba, Sr) ceramics exhibit positive values CTE of +10.3 ppm/°C for Ba₂SiO₄, +6.2 ppm/°C for Sr₂SiO₄ and +7.0 for Ca₂SiO₄ [8]. For practical applications, in addition to the dielectric properties the materials should have high thermal conductivity and low thermal expansion, which are important parameters for heat dissipation and integration of these materials in microwave devices especially for hard ceramic substrate applications [9]. However, no attempt has been made to explore the microwave dielectric properties of ASiO₄ [A = Hf, Zr] group of materials. Recently we reported the microwave dielectric properties of ZrSiO₄ sintered at 1550 °C [10].

Synthetic HfSiO₄ is isostructural with ZrSiO₄ (zirconium orthosilicate) and is named as hafnon by Curtis et al. a terminology later approved by the International Mineralogical Association. Hf and Zr are observed to be alike in their chemical properties [7, 11]. Recently, HfSiO₄ was proposed as a high κ gate dielectric material for silicon electronics because of its high dielectric constant, low leakage current, excellent integrability and thermal stability with semiconductor Si [12,13,14,15]. Very recently, Pr³⁺ doped HfSiO₄ film was reported as a promising candidate for optoelectronic applications [16]. Interestingly, the recent years saw an influx of theoretical studies on the structural, elastic, electronic and optical properties of tetragonal orthosilicates which indicates the importance of HfSiO₄ [17, 18, 19]. The main objective of the present paper is to exploit the structural, dielectric and thermal properties of HfSiO₄ ceramics whose ultralow CTE can find potential applications in the ever growing microwave substrate market.

Results and discussion

The HfSiO₄ dielectric ceramics were prepared by solid-state reaction route. The room temperature powder X-ray diffraction pattern for HfSiO₄ fired at 1400, 1500, and 1600 °C is shown in figure 1. All the reflections can be indexed according to a single-phase tetragonal structure corresponding to the I41/amd (141) space group with a = 6.580 Å, b = 5.980 Å and density of 6.941 g/cm³. The indexing was done based on the standard ICDD file No (77-1759) belonging to hafnon, suggesting a completion of reaction between HfO₂ and SiO₂ which otherwise are present at firing temperatures ≤1500 °C.

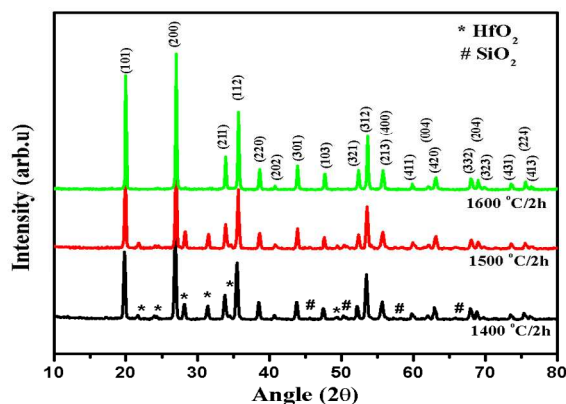


Figure 1 Powder X-ray diffraction pattern of HfSiO₄ ceramic sintered at 1600 °C

Figure 2 shows the infrared spectrum of HfSiO₄ ceramics sintered at 1600 °C. The strongest absorption peak is observed at ~ 915 cm⁻¹, which is ascribed to the asymmetric stretching vibration of the Si-O-Si bond [20, 21]. The peaks near 800 and 440 cm⁻¹ are ascribed to bending modes of O-Si-O and Si-O-Si bonds, respectively [21, 22]. The shoulder observed at 1020 cm⁻¹ is assigned to the Si-O-Si asymmetric bond stretching vibration [22]. Comparing with earlier reports, it can be suggested that HfSiO₄ ceramic is isostructural with ZrSiO₄ ceramic whose band at 615 cm⁻¹ in the spectra can be associated with the Hf-O bond in the HfO₈ group, similar to that of Zr-O bond in the ZrO₈ group [22].

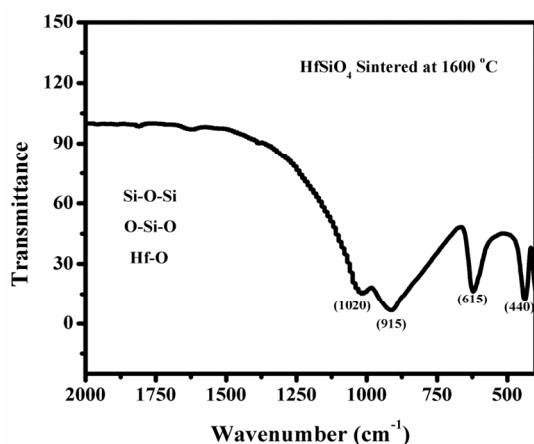


Figure 2 FTIR spectrum of HfSiO₄ sintered at 1600 °C

Figure 3 (a, b, c) shows the microstructure of HfSiO₄ sintered at 1400, 1500 and 1600 °C/2 hrs and has grains of variable sizes in the

range 1-5 μm. The presence of a few bigger grains can be observed in silicates sintered at 1600 °C/2hrs (figure 3a) which may try to reduce the internal energy by reducing the total area of grain boundary, resulting in subsequent grain growth. The figure 3 b and c indicates more porosity as compared to figure 3 a. The XRD and IR studies indicate the formation of HfSiO₄ and the ceramic found to be thermally stable at least up to 1600 °C.

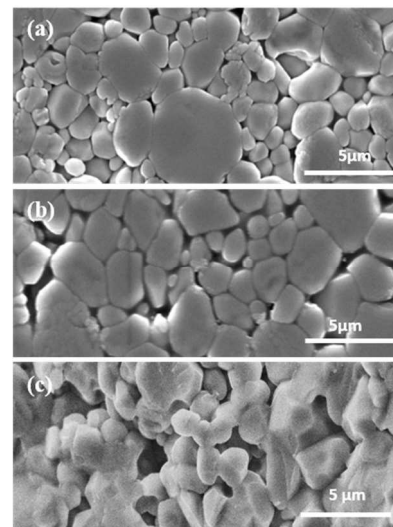


Figure 3 The SEM images of thermally etched HfSiO₄ ceramics sintered at 1400, 1500 and 1600 °C

Figure 4 (a, b, c) shows the variation of densification and dielectric properties of HfSiO₄ ceramic sintered in the temperature range of 1350-1650 °C. Sintering temperature of HfSiO₄ ceramic is optimized with the best of densification and dielectric properties. In the present investigation, HfSiO₄ ceramic shows a maximum densification of 95 % when sintered at 1600 °C. It is well known that, the reduction in surface free energy of a sintering compact due to the elimination of internal surface area associated with the pores, provides driving force for sintering [23]. It is clear from figure 4 (a) that further increase in the sintering temperature above 1600 °C cause a slight decrease in densification which is due to partial melting of the ceramic and hence leads to increases in porosity. Similar trends are also observed in the relative permittivity as a function of sintering temperature while the dielectric loss values of HfSiO₄ ceramics reach the lowest at a firing temperature of 1600 °C. It has relative permittivity of 9.71 and dielectric loss of 0.0007 at 1 MHz when sintered at 1600 °C. The porosity corrected relative permittivity at this temperature is 10.26, which is calculated using the equation derived by Penn et al. [24]. Figure 4 (b) shows the variation of relative permittivity of HfSiO₄ ceramic fired at various temperature ranges 1350-1600 °C as a function of frequency. Figure 4 reveals that the relative permittivity decreases with increase in frequency, which is consistent with the existing theories. It should be noted that the dielectric properties of ceramics are influenced by a number of factors such as processing conditions, phase transitions, purity of the raw materials, inherent crystal defects and porosity [25, 26]. At low firing temperature of 1350 °C, relative permittivity variations are from 20 to 5 and is because of low densification (~ 80 %) of HfSiO₄ ceramic compact. The sample sintered at higher temperatures the relative permittivity variations gradually decreases and reaches about 10 to 7 (200 Hz to 1 MHz) at 1600 °C and the ceramics have better densification of 95 %. This is because at lower densification

influenced by interfacial polarization, which is contributed mainly by polar and volatile molecules present in the pores.

Table 1 shows the microwave dielectric properties of HfSiO₄ ceramic sintered at 1600°C. It has relative permittivity (ϵ_r) of 7.0, quality factor ($Q_u \cdot f$) of 25,000 GHz and temperature coefficient of resonant frequency of -44 ppm/°C at 10 GHz frequency. The porosity corrected relative permittivity of this ceramic at this frequency is 7.4. The split post dielectric measurement performed on a square sheet of dimensions 50 x 50 mm made of HfSiO₄ ceramic sintered at 1600 °C also revealed similar microwave dielectric properties ($\epsilon_r = 7.5$ and $\tan \delta = 0.0006$ at 5 GHz). It must be noted that he relative permittivity

produce surface with characteristics required for applications. The AFM scan area used for the present study was $\sim 60 \times 60 \mu\text{m}$. The surface roughness (S_a) is calculated to be 118 nm, which is defined as arithmetic mean of the absolute values of the surface departures above and below the mean plane within the sampling area. The root mean square values of the surface departure within the sampling area (S_q) are given as 161 nm. A positive value of skewness (S_{sk}) = 2.2 indicates that the specified surface is more or less flatter with more peaks than valleys. It has a kurtosis value (S_{ku}) of 8.5 indicate that the surface is spiky since a centrally distributed surface has a kurtosis value larger than 3, whereas the kurtosis of a well spread distribution is smaller than 3 indicating bumpy surfaces [28].

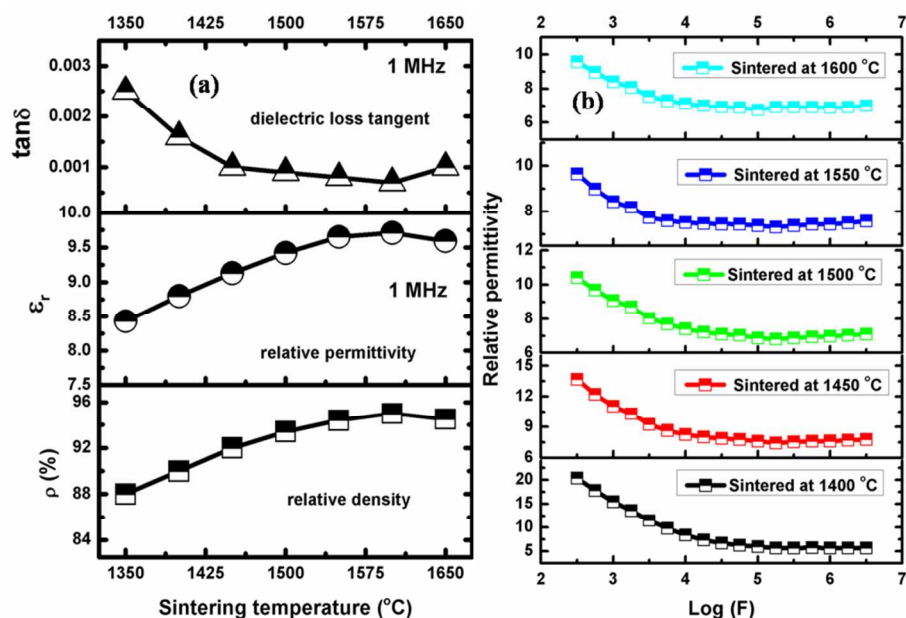


Figure 4(a) Variation of relative density, relative permittivity and dielectric loss with respect to sintering temperature and (b) Variation of relative permittivity with respect to frequency of HfSiO₄ ceramics

and dielectric loss of this HfSiO₄ ceramic is suitable for microwave substrate applications.

Table 1 Microwave dielectric properties of HfSiO₄ sintered at 1600 °C

Sample	Density (%)	Hakki and Coleman		Resonant Cavity Method	Qu*f
		ϵ_r	τ_f ppm/°C		
Cylindrical	95	10 GHz TE01 δ Mode			25000
		7.0	-44	2500	
Rectangular Sheet	95	SPDR Method 5 GHz			
		ϵ_r	$\tan \delta$		
		7.5	0.0006		

Figure 5 depicts the surface topography of HfSiO₄ ceramic sintered at 1600 °C. Surface roughness is of great importance for many technological applications requiring well-defined surfaces or interfaces [27]. Usually the desired surface engineering is achieved by various methods like machining, surface treatment and coating to

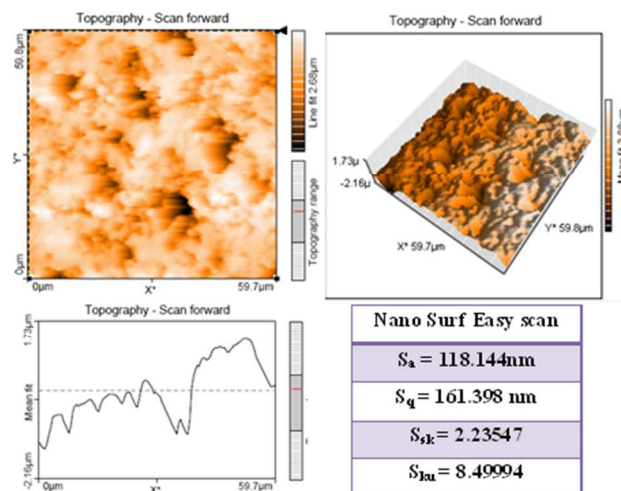


Figure 5 Surface topography of HfSiO₄ pellet sintered at 1600°C

Figure 6a shows the variation of the linear dimension of the HfSiO_4 ceramic as a function of temperature in the range of 30–800 °C. In general, brittle ceramic materials possess low thermal expansion as compared to metals and polymers. The thermal expansion of ceramics can be categorized in to distinct groups such as negative thermal expansion ($\alpha < 0 \text{ ppm}/^\circ\text{C}$), very low expansion ($0 < \alpha < 2$

positive volume thermal expansion of the unit cell, which is contradicting with the linear CTE measurements. In 1993, Yoshinori Kanno reported the thermal expansion coefficient of HfSiO_4 as 3.6 $\text{ppm}/^\circ\text{C}$ which is based on an earlier paper by B. Yoshiki in 1952 [30, 31]. In the present case the linear a and c axis CTE is 3.2 and 6.1 $\text{ppm}/^\circ\text{C}$ respectively.

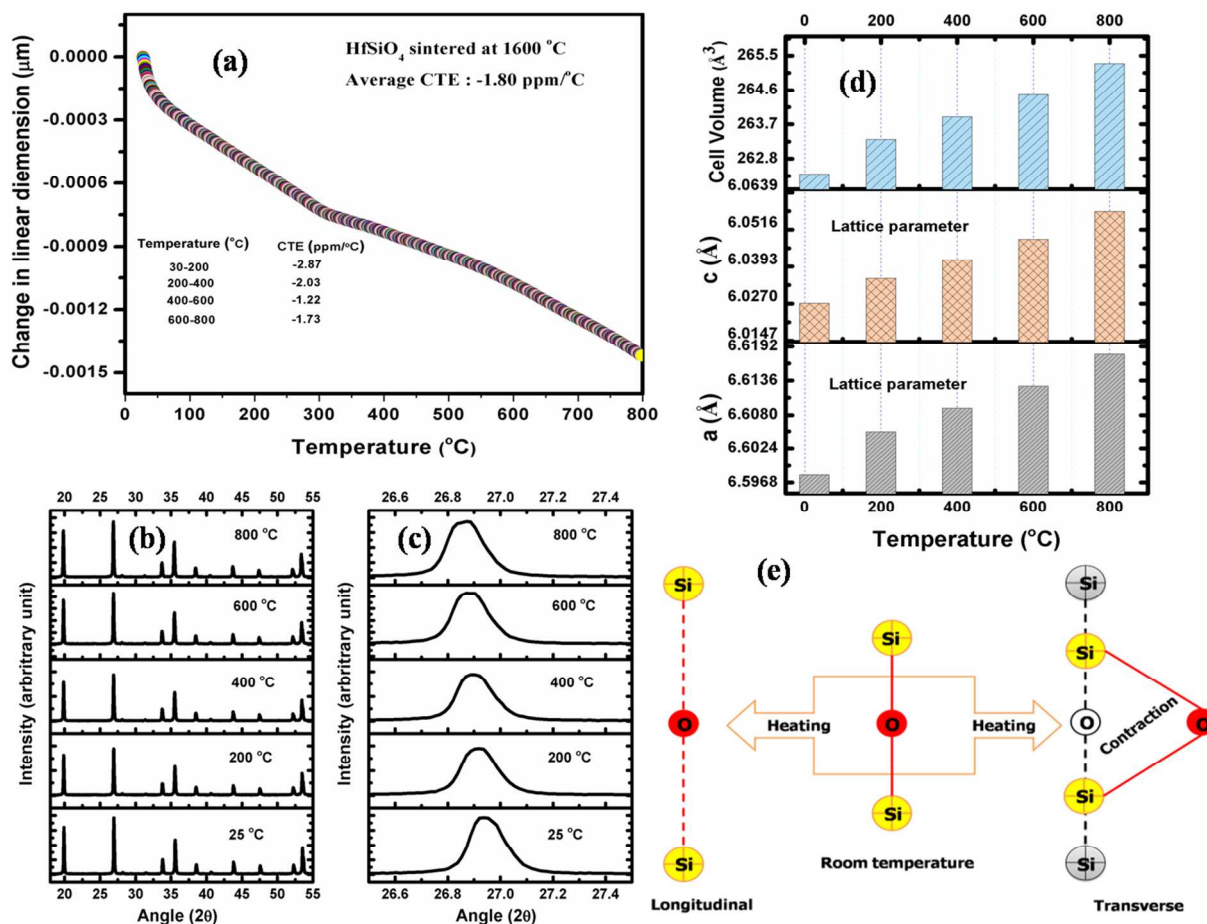


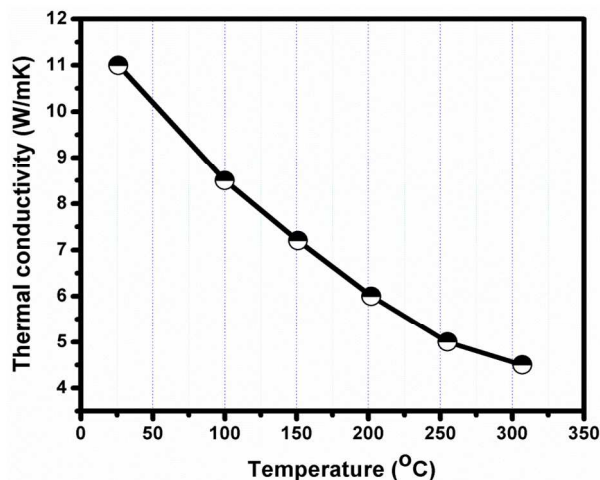
Figure 6 (a) Coefficient of thermal expansion (b) Temperature variable XRD (c) Schematic representation of bond vibrations (d) Calculated lattice parameters and cell volume of HfSiO_4 ceramic with respect to temperature and (e) a schematic of the behaviour of longitudinal and transverse vibrational modes on heating.

$\text{ppm}/^\circ\text{C}$), low expansion ($2 < \alpha < 8 \text{ ppm}/^\circ\text{C}$) and high expansion ($\alpha > 8 \text{ ppm}/^\circ\text{C}$) [29]. The HfSiO_4 is found to have a negative coefficient of thermal expansion (CTE) of -1.8 $\text{ppm}/^\circ\text{C}$ in the measured temperature range with an accuracy of 1 %. The average values of CTE calculated in different temperature ranges are also depicted in the Figure 6a. It can be observed that except at very low temperatures, CTE remains almost the same. In General, most of the electronic ceramics possess low positive coefficient of thermal expansion with a value $< 15 \text{ ppm}/^\circ\text{C}$. In order to confirm the expansion mechanism, temperature variable powder x-ray diffraction (in the range 25–800 °C) of HfSiO_4 ceramic sintered at 1600 °C was studied, which is given in Figure 6 b and c. Figure 6 c represents high intensity plane (100) varies with respect to the temperature ranges, a shift to low angle is clear from the results. Similarly figure 6 d depicts the variation of lattice parameter and cell volume of tetragonal HfSiO_4 with respect to temperature. Thus the in-situ XRD results indicates that HfSiO_4 ceramic sintered at 1600 °C shows the

However, there are other factors such as mechanical stress and porosity of the sample which may also influence the CTE measurements [32]. Hence, based on the above observations, we believe that the apparent negative thermal expansivity of HfSiO_4 is not contributed by a visible lattice expansion on heating, but due to some other mechanisms which may intimately be related to their flexible network characteristics. Interestingly, most of the negative CTE ceramics have a common framework structures like rigid tetrahedra (MO_4) and/or octahedra (MO_6) which are connected by corner sharing oxygen atoms [33]. These linkages are typical in metal oxides such as ZrW_2O_8 -type solids and zeolites and are fundamental to their negative CTE behaviour [33]. Usually, the negative thermal expansivity materials are characterized by strong atomic bonds, which often have large Pauling bond strengths, Z/p where Z is the cation charge and p is the cation coordination number such as W-O and Si-O . The two important phonon modes for metal oxides are longitudinal and transverse vibrations of Si-O-Si which were clearly visible in the FTIR spectra HfSiO_4 given in figure 2. As

represented in Figure 6e, it can be assumed that in silicates such as HfSiO_4 , the longitudinal vibrational modes tend to increase the bond length on heating. In contrast, the transverse vibration modes can have an opposing effect on the Si-O-Si bond length. The dominance of transverse modes over the longitudinal modes may be one of the reasons for the negative CTE of the HfSiO_4 ceramics in the CTE measurements. As a consequence, the dynamical deformation consumes open spaces in a crystal lattice under the condition that the thermal expansion of the core unit such as SiO_4 is suppressed [34]. However further studies are required for explaining the mismatch between the CTE obtained by XRD and dilatometer methods. The coefficient of thermal expansion value of commercially available substrates like silicon, silicon nitride, boron nitride, silicon carbide, aluminum nitride, alumina, berillia are 3.7, 3.8, 4.3, 6.8 and 7.8 ppm°C respectively [9]. Compared to the commercial substrates, HfSiO_4 falls into the category of ultralow CTE materials with high temperature stability.

Figure 7 shows the variation in the thermal conductivity (λ) of HfSiO_4 ceramics sintered at 1600°C as a function of temperature in the range of 25 to 300°C . Most of the silicates have relatively low thermal conductivities since they include both rigid and soft parts in the structure. HfSiO_4 ceramic shows decrease in thermal conductivity value with increase in temperature. Majority of non-metallic compounds also exhibit a similar behaviour with increasing temperature [35, 36, 37]. HfSiO_4 ceramic has thermal conductivity in the range of 6–11 W/mK in the measured temperature range. Li_4SiO_4 and Y_2SiO_5 ceramic which belong to orthosilicates family have thermal conductivities 4.1 and 4.6 W/mK respectively at room temperature [38, 39]. In comparison with reported thermal conductivity of Li_4SiO_4 ceramic, HfSiO_4 ceramic shows a higher value of thermal conductivity (11 W/mK) at room temperature for a sample with 95 % of densification.



In Figure 7 Variation of thermal conductivity of HfSiO_4 ceramic sintered at 1600°C as a function of temperature

most of the ceramic samples, heat transport is contributed by phonon scattering mechanism which is obviously inefficient in an environment with porosity. Hence we estimated the porosity corrected thermal conductivity of HfSiO_4 ceramic at room temperature which is 11.95 W/mK , calculated using the equation $\text{TC} = \text{TC}_{\text{corrected}} + (1-\delta)$, where δ is porosity of the sample and $\text{TC}_{\text{corrected}}$ is experimental thermal conductivity [40].

Experimental

HfSiO_4 ceramic was prepared by the conventional solid state reaction route. Equimolar amounts of HfO_2 (98% Sigma Aldrich, St. Louis, MO-63103 USA) and SiO_2 (99.9% Sigma Aldrich, St. Louis, MO-63103 USA) were ball milled for 24 hrs in a polyethylene bottle using yttria stabilized ZrO_2 balls and distilled water. The mixed powder was dried and calcined successively at 1400°C for 2 hrs. The calcined powder was ground and mixed with the 4 wt% polyvinyl alcohol (BDH Lab Suppliers, England) and shaped into disks of 11 mm diameter and ~ 2 mm height under an applied pressure of 100 MPa using a tungsten carbide (WC) die. The green compacts were heated in air at a rate of $6^\circ\text{C}/\text{min}$ up to 600°C and held at this temperature for 30 min for binder burnout. The pellets were then sintered in air at temperatures in the range of 1350°C to 1650°C for 2 hrs. The cooling rate was $2.5^\circ\text{C}/\text{min}$ until it reached 800°C and was then allowed furnace cooling. The sintering temperature was optimized for the highest density and best of dielectric properties. The bulk densities of the developed ceramic samples were measured using Archimedes method.

The crystal structure and phase purity of the powdered samples were studied by X-ray diffraction technique using Ni-filtered $\text{Cu-K}\alpha$ radiation (Philips X'pert PRO MPD XRD; Philips, Eindhoven, Netherlands) operated at 40 kV and 30 mA. The microstructures of the thermally etched sintered samples were analyzed using scanning electron microscope (JEOL-JSM 5600 LV, Tokyo, Japan). The surface roughness of the sintered HfSiO_4 ceramic was measured using Nano Surf Easy Scan Atomic Force Microscopy (AFM) operating in tapping mode and the AFM sampling area used for the measurements was $\sim 60 \times 60 \mu\text{m}$. The AFM cantilever, whose tip is made of $15 \mu\text{m}$ long monolithic silicon, has a width of $49 \mu\text{m}$, thickness $2.2 \mu\text{m}$ and spring constant 0.21N/m . For IR transmission measurements (Shimadzu Nicolet Impact 400X FTIR Spectrometer), the samples were prepared by mixing HfSiO_4 ceramic powder with potassium bromide (KBr) and pressed into pellet under a pressure of ~ 300 bar. FTIR spectrum was recorded on a Nicolet 400X spectrometer with resolution of 2cm^{-1} , accumulating 200 scans. The dielectric properties at 1 MHz were measured using a LCR meter (Hioki 3532-50 LCR HiTESTER, Nagano, Japan). The error associated with the LCR measurement is less than $\pm 2\%$ for relative permittivity as well as dielectric loss. The microwave measurements of relative permittivity, unloaded quality factor and temperature coefficient of resonant frequency were performed by using a Vector Network Analyzer (E5071C, Agilent Technologies, Santa Clara, CA). The microwave relative permittivity and dielectric loss of $50 \times 50 \text{ mm}$ square sheet of sintered HfSiO_4 ceramic were measured using Split Post Dielectric Resonator (SPDR) method at operated 5 GHz using Vector Network Analyzer. Generally, the error associated with SPDR method in computing relative permittivity is less than 1% while it is $\pm 5\%$ for dielectric loss measurement with a loss tangent resolution of 2×10^{-5} [41, 42].

The coefficient of thermal expansion was measured using a push rod dilatometer (NETZSCH, Model DIL 402 PC, Germany) with an accuracy less than 1%. The thermal expansion tests were performed according to the specifications of ASTM E228 test method with sintered cylindrical samples of diameter 8 mm and height 10 mm in the temperature range of $30\text{--}800^\circ\text{C}$. The thermal conductivity is computed from the thermal diffusivity (mm^2/s), specific heat (J/g K) and density (g/cm^3) [43] which were measured using a thermal diffusivity system (Anter Corporation, Flashline 2000, Pittsburgh, PA). The flash method with high speed xenon discharge pulse source directed to the top face of the specimen to increase the temperature of the specimen by ΔT as a function of time. The specimens used for thermal diffusivity testing were in the form of a disc, with a diameter

of 12.6 mm and a thickness of 2 mm. Specimen preparation involves ensuring the required smoothness and flatness of surfaces, by polishing with a 400 grit SiC grinding paper, followed by coating both sides of the sample with graphite for thermal contacts in order to avoid reflection of the xenon discharge light beam. In a typical measurement, three specimens were tested, at different temperature range up to 300 °C. The error associated with thermal property measurement is $\pm 1\%$.

Conclusions

The dielectric and thermal properties of HfSiO₄ ceramic prepared by conventional solid state ceramic route are investigated. The HfSiO₄ ceramic sintered at 1600 °C has a tetragonal structure with a 95 % densification. It has $\epsilon_r = 7.0$, $Q \times f = 25,000$ and $\tau_f = -44$ ppm/°C at frequency of 10 GHz. It exhibits a unique negative CTE of -1.8 ppm/°C in the 30-800 °C temperature range. These ceramics also show a promising room temperature thermal conductivity of 11 W/mK. Thus low dielectric loss and low CTE HfSiO₄ ceramic which satisfies the dielectric and thermal requirements of microwave substrates have been developed for wide range of substrate applications.

Acknowledgements

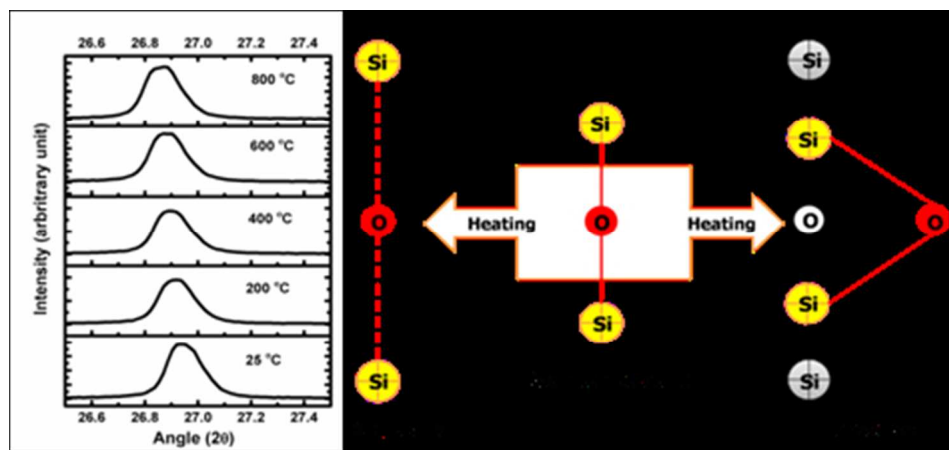
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Notes and references

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80x37mm (150 x 150 DPI)