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ARTICLE

Direct Growth of Etch Pit-Free GaN Crystals on Few-Layer Graphene

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We report high-quality GaN crystals grown directly on graphene layers without a buffer layer by metal-organic chemical vapour deposition. Photoluminescence and Raman spectra revealed that GaN crystals grown on graphene layers had mild strain as compared to those grown on sapphires and SiO₂ substrates. Etch pits were not observed on the surface of GaN/graphene, in which threading dislocations were diminished inside the bulk. This is markedly different from GaN/sapphire, in which threading dislocations were present on GaN surfaces. This opens a new possibility that graphene with π electrons and hexagonal symmetry could be an ideal substrate for GaN crystal growth instead of expensive sapphire substrates.

Introduction

Group III nitrides of inorganic compound semiconductors are widely used for light-emitting diodes and high-power/high-frequency electronic devices.¹⁻² Control of GaN crystal quality with a minimum threading dislocation density (TD) is a key step to realizing such devices.³⁻⁵ Sapphire is the most commonly used substrate, but the mismatch of crystal lattices and thermal expansion coefficients between GaN and the sapphire substrate causes dislocations on the growing GaN. Developing a new type of substrate with low cost and easy handling is highly required.

Graphene is a layered structure of hexagonally arranged carbon atoms with strong σ bonds within the plane, whereas weak π electrons are exposed to the surface. Thus, the interaction of graphene with other materials is generally weak. For example, if GaN is grown on a graphene substrate, the lattice mismatch may be very different from that of GaN grown on sapphire, which may reduce the dislocation density or generate different types of dislocations. Ideally, the strain energy could be reduced owing to the weak interaction between graphene and the host material. Therefore, graphene could act as an ideal substrate for the crystal growth of numerous materials.⁶⁻¹⁰ In this report, we demonstrate this concept by introducing graphene as a substrate for direct growth of GaN instead of sapphire. Highly crystalline GaN (0001) on a graphene substrate was synthesized by metal-organic chemical vapour deposition (MOCVD) without using a buffer layer. No yellow band in the photoluminescence (PL) spectra was observed. Tensile stress accumulated on the GaN/graphene, but its magnitude was smaller than that of GaN/sapphire. Unlike GaN/sapphire, which shows threading dislocations on the GaN surface, etch pits were not observed on the surface of the

GaN/graphene, where the threading dislocations were diminished inside the bulk material.

Results and discussion

Figure 1 shows the field emission scanning electron microscope (FESEM) images with related schematics of GaN crystal growth on graphene. The pristine graphene on SiO₂ (300 nm)/Si (Fig. 1a), which was transferred from chemical vapour deposition (CVD)-grown graphene on a Ni substrate, shows typical wrinkles and bumpy structures. The white arrows indicate wrinkles, which are generated by differences in the thermal expansion coefficients of the Ni substrate and the graphene layers caused by thermal quenching during CVD.¹¹ The black arrows show bumpy structures, which are formed during the etching and transfer process on SiO₂/Si substrates.¹² The inset image indicates a few layers of graphene transferred onto the polyethylene terephthalate (PET, 4 × 3.5 cm) film. This transferred graphene film has a transmittance of 80% at 550 nm or equivalently ~10 layers of graphene. The small D/G intensity ratio in the pristine graphene indicates the high quality of the grown graphene (see the supporting information, Fig. S1). After ozone treatment in the atomic layer deposition (ALD) chamber, the D-band intensity increased significantly. Oxygen functional groups such as epoxy and/or hydroxyl groups were likely formed owing to abundant defective sites in the CVD-grown graphene, as shown in the schematic of Fig. 1a. These sites may act as nucleation seeds for direct GaN growth. A two-temperature scheme was introduced to enhance the number of nucleation seeds for GaN grown directly on graphene: a low temperature of 530 °C for 5 min was used to form nucleation seeds and a high temperature of 1040 °C for 3 h was used for further growth. This approach is in contrast to a previous report in which a ZnO layer was used as a buffer layer prior to GaN

growth.¹³ Small agglomerates of GaN with sizes less than 100 nm were formed at the early low-temperature regime (Fig. 1b). The number of GaN nucleation seeds generated depended on the nucleation times (SI, Fig. S2). The wrinkles and bumpy structures on graphene also acted as nucleation seeds. With further growth at high temperature, GaN crystallites were formed with lateral sizes of several tens of micrometres and a height of 10 μm (Fig. 1c). Well-developed facets with various orientations were also visible from each crystallite, where the (0001) growth direction was retained from each crystallite.

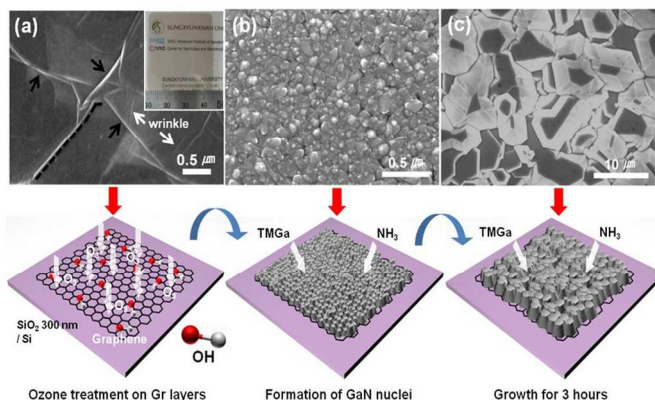


Fig. 1. FESEM images and corresponding schematics of GaN crystal growth on graphene. (a) As-synthesized graphene surface. White arrows indicate wrinkles. Black arrows indicate bumpy structures that formed during the graphene transfer process. The inset shows the transferred graphene on PET film (4×3.5 cm) with a transmittance of 80% at 550 nm. (b) Formation of initial nucleation seeds grown at 530 $^{\circ}\text{C}$ for 5 min. (c) GaN crystals grown at 1040 $^{\circ}\text{C}$ for 3 h following an initial nucleation step.

Figure 2a shows the photoluminescence (PL) spectra at room temperature of the grown GaN samples. GaN grown on the graphene (Gr)/SiO₂/Si and Gr/sapphire substrates showed clear excitonic PL peaks at 3.38 and 3.39 eV, respectively, without a yellow luminescence band. This is similar to the GaN grown on sapphire except for a slight peak shift from 3.42 eV, but it is in contrast with GaN grown on the SiO₂/Si substrate, in which a yellow luminescence (YL) band was observed at approximately 2.2 eV (band width ~ 1 eV), representing defect formation in the GaN film. The values for the full width at half maximum of the excitonic PL peaks for the three substrates were similar to each other, indicating that GaN crystallites grown on graphene have a crystal quality similar to that of GaN grown on sapphire. The respective red shifts of 0.03 and 0.04 eV for the GaN grown on the Gr/SiO₂/Si and Gr/sapphire substrates as compared to GaN grown on sapphire could be related to strain relaxation, which will be discussed later.

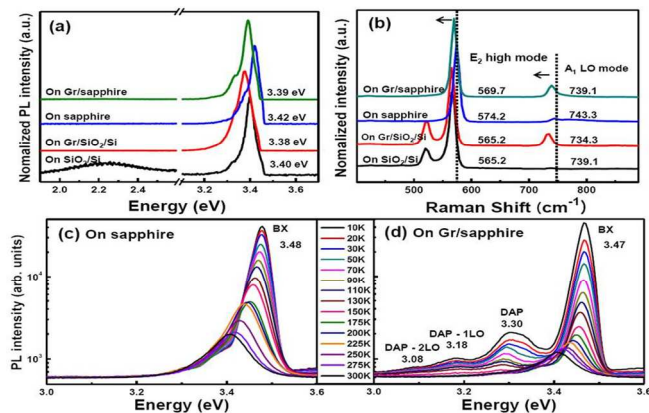


Fig. 2. (a) PL spectra from GaN crystals grown on sapphire, graphene, and SiO₂ substrates at a temperature of 300 K. A diode-pumped solid-state (DPSS) laser with a 355 nm wavelength was used as the excitation light. (b) Raman spectra from GaN samples grown on four types of substrates (Gr/sapphire, sapphire, Gr/SiO₂/Si, and SiO₂/Si) with an excitation wavelength of 532 nm. The dashed line shows the red-shift of the GaN crystal on graphene substrate. (c) and (d) Temperature-dependent GaN PL spectra from 10 K to 300 K on the sapphire and graphene substrates, respectively. The excitation wavelength was 325 nm. PL emission lines are labeled “BX” and “DAP” which represent luminescence from bound exciton and donor acceptor pair, respectively.

The presence of residual stress of the GaN film is well represented by the frequency shift and width of the E₂^H mode.^{14–18} Figure 2b shows clear E₂^H and A₁ LO modes for the four samples at room temperature. The E₂^H mode of strain-free GaN is 567 cm⁻¹ (see Table 1). The GaN film grown on the sapphire substrate induces general compressive strain, whereas the GaN film on the silicon substrate induces tensile strain.¹⁹ In Fig. 2b, the blue shift of the E₂^H peak frequency by 7.2 cm⁻¹ for the GaN directly grown on sapphire and by 2.7 cm⁻¹ for the GaN grown on the Gr/sapphire substrate corresponds to compressive strain, although the latter is milder than the former. This indicates that the strain is minimized by using graphene as a substrate. On the other hand, the red shift by 1.8 cm⁻¹ in the GaN grown on the Gr/SiO₂/Si and SiO₂/Si substrates is an indication of mild tensile stress. This implies that the graphene transferred onto different substrates is slightly influenced by the substrate type, presumably owing to the surface morphology of the substrate.

Table 1. The grown GaN relationships of substrate/equipment/Raman (E₂^H)/PL/Thickness/TD density/Stress/Nucleation layer

Substrate	Equip.	Raman (E ₂ ^H)	PL (eV)	Thickness (nm)	Threading dislocation density (cm ⁻²)	Stress	Nucleation layer
Ref 14	...	HVPE	567	...	50	3 × 10 ⁶	...
Ref 15	sapphire	HVPE	569.1	3.42	80	<10 ⁶	...
Ref 16	Si(111)	MOCVD	565.2	3.405	1.5, 2.5 (Ref. 24)	~10 ⁶ (Ref. 24)	Tensile GaN / AlN
Ref 17	6H-SiC	MBE	565.3	3.414	2–5	4 × 10 ⁶ (Ref. 21)	Tensile AlN
Ref 18	sapphire	MOCVD	568.5	3.432	2–5	...	compressive ...
This work	Graphene/SiO ₂ /Si	MOCVD	565.2	3.38	10	Total dislocations density (cm ⁻²) 7.6 × 10 ⁶	Tensile GaN

Figures 2c and 2d show the PL spectra of GaN on sapphire and on Gr/sapphire as a function of temperature from 10 K to 300 K. The spectra are dominated by neutral donor-bound recombination.²⁰ Red shifts and broadening of all emission lines were observed as the temperature decreased. The

temperature dependence of the line width is attributed to phonon-induced band broadening.²¹ The characteristic red shift with increasing temperature is attributed to band gap reduction from lattice expansion and to electron–phonon interaction.²² As shown in Fig. 2c, no donor–acceptor pairs (DAP) were observed in GaN grown on sapphire. On the other hand, DAP lines were clearly visible in the GaN grown on Gr/sapphire. As shown in Table 2, the gaps between DAP, DAP-1LO, and DAP-2LO are nearly regulated with an interval of ~ 100 meV in the GaN grown on Gr/sapphire. Carbon could be a source of acceptor sites owing to the presumably disassembled graphene layers during nucleation of GaN by pyrolysis at high growth temperatures. Carbon doping in GaN crystals is generally obtained by implantation or by using CBr_4 gas.^{23,24} In these methods, defects in GaN crystals are generated by physical force and etching, resulting in the appearance of a YL band. In the current case of Gr/sapphire, the carbon atoms were doped naturally by pyrolysis, without creating defect sites by physical damage.

Table 2. Comparison of DAP according to various dopant concentrations and temperatures

Dopant concentration (cm^{-3})	Temperature (K)	PL (eV) : donor - acceptor pair (DAP)
Mg ^[36] : $\sim 10^{17}$	4	DAP : 3.26 DAP - 1LO : 3.17
Be ^[37] : $\sim 10^{18}$	5	DAP : 3.38 DAP - 1LO : 3.29 DAP - 2LO : 3.20
Si ^[38] : $\sim 10^{17}$	2	DAP : 3.27 DAP - 1LO : 3.18
C ^[39] : $\sim 10^{18}$	2	DAP : 3.15
This work	10	DAP : 3.30 DAP - 1LO : 3.18 DAP - 2LO : 3.08

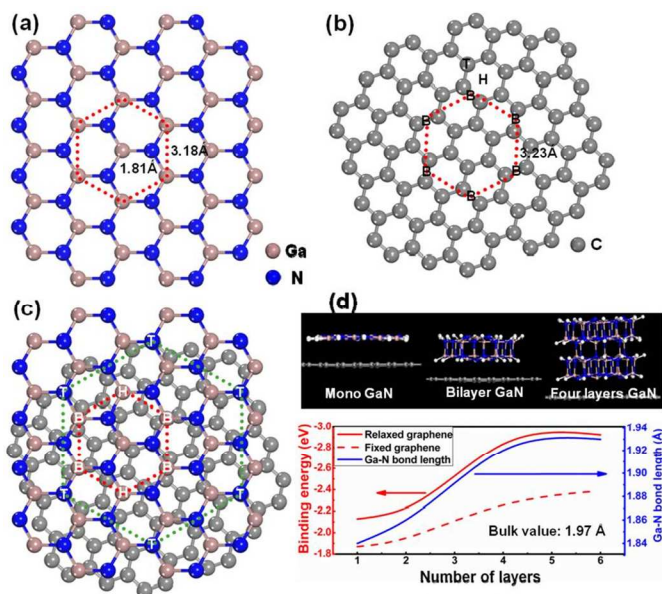


Fig. 3. (a) Lattice constants of GaN and (b) graphene super cell are 3.18 Å and 3.23 Å, respectively (B: bridge site, H: hexagonal site, T: top site). (c) Overlapped super cell structure of (a) and (b) indicate Ga-polar GaN grown on graphene. (d) Binding energy of GaN pair on graphene.

The mild tensile stress in GaN grown on Gr/SiO₂/Si is markedly different from the high compressive stress accumulated in GaN grown on Gr/sapphire. Figure 3 shows the theoretical simulation data based on density functional theory, which is generally used in nanostructure calculations.²⁵ Figure 3a shows the ball-and-stick atomic structure of the GaN monolayer. The Ga–Ga or N–N distance is 3.18 Å. This closely matches the separation distance (3.23 Å) of the next bond-centre site of graphene, as shown in Fig. 3b. Every Ga and N atom is positioned at the bond-centre site of graphene. Overlapping a super cell of GaN on the graphene layer resulted in an alternative location of N atoms at every other top site of graphene (indicated by green dashed line) and Ga atoms alternatively at H (hexagonal) sites and bond-centre sites of graphene (red dashed line) (see Fig. 3c). As the number of Ga and N atoms increases to form a monolayer, the GaN structure is converted into a hexagonal phase. The Ga–N bond length approaches that of the bulk material, as shown in Fig. 3d, as the number of layers increases. Maintaining hexagonal symmetry and minimum strain as compared to hexagonal GaN/sapphire is an advantage of using graphene as a substrate. The graphene layer could be strained by the substrate, which is evidenced by the difference in the binding energy with and without relaxation of the graphene layer. On the other hand, the interfacial binding energy increases, becoming saturated to less than -3.0 eV per GaN at six GaN layers. This is the origin of stress of the grown GaN, even with a graphene substrate. At high growth temperatures, therefore, the grown GaN layers, in particular those near the interface, are expected to be slightly strained. Nevertheless, this interfacial energy is smaller than that of GaN/sapphire, as shown in the calculated strain, stress, and frequency shift in Table 3. As a consequence, we expect the strain developed on the graphene surface to be presumably smaller than on sapphire from the theoretical simulation data.

Table 3. Strain comparison of GaN on Gr/SiO₂/Si, sapphire, and Gr/sapphire. E_2^H mode phonon frequency of bulk is 567 cm^{-1} (see Kitamura et al.¹⁴)

	GaN on Gr/SiO ₂ /Si	GaN on sapphire	GaN on Gr/sapphire
$\Delta\omega$ (cm^{-1})	-1.8	+7.2	+2.7
σ_{xx} (GPa)	-0.7	+2.8	+1.1
ε (%)	0.2 ~ 0.4	0.9 ~ 1.8	0.4 ~ 0.6

$$\Delta\omega$$

$$\Delta\omega : E_2^H \text{ phonon frequency}$$

$$C : \text{linear stress coefficient} (\text{cm}^{-1} / \text{GPa}) \rightarrow 2.56 \text{ cm}^{-1} / \text{GPa}$$

$$\sigma_{xx} : \text{biaxial stress (GPa)}$$

$$\text{bulk GaN} \rightarrow 567 \text{ cm}^{-1}$$

$$E = \text{stress} / \text{strain} = \sigma / \varepsilon$$

$$E : \text{Young's modulus of bulk GaN} \rightarrow 160 \sim 290 \text{ GPa}$$

As shown in Table 3, using the phonon frequency (E_2^H) change with respect to the bulk value (567 cm^{-1}),¹⁴ $\Delta\omega$ (cm^{-1}) = $C \sigma_{xx}$ formula,²⁶ already well known relationship to calculate the strain of GaN grown on sapphire and that of GaN grown on Gr/sapphire simultaneously, the biaxial stress (σ_{xx}) was extracted. The linear stress coefficient, C , is a constant value. Young's modulus ($E = \sigma/\varepsilon$) was used to obtain the strain value by substituting the value of the obtained stress. The mild tensile stress on GaN/Gr/SiO₂/Si was expected owing to the weak

interaction between graphene and GaN. The strain of GaN/Gr/sapphire is approximately three times smaller than that of GaN/sapphire, as expected from the theoretical arguments given in the previous paragraph.

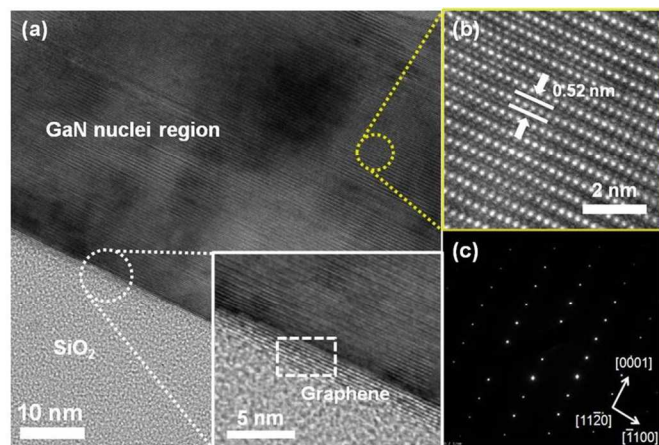


Fig. 4. (a) Low-magnification cross-sectional TEM image of GaN nuclei region on graphene. The number of graphene layers is shown in the inset image. (b) The corresponding high-resolution TEM image. (c) Electron diffraction pattern of GaN crystal grown on Gr/SiO₂/Si.

Figure 4a shows the cross-sectional transmission electron microscope (TEM) images of GaN near the interface of graphene. The inset image clearly shows no defects such as voids or cracks. Multilayer (~10 layers) graphene with slight variations in thickness, which is a characteristic of multilayer graphene on a Ni substrate,¹¹ was also clearly visible at the interface. In addition, the high-resolution TEM image of GaN crystals in Fig. 4b indicates a well-aligned crystal lattice array. The lattice spacing between the nearby planes is 0.52 nm, in good agreement with the d-spacing of GaN (0001) planes grown on sapphire.¹³ The electron diffraction pattern shows a (11 $\bar{2}$ 0) zone axis pattern of the grown GaN crystal on graphene in Fig. 4c (see also SI, Fig. S3).

GaN epitaxial layers can be grown, in general, by the well-known two-step MOCVD method on (0001) sapphire.²⁷ These layers contain a high density of threading dislocations (TDs). There are three types of TDs in GaN: pure edge, pure screw, and mixed types. Epitaxial layers predominantly contain pure edge-type TDs that have a line direction perpendicular to the GaN sapphire interface. As a consequence, edge TDs are generated as a result of the large lattice mismatch between GaN and sapphire. TDs are detrimental to electronic and photoelectric devices. The TD density is, therefore, a critical parameter for judging the quality of GaN films. To understand the formation of TDs in GaN grown on Gr/SiO₂/Si, we inspected two TEM cross-sectional micrographs. It is commonly presumed that TDs evolve from the coalescence of tilted and twisted islands during the initial stages of GaN deposition.²⁸ Bright-field TEM images, recorded under two-beam conditions in order to image all dislocations with different Burgers vectors, were used to estimate the density. Figures 5a and b show the cross-sectional TEM images of the sample grown on the Gr/SiO₂/Si substrate. The TD types in the GaN crystal were determined by applying the $g \cdot b$ criterion to the two-beam images of the cross-sectional TEM specimen (g is a reciprocal lattice vector and b is a Burgers vector), which

were recorded near the [11 $\bar{2}$ 0] zone axis with $g = [1\bar{1}00]$ in Figs. 5a and b.²⁹

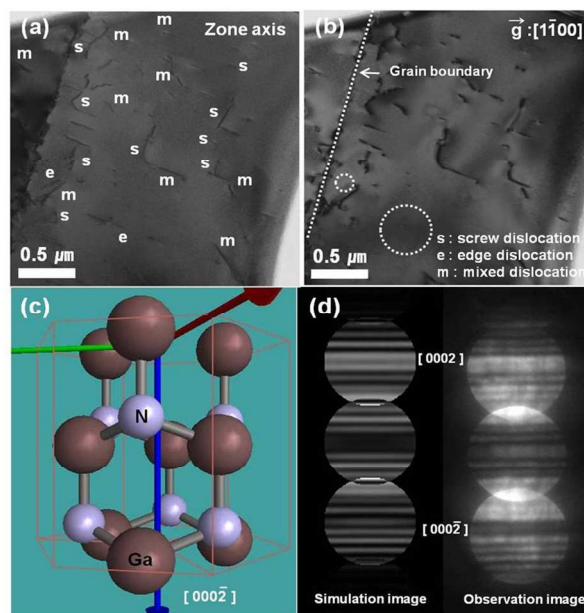


Fig. 5. Cross-sectional two-beam bright-field TEM images of GaN crystal on graphene: (a) Zone axis and (b) $g = [1\bar{1}00]$ image showing screw and mixed dislocations. (c) Convergent beam electron diffraction (CBED) simulation structure of stacking-grown GaN on Gr/SiO₂/Si and (d) simulated and observed CBED patterns for 160 nm thickness.

Here we discuss pure edge dislocations in more detail. The dislocations shown only in $g = [1\bar{1}00]$ of the two-beam image are pure edge dislocations because threading dislocations have a dislocation line vector parallel to the c -axis, as shown in Fig. 5b. On the other hand, the dislocations shown in the [11 $\bar{2}$ 0] zone axis of the two-beam image are pure edge, pure screw, and mixed dislocations, as shown in Fig. 5a. This indicates that the predominant dislocations in GaN crystals grown on Gr/SiO₂/Si were screw- and mixed-type dislocations. The numbers of edge-, screw-, and mixed-type dislocations for GaN crystals grown on Gr/SiO₂/Si differed from those of previous studies on sapphire substrates, which revealed pure edge-type dislocations only.³⁰⁻³³ The predominance of mixed-type dislocations on Gr/SiO₂/Si may be attributed to a higher density of screw-type dislocations as compared to those on GaN on sapphire. The dashed line in Fig. 5b is the grain boundary of the GaN crystal. The number of TDs was counted at random positions on a scale of 2 μm . The estimated total TD density ranged from $8 \times 10^8 \text{ cm}^{-2}$ to $3.6 \times 10^9 \text{ cm}^{-2}$, which is comparable to that of GaN grown on sapphire, 6H-SiC, and Si(111) but slightly higher than that of GaN grown on sapphire substrates (see Table 1).

Convergent beam electron diffraction (CBED) is one of the most powerful techniques for determining the polarity of GaN crystals on graphene.³⁴ Since GaN is a non-centrosymmetric crystal, JEMS software was used to simulate the index of (0002) and (000 $\bar{2}$) directions of the experimental patterns and consequently to deduce the nanostructure polarity (Figs. 5c and d). The intensity distribution change also depends on the sample thickness. The simulation and experimental disks

agreed well with each other, indicating that these crystals were grown with Ga polarity.

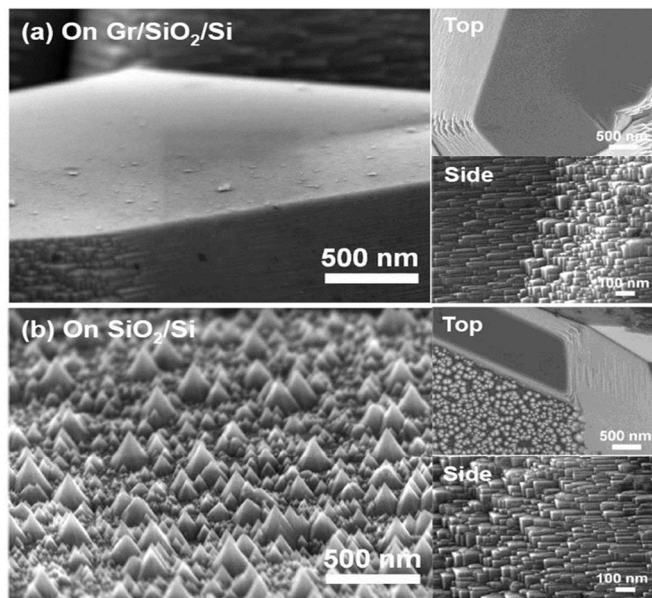


Fig. 6. FESEM images of GaN grown on (a) Gr/SiO₂/Si and (b) SiO₂/Si after etching for 20 min in 0.5 mol KOH. The right images show top and side views of GaN after etching.

Unlike typical GaN crystals grown on sapphire, GaN grown on Gr/SiO₂/Si in the current study generated edge dislocations perpendicular to the (0001) growth direction, and therefore most dislocations were buried within the GaN crystals. Thus surface etch pits were not visible on the surface of GaN grown on Gr/SiO₂/Si (Fig. 6a). Even after surface etching with 0.5 mol KOH at 80 °C for 20 min, etch pits were not visible, as shown in the two side images of Fig. 6a. On the other hand, for GaN/SiO₂/Si, several large etch pits and many small etch pits were formed, as shown in Fig. 6b. An etch pit density (EPD) of approximately $8 \times 10^9 \text{ cm}^{-2}$ was obtained, which is similar to the EPD of GaN grown on the sapphire substrate. The shape of the etch pits in the top view was clearly different from that of the side view, which shows cylindrical protrusions. Most dislocations were generated perpendicular to the growth direction. To demonstrate the quality of GaN crystals grown on graphene, we fabricated a light emitting diode(LED) using five-period InGaN/GaN multi-quantum wells (MQWs) (see SI, Fig. S4). Light emission was demonstrated from the as-fabricated LED. This opens the possibility of fabricating flexible LEDs in the near future.

Conclusion

We have grown high-quality GaN crystals directly on graphene layers without buffer layer deposition by MOCVD. The quality and microstructures of these GaN crystals revealed several unique features as compared to GaN directly grown on sapphire or SiO₂ without graphene: i) An (0002) GaN peak perpendicular to the substrate was dominant, ii) the GaN crystal showed mild strain, iii) screw and screw mixed with edge dislocations were dominant, and iv) threading dislocations were spread inside the GaN bulk and no etch pits were formed on the

GaN surface. Therefore, we believe that graphene with π electrons and hexagonal symmetry could be an ideal substrate for numerous hexagonal crystal growth systems, in particular for layered structures such as BN and transition metal dichalcogenides. Further research is required to investigate the dependence of quality of crystals on the method of synthesis and the related growth parameters.

Experimental section

Preparation of graphene substrate

A 300 nm Ni film on a SiO₂ 300 nm/Si substrate was prepared by electron beam (e-beam) evaporation. The growth method was similar to that described in a previous study.¹¹ Poly(methyl methacrylate) (PMMA) (e-beam resist, 950k C4, MicroChem) was spin coated onto the graphene/Ni film at 1000 rpm for 60 s. The sample was soaked in nickel etchant (Type I, Transene) for 3 h. After rinsing by deionized water, the PMMA/graphene layers were fished onto the SiO₂ (300 nm)/Si wafer and the PMMA was removed by acetone. Commercial atomic layer deposition (ALD) equipment (Lucida D100, NCD Technology, Korea) was used for ozone treatment at 100 °C. Ozone gas was supplied for 3 s and purged with N₂ gas for 2 s, which comprised one cycle. This was repeated for 50 cycles.

MOCVD growth of GaN films on graphene

A GaN-based three-dimensional (3D) microstructure was grown on the graphene/SiO₂/Si substrate by metal-organic chemical vapour deposition (MOCVD, Thomas Swan). The nucleation layers were grown at 530 °C using trimethylgallium (TMGa) and ammonia (NH₃) for 5 min. The growth of a 10- μm -thick GaN-based crystal structure was performed at 1040 °C under a growth pressure of 665 mbar for 3 h. The grown GaN was etched by 0.5 mol KOH for 20 min for etch pit observations.

Characterization of graphene and GaN

The number of graphene layers was analysed using a UV-vis spectrometer and transmission electron microscope (TEM). We used the CRM 200 system (Witec, Germany) with ~ 20 mW of 532 nm laser for confocal Raman spectroscopy. The PL spectra were recorded in the temperature range of 10–300 K using a closed-cycle cryostat and a He–Cd laser of 325 nm excitation wavelength with a maximum input power of 15 mW. For the structural analysis, the obtained sample was measured by X-ray diffraction (XRD, Rigaku SmartLab, Japan) at a scan rate of 2 deg/min from 20 to 80 deg. To obtain cross-sectional TEM specimens of the GaN thin films a few micrometres thick, we used a conventional method.³³ The specimens were sliced into small pieces (5 mm \times 2 mm). A sandwich structure was prepared by bonding two slices with the multilayers facing each other, with the glue (G-1 epoxy) on the surface of the multilayers. This sandwich structure was cured in an oven at 130 °C for up to 4 h. One side was polished to a mirror-like finish and the other side was polished by a wedge-polishing technique with a tripod polisher. After final polishing, ion milling (FISCHIONE LAMP 1010) was conducted to obtain GaN thin films. TEM observation of the GaN thin films was done by JEM 2100F (JEOL).

LED fabrication with GaN crystals on graphene/sapphire

GaN-based LEDs were grown on the graphene/sapphire substrates by MOCVD. The LED structure consisted of a Si-doped n-type GaN layer, five periods of InGaN/GaN multi-quantum wells (MQWs), and a 150-nm-thick Mg-doped p-type GaN layer. After cooling down from the thermal cleaning step, a GaN nucleation layer approximately 25 nm thick was deposited at 530 °C on the graphene/sapphire substrate. A 2- μ m-thick undoped GaN epilayer was subsequently grown at 1040 °C. Thereafter, the temperature was gradually decreased to 750 °C in order to grow InGaN/GaN MQWs in N₂ ambient; the MQWs consisted of five periods of 3-nm-thick InGaN well layers and 12-nm-thick GaN barrier layers. Finally, the Mg-doped p-type GaN epilayer was grown in H₂ ambient at 950 °C (SI, Figure S4).

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Notes and references

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ToC

We report high-quality GaN crystals grown directly on graphene layers without a buffer layer by metal-organic chemical vapour deposition. This opens a new possibility that graphene with π electrons and hexagonal symmetry could be an ideal substrate for GaN crystal growth instead of expensive sapphire substrates.

