



Flexible InP–ZnO nanowire heterojunction light emitting diodes†

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Flexible, substrate-free nanowire (NW) devices are desirable to overcome the extremely challenging task of integrating III–V or III–N semiconductor devices such as LEDs and lasers on a range of optoelectronic circuits or biochips. In this work, we report the demonstration of core–shell p-InP/n-ZnO heterojunction NW array LEDs. The emission from the devices consists of three peaks at room temperature due to conduction band-to-heavy hole band transition, conduction band-to-light hole band transition and recombination at the substrate. At 78 K, an additional peak due to Zn acceptor levels is observed, whereas the peak due to the conduction band-to-light hole band transition quenches. Flexible LEDs are then fabricated by embedding the NW arrays in SU-8 to enable subsequent lift-off from the substrate. Compared with the original on-substrate LED device, broader, red-shifted and multiple peaks are observed from the flexible devices, which may be due to non-uniform strain related effects in the NWs caused by the SU-8 film. A slightly higher series resistance as compared to the on-substrate device and significant Joule heating suggest that good heatsinking is required for these flexible devices. Nevertheless, our study paves a promising way towards flexible and low power LEDs.

Introduction

Flexible LEDs are gaining rapid popularity due to a plethora of applications such as in flexible touch screens, flexible displays, sensors and wearables that make conformal contact with the human skin for accurate measurements. Organic LEDs (OLEDs) are the mostly developed devices that are currently being used

New concepts

In this work, we study light emitting diodes based on ZnO–InP nanowire core shell structures. To the best of our knowledge, this is the first report in which various electrical and optical properties of InP/ZnO nanowire array LEDs were studied. To integrate these LEDs on a wide range of optoelectronic circuits and biochips, flexible and substrate free nanowire devices are desirable. We use a very simplified process to lift off nanowires from the substrate. After lift-off, we fabricate devices and study temperature dependent electrical and optical properties on the flexible substrate. Previously, some reports have demonstrated flexible III–N nanowire visible LEDs. However, there are no reports on flexible III–V nanowire LEDs operating in the near-IR region. Our work provides a pathway for integrating low power LEDs on various substrates irrespective of their shape, lattice constants, chemical and mechanical properties and pave a way towards portable light sources.

as flexible displays.^{1–4} However, OLED technology suffers major drawbacks in terms of device lifetime, efficiency, brightness, and stability in humid conditions as compared to inorganic LEDs.^{5–7} Significant research has been done to demonstrate planar inorganic LEDs with III–N or III–V semiconductor compounds.^{8–11} Achieving a higher extraction efficiency with these planar devices is always challenging and this leads to the development of nanowire (NW) LEDs which can overcome this issue owing to their one-dimensional geometry with a large surface area, leading to more efficient light emission and effective current spreading.^{12–15} Furthermore, in order to fabricate high optical quality and defect-free III–V NWs, there is a stringent requirement to use lattice matched crystalline substrates. These substrates are expensive due to rarity of III–V materials and are rigid and fragile, while they are predominantly used as growth templates and do not contribute to the operation of the NW device other than for contact purposes. Preparing flexible LEDs allows the reuse of the substrates, as well as easy integration with other devices as they can be bent into the desired shape and are lightweight and less brittle.

Mechanical peeling of NWs embedded with polydimethylsiloxane (PDMS) has been investigated to prepare substrate-free LEDs.^{16,17} However, this technique has been demonstrated on only very long NWs as a thin PDMS layer is very difficult to

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handle.¹⁷ Moreover, the dry etching rate of PDMS is very slow and requires gases like CF_4 and O_2 , which can damage the NWs during the etching process.¹⁸ On the other hand, the SU-8 photoresist has excellent thermal properties and is easy to process and etch with precise control of its thickness. A thin SU-8 film can be easily handled, making it a good alternative to PDMS. However, SU-8 adheres very well to the substrate and is hard to be mechanically peeled off like PDMS. To overcome this problem, in this paper, we report a simple solution where an SiO_2 film is used as a sacrificial layer for detaching NWs embedded in SU-8 from the substrate.

Various types of optoelectronic devices such as LEDs,¹⁹ solar cells,²⁰ lasers,²¹ transistors²² and photodetectors²³ have been demonstrated based on InP NWs due to their direct bandgap, high carrier mobility and low surface recombination rate.²⁴ In particular, InP NW LEDs have been reported based on axial homojunction^{19,25,26} and both axial and radial quantum well devices^{15,27–31} grown by bottom-up techniques. In comparison, radial structures have an advantage over axial structures due to a larger recombination region owing to a higher surface area-to-volume ratio of NWs. However, radial quantum well structures require a lot of optimisation due to the facet evolution process as QWs prefer a zinc blende (ZB) structure, whereas InP NWs have a wurtzite (WZ) structure.^{29,31,32} Furthermore, in order to develop an efficient LED, higher doping concentrations are required to obtain a good ohmic contact to reduce the resistance of the device.³³ However, these dopants increase optical losses significantly due to free carrier absorption³⁴ and Auger recombination.³⁵ Some of these issues could be addressed by incorporating a radial junction made up of transparent conducting oxide (TCO) instead of a heavily doped homojunction. Several binary n-type TCO compounds such as ZnO, SnO_2 , In_2O_3 , CdO, impurity-doped SnO_2 ($\text{SnO}_2\text{:Sb}$ and $\text{SnO}_2\text{:F}$), impurity-doped In_2O_3 ($\text{In}_2\text{O}_3\text{:Sn}$, or ITO) and impurity-doped ZnO (ZnO:Al and ZnO:Ga) have been investigated for practical use,³⁶ demonstrating that, owing to their wide bandgap and thus transparency to near infrared light, they are highly suitable to be used as n-type contact layers for semiconductors such as InP.

To study the LED properties from p-InP NWs, we use ZnO as an n-type layer. The origin of n-type conductivity in ZnO is attributed to oxygen vacancies, zinc interstitials and unintentional hydrogen doping.^{37,38} Hydrogen which is also considered as a dominant donor comes from atomic layer deposition (ALD) precursors such as diethyl zinc and H_2O , as well as water vapor present inside the chamber. Another advantage is that the thin ZnO layer acts as a passivation layer for InP nanowires.³⁹ In this work, we demonstrate flexible InP NW heterojunction LEDs conformably coated with a ZnO layer and compare the performance of these flexible NW LEDs to that of those fabricated on InP substrates. These flexible LEDs will open up a vast range of applications in sensors, detectors, night vision illumination and various medical applications.

Experimental

Fabrication of our on-substrate LED device can be divided into two sections: selective area epitaxy (SAE) of NWs by

metal–organic chemical vapour deposition (MOCVD) and device fabrication. Fig. 1(a) shows the p-InP NWs grown by the SAE-MOCVD technique.²¹ To grow the NWs, a 150 nm-thick SiO_2 mask was deposited by plasma assisted chemical vapour deposition on $\text{p}^+\text{InP}(111)\text{A}$ wafers. The mask was patterned by electron beam lithography in a Raith 150 system and dry-etched using an Oxford Plasmalab 80 Plus reactive ion etching system to open holes of 300 nm size with a pitch of 1 μm . This mask acted as a template for SAE growth and was designed to obtain NW arrays with dimensions of 100 $\mu\text{m} \times 100 \mu\text{m}$. After this, SAE-MOCVD was carried out in an AIXTRON 200/4 system at a base pressure of 100 mbar with H_2 as the carrier gas at a total flow rate of 14.5 l min^{-1} . Trimethylindium and phosphine were used as precursors with molar flow rates of 6.07×10^{-6} and $4.91 \times 10^{-4} \text{ mol min}^{-1}$, respectively, for 15 min at 730 $^\circ\text{C}$. For p doping, diethylzinc with a molar flow rate of $8.63 \times 10^{-6} \text{ mol min}^{-1}$ was used. Scanning electron microscopy (SEM) was carried out using an FEI Verios 460 system to image the NW arrays after growth and during the LED fabrication process, as shown in Fig. 1. Based on the SEM measurement, the average length of NWs is 3 μm with an average diameter of 400 nm.

Fig. 1(d) shows a schematic of the fabrication process. After growth, the entire NW array was planarised using SU-8, a transparent electrically insulating polymer by spin coating at 3000 rpm and baking at 95 $^\circ\text{C}$ for 2 min, which resulted in a thickness of 5 μm . To expose the top of the wires, inductively coupled plasma reactive ion etching (ICP-RIE) was carried out. $\text{SF}_6/\text{O}_2/\text{Ar}$ gas chemistry was used with a flow rate of 2/10/20 sccm and a pressure of 1.33 hPa with ICP and bias powers of 300 and 20 W, respectively. Fig. 1(b) shows the SEM image after etching. The top of the NWs was exposed to a depth of $\sim 2 \mu\text{m}$. A 30 nm-thick conformal n-ZnO layer was then deposited using ALD at 150 $^\circ\text{C}$. Finally, 70 nm ITO and 10 nm Au layers were deposited using sputter deposition to decrease the contact resistance (Fig. 1(c)), followed by 150 nm of Au pad deposition using an electron beam evaporator adjacent to the NW array using a shadow mask. To make the bottom contact, the InGa alloy was applied on the back of the p-type wafer.

To prepare flexible LEDs, after planarisation, UV exposure for 1 min and hard baking at 150 $^\circ\text{C}$ for 5 min were subsequently performed to enhance the cross-linking of SU-8 to make it mechanically, chemically and thermally strong. After this, photolithography using the MaN-1420 resist was performed to expose only the top of the NWs for subsequent ICP etching under the same conditions as mentioned above. Then, the sample was dipped into 48% hydrofluoric acid for 5 min to etch off the SiO_2 mask layer and lift off the NWs embedded inside the SU-8 film. The sample was flipped and a 150 nm Au layer was deposited as the back contact using an electron beam evaporator. This thin film was then transferred onto a transparent acetate film pre-deposited with Au pads. To bond the back contact of the NW array with the Au pads on the transparent film, silver paste was applied. Then, an n-ZnO film with a thickness of $\sim 30 \text{ nm}$ was deposited using atomic layer deposition to form the radial heterojunction, followed by 70 nm ITO and 10 nm Au deposition for the top contact.



Fig. 1 SEM images of the InP NW array after growth and during the LED fabrication process at a 15° tilted view. (a) As-grown NW array. (b) After SU-8 etching with ICP-RIE. (c) After ZnO, ITO and Au deposition. All scale bars are 2 μm. (d) Schematic diagram of the LED fabrication process of the (left) on-substrate and (right) flexible device.

All the edges of the polymer film were covered with Kapton tape to avoid shorting during ZnO deposition. Finally, a 150 nm top Au pad was made near the NW array using a shadow mask (Fig. S1, ESI†).

To characterise the electrical and optical properties, both electroluminescence (EL) and photoluminescence (PL) measurements were performed using a home-built μ-PL system using a CCD (Princeton Instruments, PIXIS) and a spectrometer (Acton, SpectraPro 2750). *I*-*V* measurements were carried out using a Keysight B2902A source/measurement unit.

To understand the junction properties, electron beam induced current (EBIC) measurements were performed on the on-substrate device using an FEI Helios 600 Nanolab FIB system with a beam current of 0.17 nA and an accelerating voltage of 2 kV. For this purpose, after SAE growth, a 120 nm ZnO layer was deposited using similar conditions on NWs with a diameter of ~500 nm. ICP-RIE etching was performed to expose the top of the NWs so that the cross-section could be clearly seen under the microscope. Finally, a thin layer of SU-8 was spin-coated to avoid shorting and ICP etching was performed to expose the tips of the embedded NWs. To measure the signals from the NWs, the substrate and thus the NW core

were electrically contacted with the FIB stage and the ZnO shell was contacted using a nano-manipulator inside the FIB chamber.

Results and discussion

First, we measured the *I*-*V* characteristics of heterojunction ZnO-InP NW LEDs fabricated on the InP substrate. Fig. 2(a) shows a rectifying behaviour typical of an LED with a turn-on voltage of around 4 V and a series resistance of around 88.9 Ω (Fig. S2(a), ESI†). To have a deeper understanding of the *I*-*V* characteristics, the $\ln(I)$ vs. *V* plot was constructed, the slope of which gives an ideality factor (*n*) of 12.2 and the y intercept gives a reverse saturation current of 24.6 nA (Supplementary 1 and Fig. S2(b), ESI†). Here, an ideality factor larger than 2 is consistent with previously reported InP NW LEDs and implies that the current is dominated by tunnelling.^{19,26} Another reason for the high ideality factor may be the formation of a native oxide layer at the InP/ZnO interface.³⁹

The room temperature EL spectrum measured at 5 mA is shown in Fig. 2(b), in comparison with their PL spectrum. Both

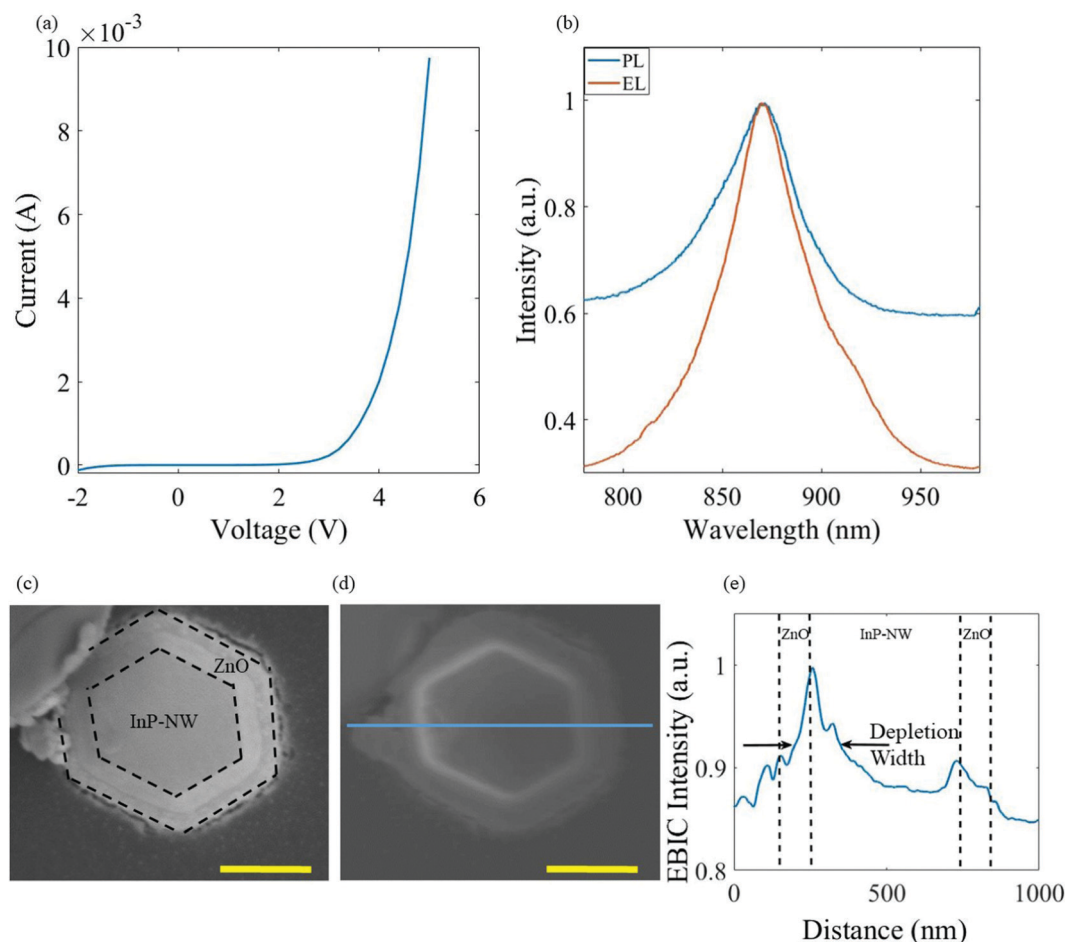


Fig. 2 (a) I - V characteristics of the on-substrate device at room temperature. (b) PL spectrum at a 17 μ W excitation power and EL spectrum at a 5 mA current. (c) SEM image of a single NW from the fabricated LED array. The image on the top left corner is the contact probe in good contact with the ZnO shell. (d) EBIC signal acquired under an accelerating voltage of 10 kV corresponding to the SEM image in (c). (e) EBIC scan profile across the NW, indicated by the blue solid line. Scale bars in (c) and (d) are 300 nm.

spectra display a same peak at 870 nm confirming that Joule heating is kept to a minimum in our device even at continuous current injection. In the PL spectrum, the appearance of only one peak at 870 nm confirms that NWs have a pure WZ structure. It has previously been reported that, for the above-mentioned growth conditions, the NWs have a pure WZ phase irrespective of Zn doping concentration.⁴⁰ However, in the EL spectrum, a shoulder at a higher wavelength is observed which will be discussed later.

The property of the p-InP/n-ZnO heterojunction was investigated using the EBIC measurement of an individual NW device. Fig. 2(c) shows the cross-sectional SEM image of the NW, with a clearly visible contrast of the InP NW core and ZnO shell. Fig. 2(d) displays the corresponding EBIC signal measured under an accelerating voltage of 2 kV and a 0.17 nA current. From Fig. 2(d), a bright ring can be observed at the NW-ZnO interface, which can be attributed to the depletion region where the highest EBIC current is produced under 0 V bias. The EBIC intensity profile is plotted across the diameter of the NW as shown in Fig. 2(e). The depletion width is approximately 138 nm when measured at $1/e$ of the maximum intensity

value. Using the depletion width we estimated the p-doping level in the NW to be $6.75 \times 10^{16} \text{ cm}^{-3}$ which is consistent with previously reported values (Supplementary 3, ESI†).⁴⁰ To calculate the carrier concentration of ZnO (N_D), a planar 30 nm film of n-ZnO deposited under the same conditions on a glass substrate was calculated to be $2 \times 10^{19} \text{ cm}^{-3}$ using a combination of Drude and Cody-Lorentz models by ellipsometry and is consistent with the reported values in the literature.⁴¹ It is noticed that the EBIC profile is asymmetric, extending more into p-doped NWs. This confirms that the depletion region is primarily formed in the NW where the majority of recombination occurs in this region. The possible reason for the non-symmetric profile at both the junctions could be a small angle tilt or non-uniform ICP etching of the ZnO shell which is evident from the SEM image (Fig. 2(c)).

Fig. 3(a) shows the EL spectra from the NW array LEDs on the substrate with increasing forward current injection level at room temperature. The EL signal is detectable at an injection current of 1 mA, showing a peak at 870 nm with a shoulder at around 920 nm. To investigate the origin of different peaks, Lorentz curve fitting was performed (Fig. 3(e)). It is found that

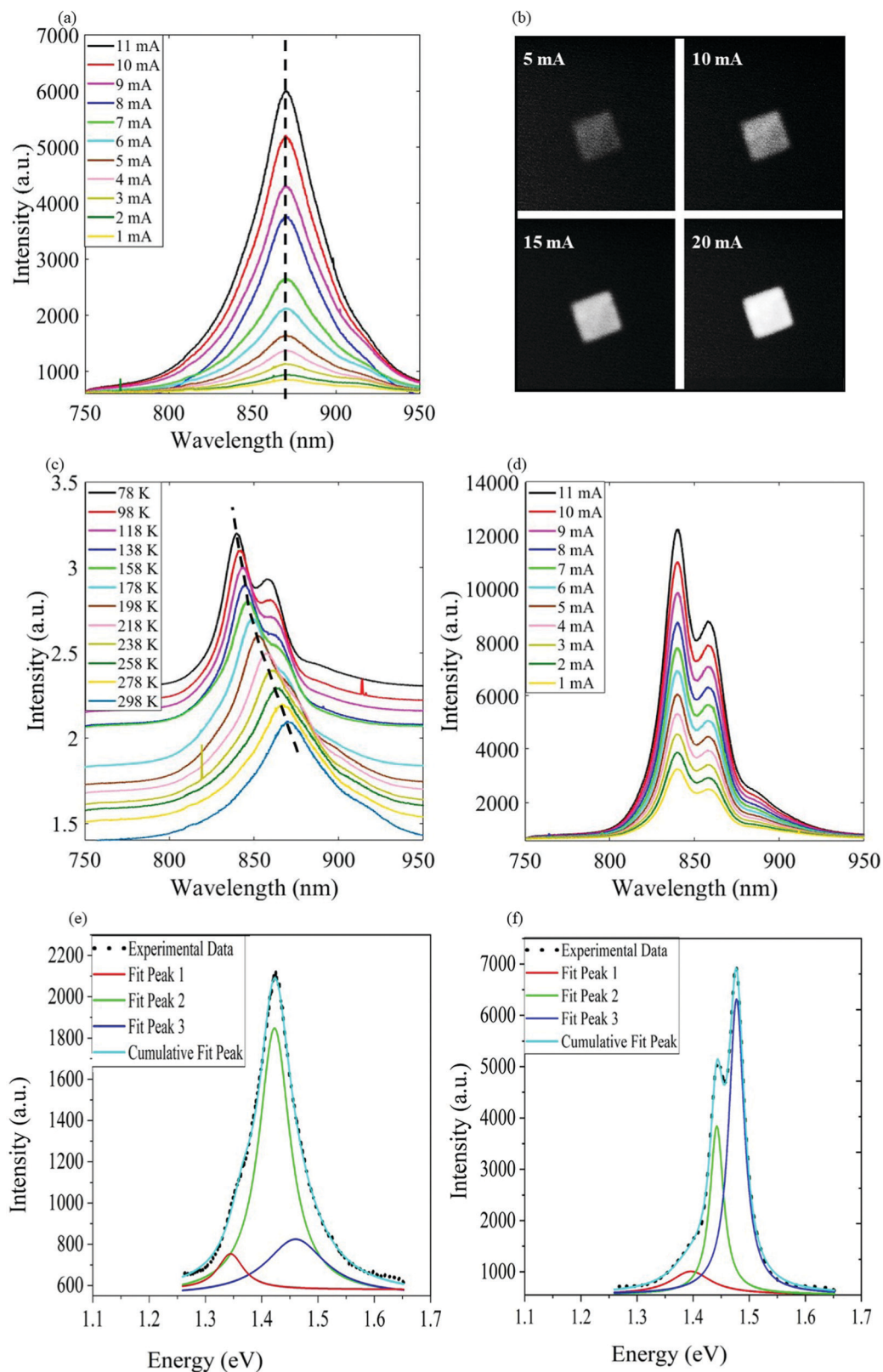


Fig. 3 On-substrate NW array LED characteristics of the device. (a) Room temperature EL spectrum at different current injection levels. The black dashed line indicates the peak of the spectrum at 870 nm. (b) Optical images of the emission at room temperature from the $100 \times 100 \mu\text{m}^2$ array at different current injection levels. (c) Temperature dependent EL spectrum of the device at a 5 mA current where the black dashed line indicates the peak position. (d) EL spectrum at 78 K at different current injection levels. Lorentz curve fitting of the EL spectrum measured at 5 mA at (e) room temperature and (f) 78 K.

there are three peaks with a major contribution from the peak at 1.42 eV which corresponds to the fundamental bandgap of WZ InP NWs.^{42,43} The peak at a higher energy of 1.46 eV is due to the light hole and heavy hole splitting in WZ structures.⁴⁴ A. Zilli *et al.* reported that this peak becomes dominant at temperatures above 150 K due to thermal population of higher energy states.⁴² The energy difference between these two peaks has been reported to be around 40 meV, consistent with our value of 37.8 meV.^{42,44,45} We attributed the peak at the lowest energy of 1.34 eV to the recombination occurring at the ZB InP substrate.⁴³ The existence of this peak confirms that some carriers leak into the substrate from NWs. With the increase of injection current, the EL intensity increases with the peak position slightly red-shifted to longer wavelengths due to Joule heating. Fig. 3(b) shows the optical microscopy image of the NW LED array captured using an infrared camera with a 10× lens. It is clearly evident that light is emitted from the NWs, and with the increase of current injection, the emission intensity becomes more uniform across the entire array.

To investigate the temperature dependent characteristics of the device, EL was conducted at an injection current of 5 mA from room temperature to 78 K and the spectrum is shown in Fig. 3(c). The linewidth decreases with decreasing temperature and the spectrum is blue-shifted as a result of increased bandgap with the decrease of temperature.⁴² Interestingly, at ~238 K, we observed another peak in the EL spectrum, which becomes more prominent at lower temperatures. Fig. 3(d) indicates the LED behaviour at 78 K as a function of increasing injection current. To understand the origin of this peak, Lorentz fitting was performed at 78 K (Fig. 3(f)) and 3 peaks were observed. The highest energy peak at 1.48 eV corresponds to the bandgap of NWs which is also known as the A exciton peak in the WZ semiconductor. We attribute the lower energy peak at 1.44 eV to radiative recombination from the conduction band to the Zn acceptor in the NWs. We calculated the activation energy of the Zn acceptor level for a doping concentration of $6.75 \times 10^{16} \text{ cm}^{-3}$ obtained from EBIC measurements using the formula given by K. Hansen *et al.*⁴⁶

$$E_A = E_0 - a(N_A^-)^{1/3} \quad (1)$$

where E_0 is the ionisation energy (51 meV) and $a = 3.9 \times 10^{-5} \text{ meV cm}$. This gives an activation energy of 35.1 meV which is the same as the energy difference between these peaks confirming that the lower energy peak is a result of Zn acceptor levels. Previously, a defect related peak in pure wurtzite InP NWs has been observed by various groups.^{42,47–49} The lowest energy peak at 1.40 eV is due to recombination at the substrate. The difference between this peak and the highest energy peak is around 80 meV which is consistent with the energy difference of WZ and ZB InP NWs.⁴³

The comparison between the I - V characteristics of the flexible device and on-substrate device in Fig. 4(a) shows that the turn-on voltages for both devices are similar at ~3.5 V using an extrapolation method described in the Section S5 (ESI†). However, there is a 'softer' turn-on voltage for the flexible device. The flexible LED is also slightly more resistive

compared to the on-substrate one, which might be due to the poorer contact formed on the flexible device using silver adhesive paste.

To compare with the on-substrate NW LEDs, the PL characteristics of flexible LEDs were investigated. As shown in Fig. 4(b), multiple, red-shifted and broader PL peaks were observed from the flexible device, in comparison to the on-substrate device. Furthermore, the peak of the PL spectrum varies across the array, which is not observed for the on-substrate device. A possible explanation for these multiple peaks could be the effect of non-uniform strain on the NW array imposed by the SU-8 film or the acetate film.^{50–52} We mapped the surface profile of the peeled SU-8 film after integration on the acetate sheet using an optical profilometer (Fig. S3, ESI†) and observed that one radius of curvature varies across the NW array. However, due to measurement setup limitations, we could not quantify the strain induced in the NW array. Moreover, the intensity of the emission decreases significantly in comparison to the on-substrate device due to the wavy nature of the flexible device which leads to less light entrance into the detector.

Fig. 4(c) shows that, as the injection current increases, there is a significant red shift in the EL spectrum due to Joule heating. In comparison, the on-substrate device does not show any significant shift in the emission wavelength (Fig. 3(a)). This effect can be understood in terms of the InP substrate providing better heat dissipation in comparison to the transparent acetate film used for supporting the flexible device. We also fit the EL spectrum obtained at 1 mA and found that two additional peaks at longer wavelengths (peak 1 and peak 2) in addition to the fundamental bandgap peak (peak 3) and transition from the conduction band to the light hole band (peak 4) (Fig. 4(e)). The recombination from the conduction band to the heavy hole band and the light hole band is observed at 1.41 eV and 1.45 eV, respectively. Red shift from the actual position is due to heating; however, the energy difference remains around 40 meV. The possible reason for the two additional red-shifted peaks might be strain related. Since the radius of curvature varies across the NW array, it may induce non-uniform strain at different positions, similar to the report by G. Signorello *et al.*⁵¹

The temperature dependent behaviour of these peaks was further studied by measuring the EL spectra at an injection current of 2 mA from room temperature to 78 K. As shown in Fig. 4(d), the emission spectrum is blue-shifted with decreasing temperature due to the temperature dependence of bandgap. Moreover, slightly red-shifted, broader and multiple peaks are observed (Fig. 4(f)) as compared to the on-substrate device which is possibly due to the combined effect of heating and strain induced by the SU-8 and/or acetate film on the NWs due to thermal coefficient mismatch.^{50–52} Moreover, a small peak at a higher energy with a difference of ~40 meV indicates the splitting of heavy hole and light hole bands confirming that the temperature is higher than 150 K as this peak becomes dominant at temperature above 150 K due to thermal population of higher energy states.⁴²

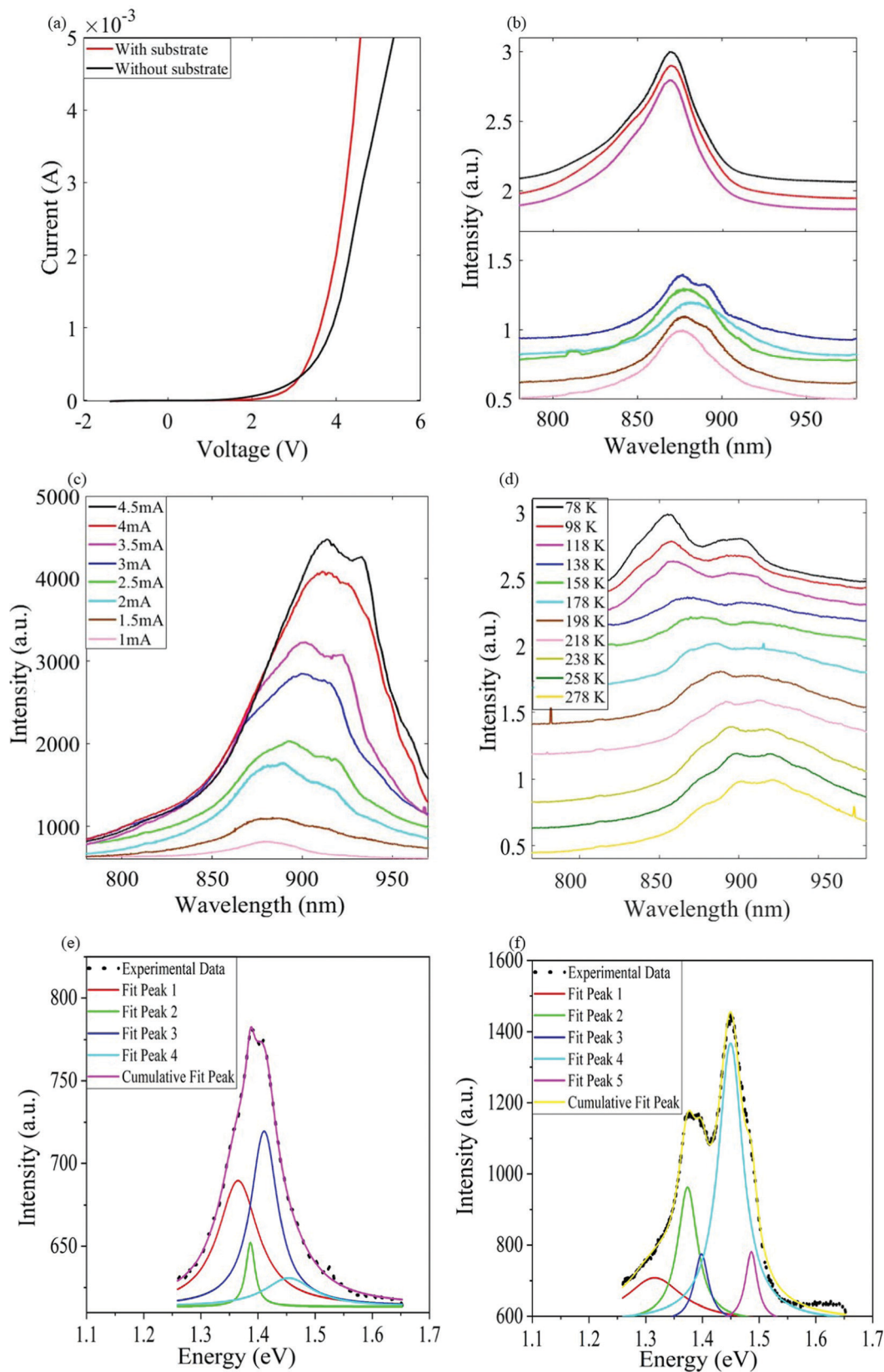


Fig. 4 (a) I - V characteristics of the on-substrate and flexible devices. (b) Room temperature PL spectrum at different positions on the array for on-substrate (top) and flexible devices (bottom). (c) EL spectrum of the flexible device at room temperature at different current injection levels. (d) Temperature dependent EL spectrum at a 1 mA current from the flexible LED. Lorentz curve fitting of the EL spectrum measured at 1 mA at (e) room temperature and (f) 78 K.

Conclusions

In summary, we have demonstrated core-shell InP/ZnO heterojunction NW LED arrays and successfully fabricated flexible LEDs by embedding the NWs in a thin SU-8 film, which is then peeled off from the InP substrate. We have also studied the temperature dependent behaviour of these LEDs and confirmed that the EL spectrum consists of 3 peaks at both room temperature and 78 K. The peaks related to conduction band-to-heavy hole band transition and recombination at the substrate are present across the temperature range, whereas the peak related to the transition from the conduction band to the light hole band quenches at a low temperature and the Zn acceptor level peak became prominent at around 238 K. Our work provides a pathway for integrating LEDs on various substrates irrespective of their shapes, lattice constants, and chemical and mechanical properties. Future work will be carried out to further optimise the fabrication process and improve heat sinking (to minimise Joule heating) to enhance the performance of the flexible device for a plethora of applications as low power and portable light sources.

Conflicts of interest

The authors declare no conflicts of interest.

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